# Aaditya Doshi

AMS Validation Engineer | Noida (U.P)

✉ aaditya.doshi09@gmail.com | ✆ +91-7406567930

## 🏫 EDUCATION

**RGPV UNIVERSITY Bhopal**
B. Tech in ECE | MIT Mandsaur | June 2013

## 🔑 KEY SKILLS

-Analog circuit validation
-Test plan design and execution
-Test methodology development
- Test Automation development
-Industry-standard tools and equipment
-Problem-solving and troubleshooting
-Collaboration and teamwork
-Time management and deadline adherence

## 🔲 CORE SKILLS

SDADC, SARADC, R2RDAC, CMP,
CLOCK, OSCILLATORS, PLL
SE -DE BUFFERS, IOPADS
ANLOG FILTER, ADCBIST
LDO, DCDC, POWER SEQUENCING
UART, I2C, SPI

## 🕸 SOFTWARE SKILLS

Programming Languages-
Embedded C, NI- LabVIEW,
TestStand, Python, Python Libraries,
MATLAB, Assembly
IDE Tool
IAR Workbench, TI-CCS, Arduino,
ARC Metaware, GNU-GCC.
DATA Analysis Tool
MS-Excel, JMP, TIBCO Spotfire.

## SUMMARY

Results-oriented Analog Validation Engineer with **9+** years of experience in Validation field. Proven track record in conducting comprehensive validation tests to ensure the functionality, performance, and reliability of analog designs. Skilled in designing test plans, developing test methodologies, and executing test cases. Strong understanding of analog circuitry and proficient in using industry-standard tools and equipment. Collaborative team player with excellent problem-solving abilities and meticulous attention to detail. Committed to delivering high-quality results within project deadlines. Proficient in development of optimized automation for enhance test capabilities and test time reduction. Seeking opportunities to leverage expertise in analog validation to drive product excellence and innovation.

## EXPERIENCE

### NXP INDIA PVT. LTD.

Analog Validation Engineer | Staff Level
Nov. 2019 – Present | Noida (U.P)

❖ Responsible for characterizing various Analog IPs in SOC Such as ADCs , DAC, Temperature sensor, PGA, Buffers, Bandgap, IO pads and High Speed LVDS Pads across PVT.
❖ Leading the 12+ member Data converter and Sensor Domain groups activity.
❖ Developed various tools and methodology to improve t
❖ Handling  various analog debugs and provide the solution to the design team.
❖ Provide solutions for the ADCs critical debugs and save the cost of revision tap outs.
❖ Provide solution to various ATE setup issues by bench setup correlation.
❖ Developed ADC utility kit for ADC parametric simulation, design and other validation team is using this solution.
❖ Developed the Instrument front end control software  helps lot to team in lockdown situation.

## INSTRUMENT

Power Supplies , AWG , Mixed Signal Oscilloscope, Audio Precision, Multimeters , SMU , Temperature Forcing Unit(Thermostream , MaxTC) , Pattern Generator , Audio Analyzer and Switches , NI Pxie and NI-cards.

## LINKS

LinkedIn
Linkedin.com/in/aaditya-doshi-ECEMIT2013

GITHUB
github.com:AadityaMDS/AadityaDoshi.git

## INFORMATION

Address
C-904 JM Aroma, Sector 75 Noida(U.P)

DOB 30-09-1991|Language English, Hindi

## SANKALP SEMICONDUCTOR  PVT. LTD.

Analog Validation Engineer | Senior Level
**Client – Analog Devices India Pvt. Ltd.**
May 2017 – Oct. 2019 | Bengaluru (Karnataka)

- ❖ Worked in various team at Analog Devices (BMS Validation Team , ADAS 77GHz Radar Validation Team , Ethernet Validation Team).
- ❖ Handled the validation of Analog IPs such as ADCs , Buffers , MIPI and LVDS pads , Temperature sensor , Power Sequencing across PVT.
- ❖ Involved in various debug activity and worked closely with design team.
- ❖ Developed the  validation bench from scratch and wrote all the test cases and achieved all result within the defined timelines.
- ❖ Worked closely with the design team and tester team for zero customer issue.

## TESSOLVE  SEMICONDUCTOR  PVT. LTD.

Analog Validation Engineer | Entry Level
**Client – Texas Instrument India Pvt. Ltd.**
Nov.2013 – April 2017 | Bengaluru (Karnataka)

- ❖ Worked in Low power MSP432 SOC Bench validation team.
- ❖ Responsible for Bench characterizing the Clocks , Internal and External oscillators , Power consumption , SARADC, LDO and DCDC.
- ❖ Involved in Brings up activity of SOC and Board.
- ❖ Responsible for development of automation for all assigned IPs and handled various setup debugs.
- ❖ Worked closely with design team and ATE teams.
- ❖ Developed all test plans and PVT reports of all assigned modules.
- ❖ Developed all Low-speed protocols drivers UART, SPI and I2C to communicate all board peripherals.