ETCH sAo SKETCH — OLED VERSION

SAO PORT

X1 — SAO_conn_SFH11-NBPC-D03-ST-BK

VIN

SDA
GPIO1

GPIO2
SCL

GND

DISPLAY

R30 0805 10K
R31 0805 10K

SDA
SCL

JP1
JP2

X3
OLED_128x128

SDA
SCL

GND

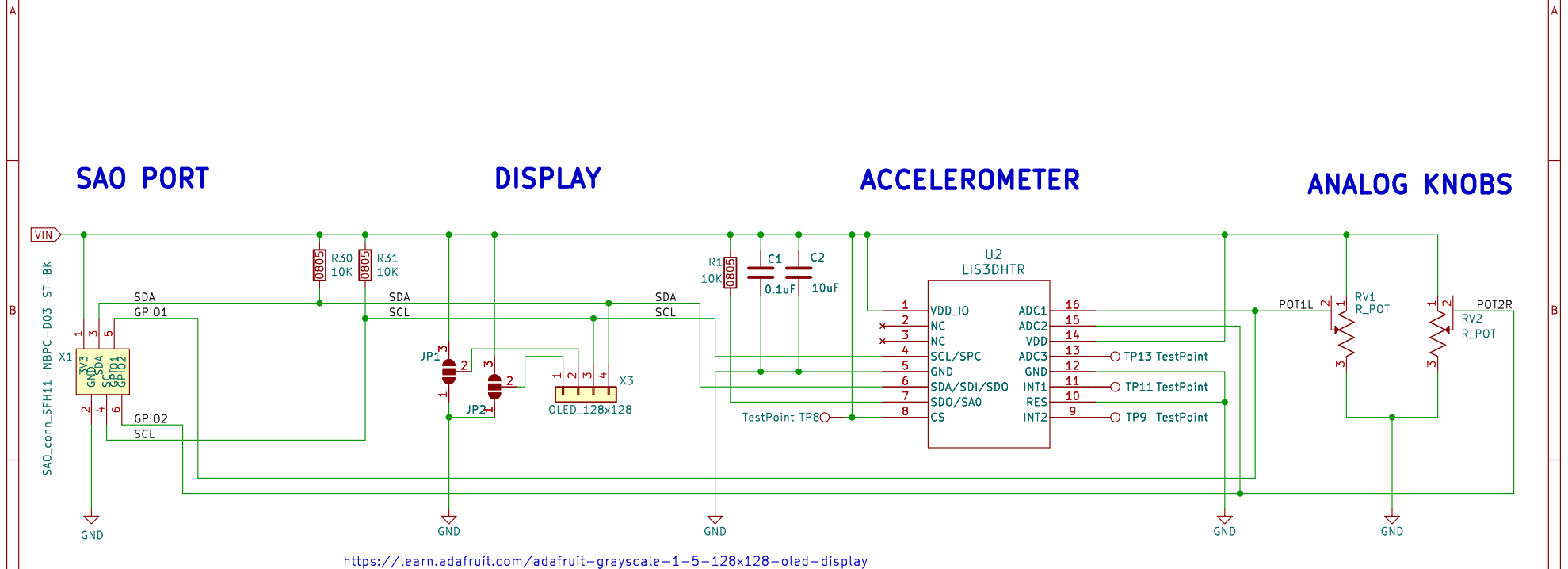https://learn.adafruit.com/adafruit-grayscale-1-5-128x128-oled-display

https://hackaday.io/project/175182-simple-add-ons-sao
using Sullins SFH11-NBPC-D03-ST-BK female header
https://www.digikey.com/product-detail/en/sullins-connector-solutions/
SFH11-NBPC-D03-ST-BK/S9717-ND/4558818

ACCELEROMETER

R1 0805 10K
C1 0.1uF
C2 10uF

U2
LIS3DHTR

Pin	Name		Name	Pin
1	VDD_IO		ADC1	16
2	NC		ADC2	15
3	NC		VDD	14
4	SCL/SPC		ADC3	13
5	GND		GND	12
6	SDA/SDI/SDO		INT1	11
7	SDO/SA0		RES	10
8	CS		INT2	9

TP13 TestPoint
TP11 TestPoint
TP9 TestPoint

TestPoint TP8

GND

https://learn.sparkfun.com/tutorials/lis3dh-hookup-guide

ANALOG KNOBS

POT1L
RV1 R_POT

RV2 R_POT
POT2R

GND   GND

I2C ADDRESS TABLE

All 7-bit addresses should be greater
than 0x07 and less than 0x78 (120).

LIS3DHTR 3-AXIS Accel	0x19 with SDO/SA0 high (default)
LIS3DHTR 3-AXIS Accel	0x18 with SDO/SA0 low (remove R1 and jumper pin 7 to ground)
OLED 128x128 1.5 inch SSD1327	0x3C (dec 60)

All non-polarized capacitors are X7R or X5R ceramic unless otherwise noted.

Visit www.Core64.io for information on assembly and optional features.

Concept and design by Andy Geppert @ www.MachineIdeas.com

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