

Project Title

1st Given Name Surname
dept. name of organization (of Aff.)
IIIT Hyderabad
Hyderabad, India
email address

2nd Given Name Surname
dept. name of organization (of Aff.)
IIIT Hyderabad
Hyderabad, India
email address

Abstract—foo.

Index Terms—component, formatting, style, styling, insert

I. INTRODUCTION

Explain the application with the use case. You can illustrate the use with a diagram here.

II. MOTIVATION

In the current system architecture, which you explained in previous section, what are the bottlenecks, in the software approach using CPU or GPU? Make a case for acceleration using FPGAs through profiling or some other argument.

Identify the performance metrics and try to make your arguments tight using numbers as much as possible.

III. APPROACH

What is the architecture of the proposed system? What goes on CPU and what goes on FPGA? What is the overall data flow? Specifically, how do we move data to FPGA and back? How do we store information on FPGA, like use BRAMs or DRAM? Draw the architectural diagrams.

IV. PROJECT PLAN

How do you plan to approach the whole project? Divide the project into submodules with timelines and who does what.

V. RELATED WORK

So and so [1] did so and so work.

Cite all the relevant papers in this section and write a line or two about them here.

VI. EXPERIMENTAL RESULTS

What is your experimental analysis approach? What is the experimental setup? What are the benchmark programs you are considering? What are the performance metrics? What graphs and tables you would like to present and analyze?

VII. CONCLUSIONS

That's how I changed the world :-)

REFERENCES

- [1] S. M. Trimberger, "Three ages of fpgas: A retrospective on the first thirty years of fpga technology," *Proceedings of the IEEE*, vol. 103, no. 3, pp. 318–331, March 2015.