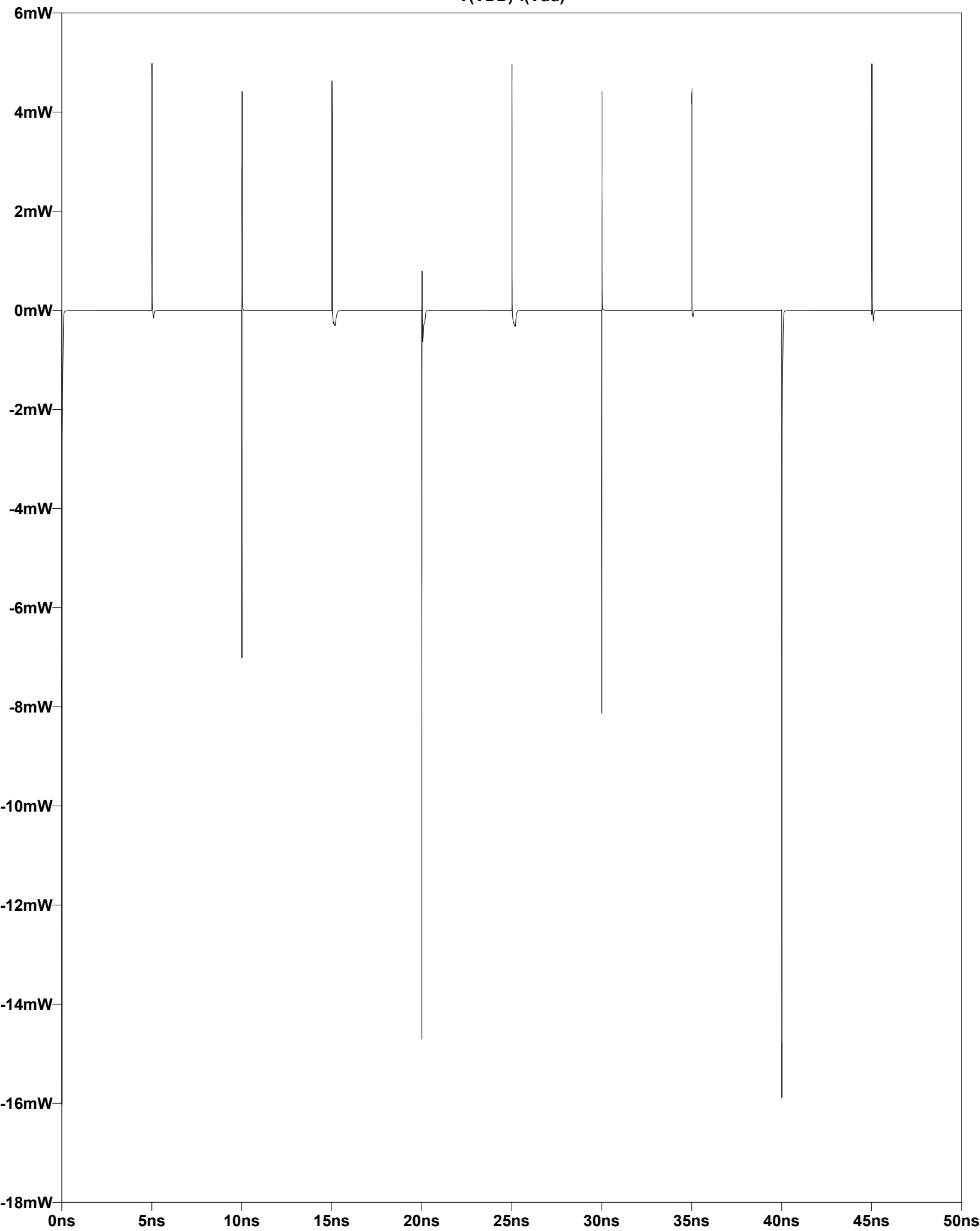


$V(VDD) \cdot I(Vdd)$



--- E:\CMOS Full Adder.raw ---