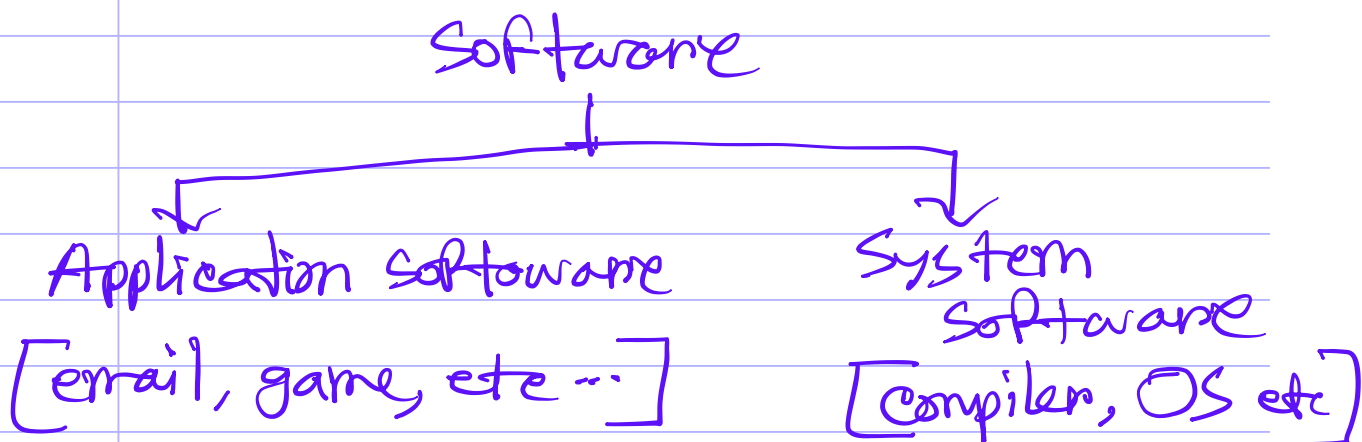
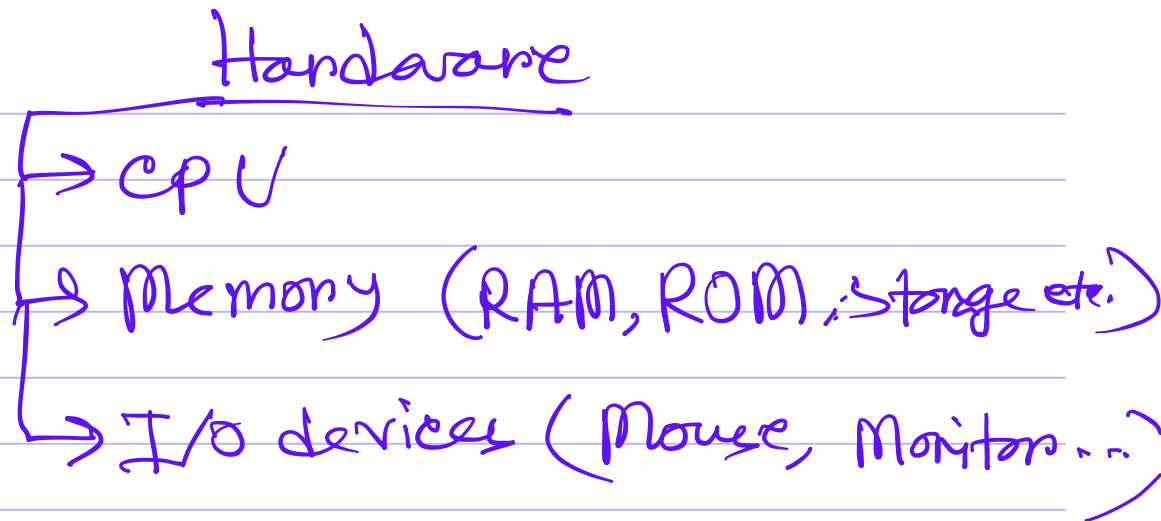


MIPS: soft Assembly Language.





⊛ Compiler: High level Language to
(mid level) Assembly language & convert to
compiler.

⊛ Assembler: Mid level to low level
(Binary) to convert to Assembler.

Primary Memory:

⊛ RAM → DRAM (Dynamic) - DDR4 RAM
→ SRAM (Static) - Cache Memory

Secondary Memory:

→ Rom

→ Hard Disk etc