

INDEX

Report file:

1. Problem Statement
2. Top Level Block Diagram
3. Inputs and Outputs
4. Assumptions
5. Key Bindings
6. Design Approach and Methodology
7. State Diagram and State Table
8. Logisim Implementation
9. Pin_Out Diagrams
10. Sample Inputs and Outputs
11. Additional Functionalities
12. Bill of Materials
13. Appendix

Logisim Circuit - Scrolling Display.circ

1. Main Circuit
2. Zero Counter
3. Counter
4. ROM Address Locator
5. Shift Register

Simulation_Logging_Table.txt

Sample Output

1. Scroll
2. Pause
3. Repeat
4. Reset