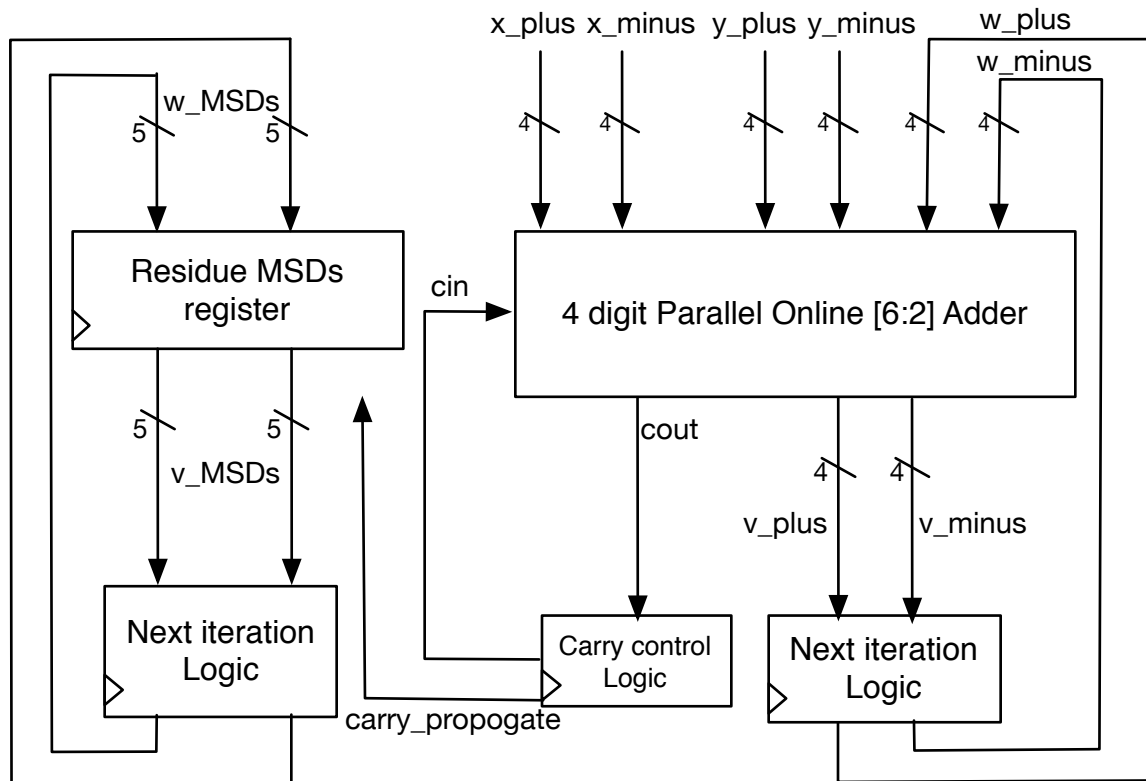


(a) Conventional Structure



*both w_{MSDs} and v_{MSDs} contain a 5-bit plus part and a 5-bit minus part

*Next iteration Logic follows Line 5 in Algorithm 1

(b) Optimized Structure