

p6 §3

IML

Dsp Com I

2001

- a) packet - variable sized frames contain addressing information  
- sent sporadically,  
circuit - fixed size info sent periodically  
- no addressing info, address is implied by position in time.
- b) circuit - fixed delay, fixed capacity
- c) efficient use, not consuming resource unless need to, can ~~use~~ statistically multiplex.
- d) FDM  $\rightarrow$  circuit (had to do packet)  
TDM  $\rightarrow$  either can't provide periodic or aperiodic structure
- CDM  $\rightarrow$  ~~either because~~ sporadic use of a code ~~name~~ is possible (but no guarantee that channel won't be overused  $\rightarrow$  too much interference)  
so circuits yes  
packets so-so.