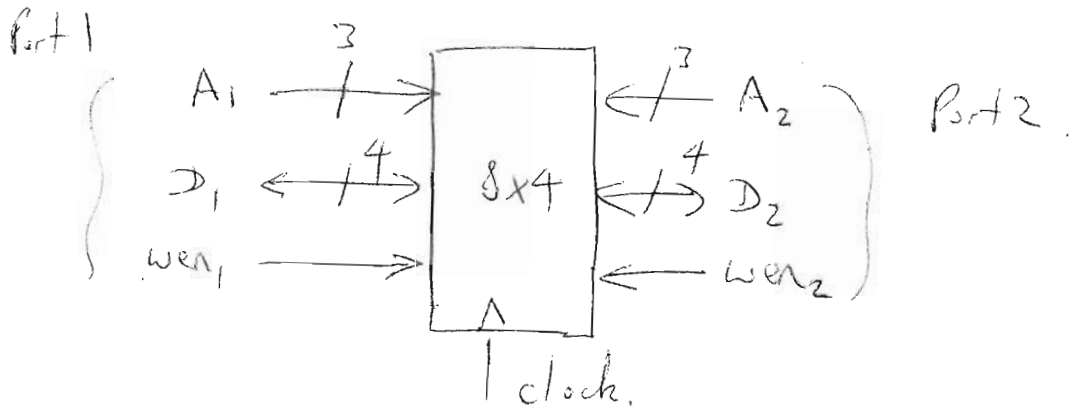


SHD 2003

Four MarksDual Port Reg File

(There are a variety of answers since not all ports need to be writable, indeed the write port might be separate again.)

Non determination: if both ports have write capability and both write to same reg in same clock cycle.