a) latency - clear from putting symbols into channel to getting them out capacity - rate at which symbols ca be place in a taken out of channel.

b) no

c) highe layer channel which relies on end-to-end actuable deemants will be limited by the round frip time ic

delay for single back in simple ARD is

B + 2 Td

where Bis block size, I capacity, I'd delay atmitione way) on channel.

if B/r small compact to 2 Td

Her capacity of Indu layer chand in

\[
\frac{B}{2\lambda d}.
\]

p693 (contd)

d) shelp under (and laze window)

effectively means B/r becomes lage

wit. 27d

e) interactions at application layer ofthe involve small amont of information so lage winder size doesn't rely.