i) error combol (the one time arm on depitsation)

Stal raws of component (southers the and channel.

live of stage (est of south munipolition less verine here)

ii) quantisation wire (race more bits / sample)

sampling arm (sample at 2 times BW, more BW higher

sampling, eg telephony is CD)

iii) fixed capitaly, fixed delay not so conserved with arm

ii) sync ideal if really fixed segment rate south

sync or if surranted on dely made, bufferry

in lady can south.