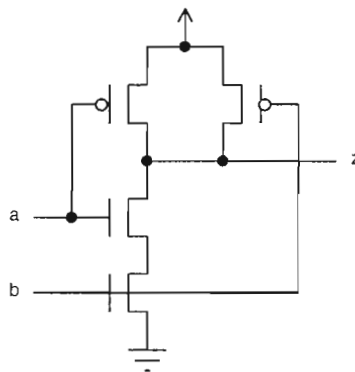
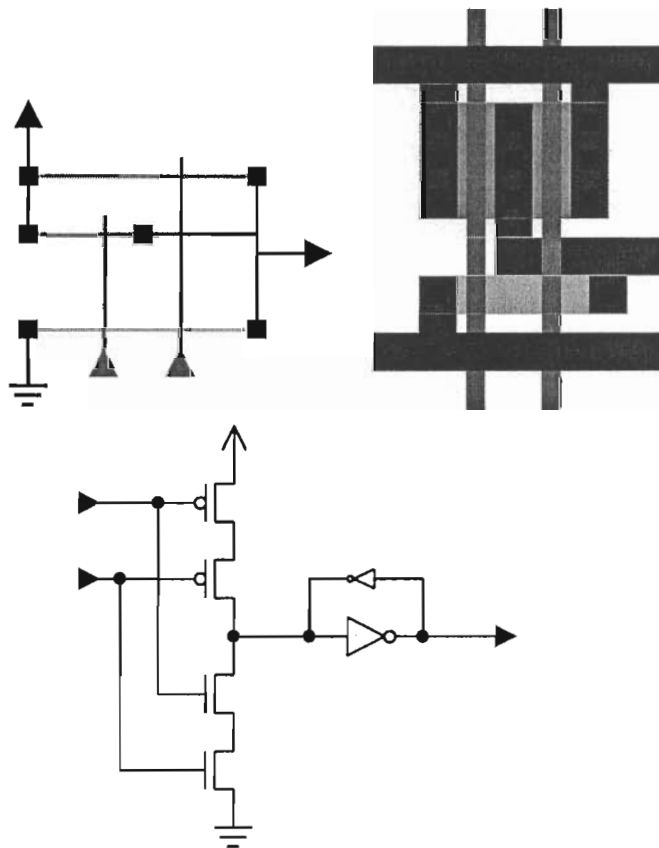


**Answer A**

AND function using nFETs in series for pull-down, complementary circuit using parallel pFETs for pull-up.



The small inverter provides a weak feed-back to retain state. Transition on output occurs only after transitions on both inputs.

*Question draws on early section on combinational logic and late section on self-timed logic.*