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Generate Bill of Materials

Header:

Item\tPart\tDescription\tPCB Footprint\tReference\tQuantity\tOption

Combined property string:

{Item}\t{Value}\t{Description}\t{PCB Footprint}\t{Reference}\t{Quantity}\t{Option}

Description

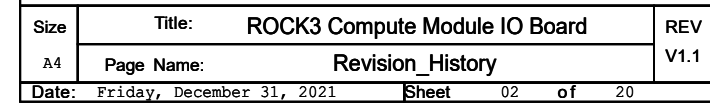
Note

Option

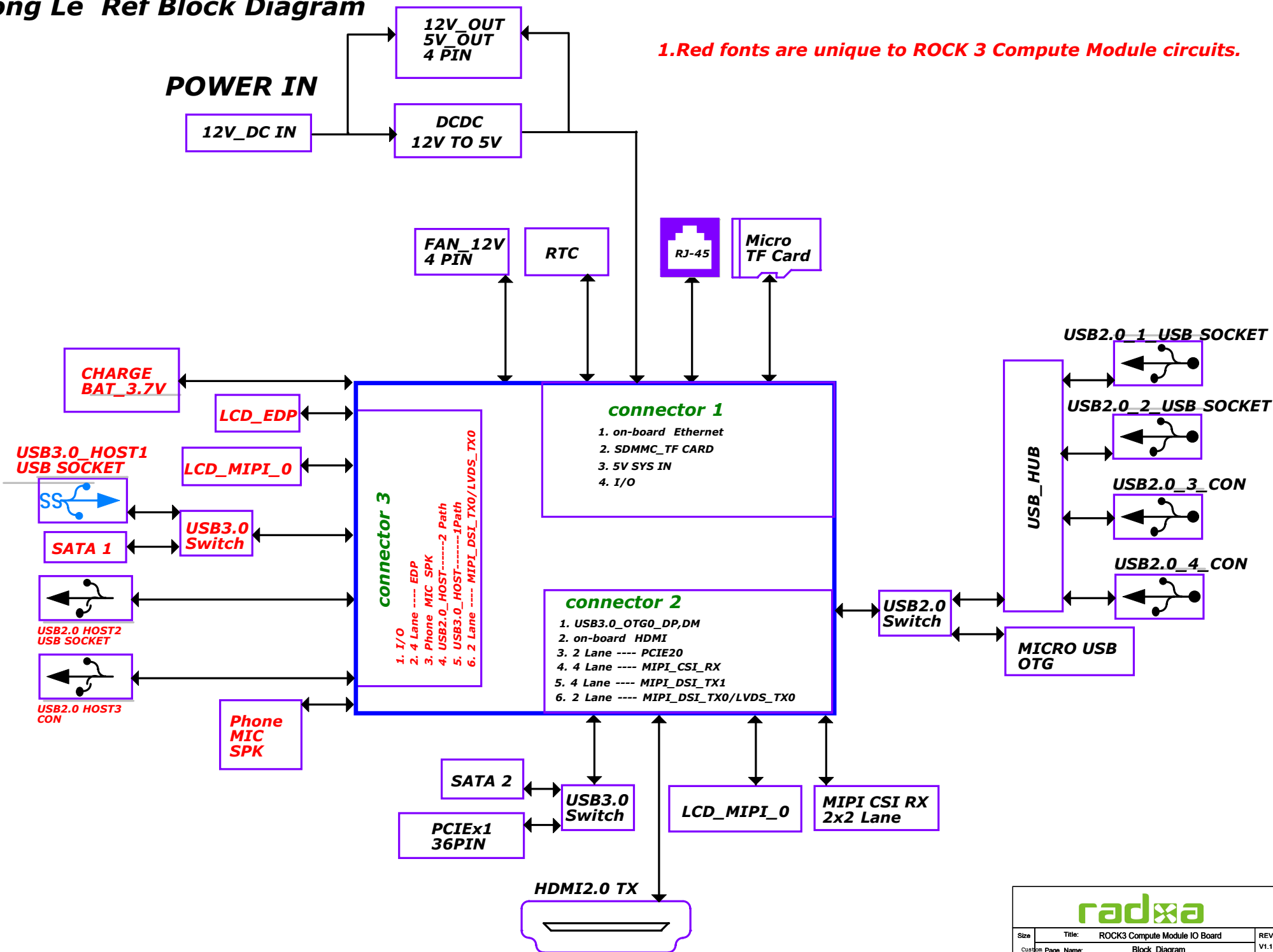
Notes

- NOTE 1:
Component parameter description
1. DNP stands for component not mounted temporarily
2. If Value or option is DNP, which means the area is reserved without being mounted
- NOTE 2:
Please use our recommended components to avoid too many changes.
For more informations about the second source,please refer to our AVL.

<i>Version</i>	<i>Date</i>	<i>By</i>	<i>Change Description</i>	<i>Approved</i>
<i>V1.0</i>	<i>2021-10</i>			
<i>V1.1</i>	<i>2021-12</i>			

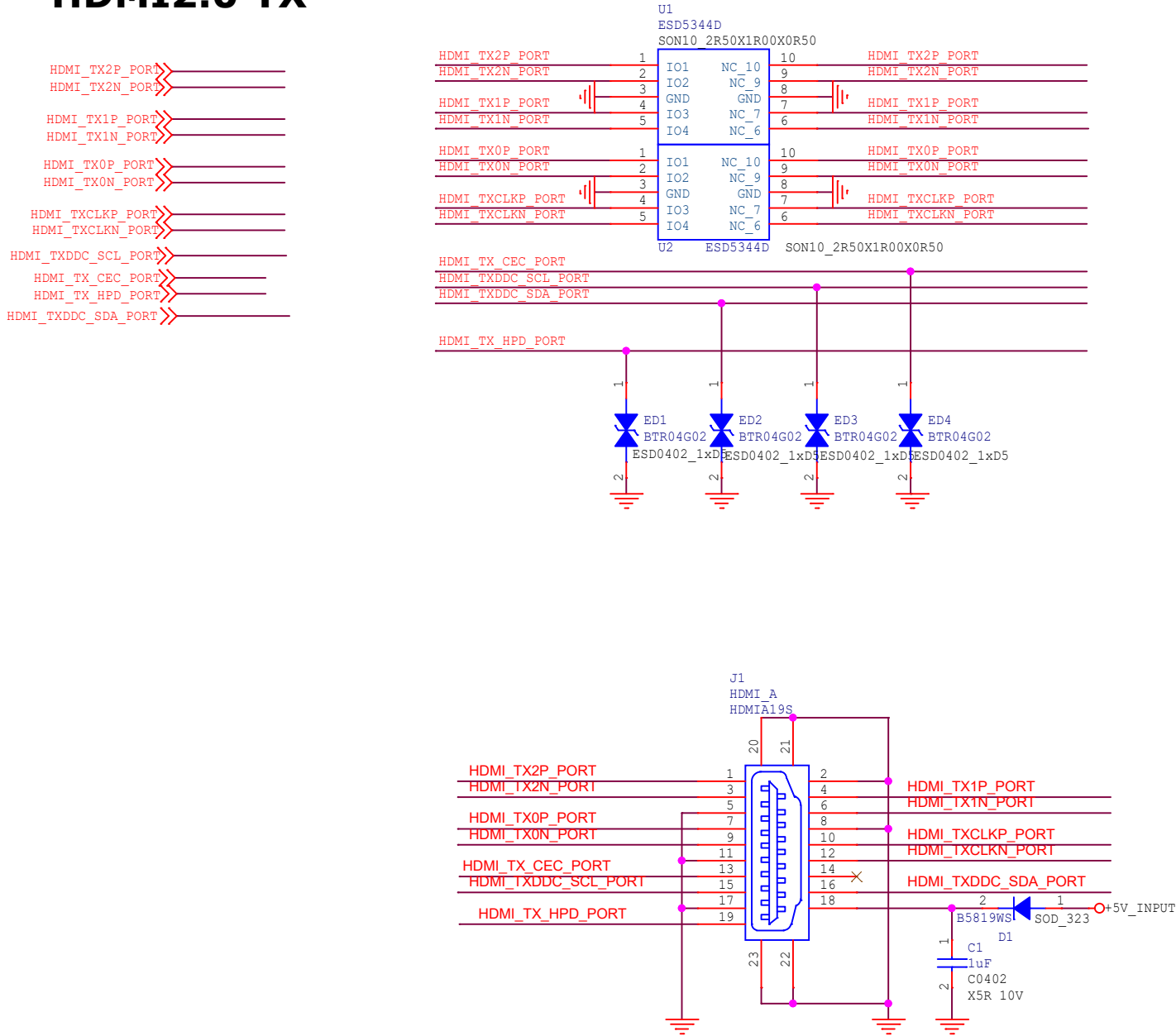


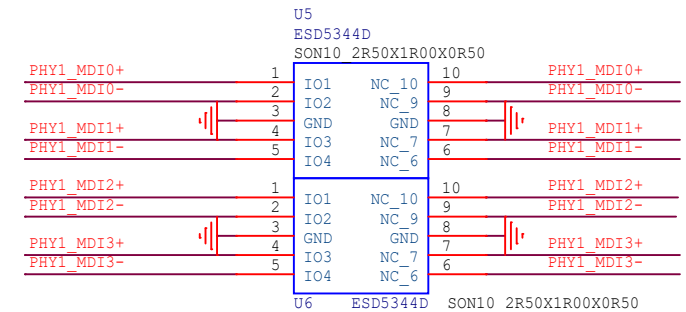
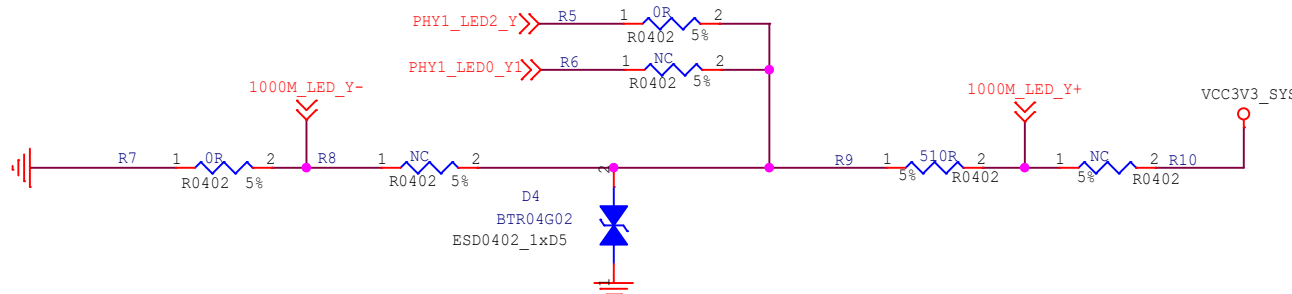
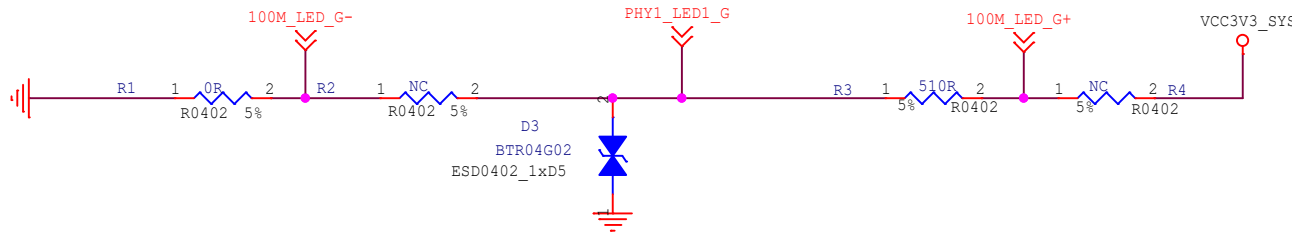
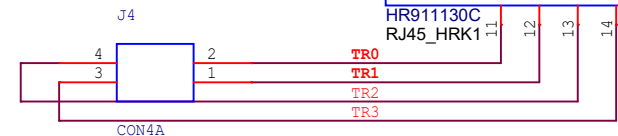
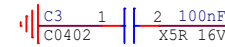
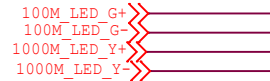
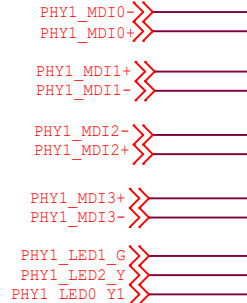
Gong Le Ref Block Diagram



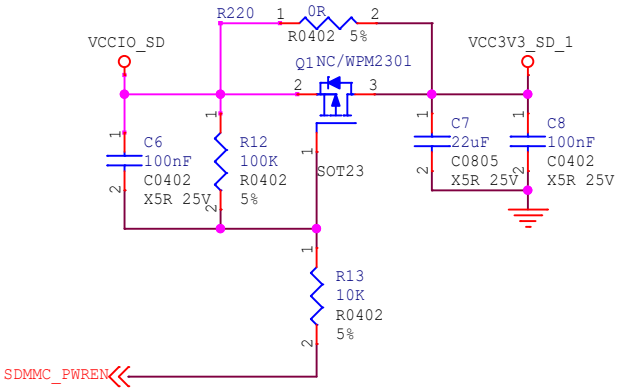
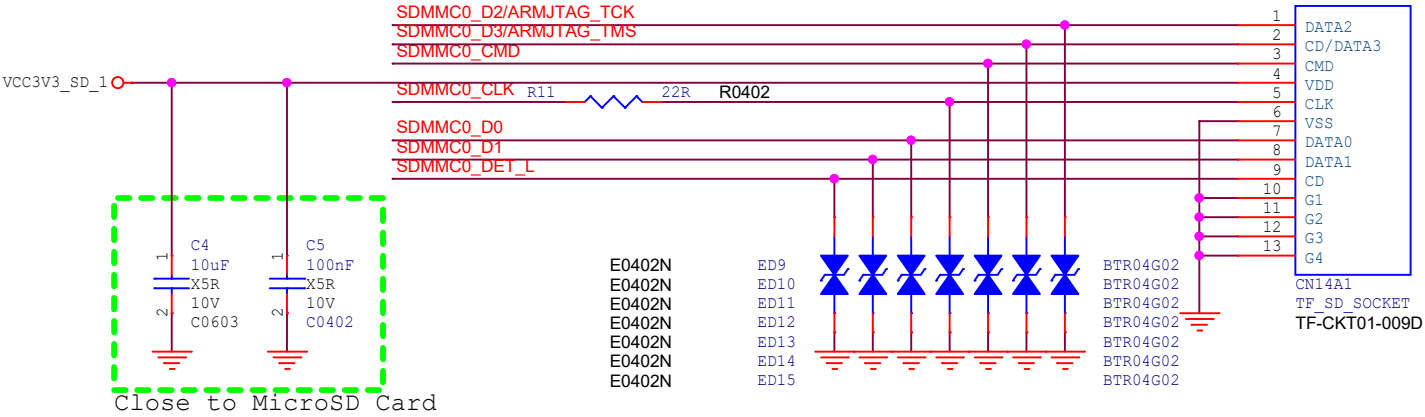
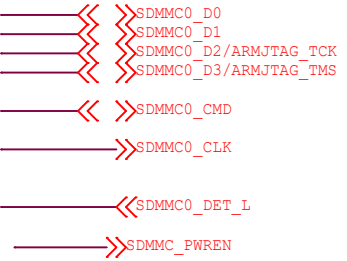
HDMI2.0 TX

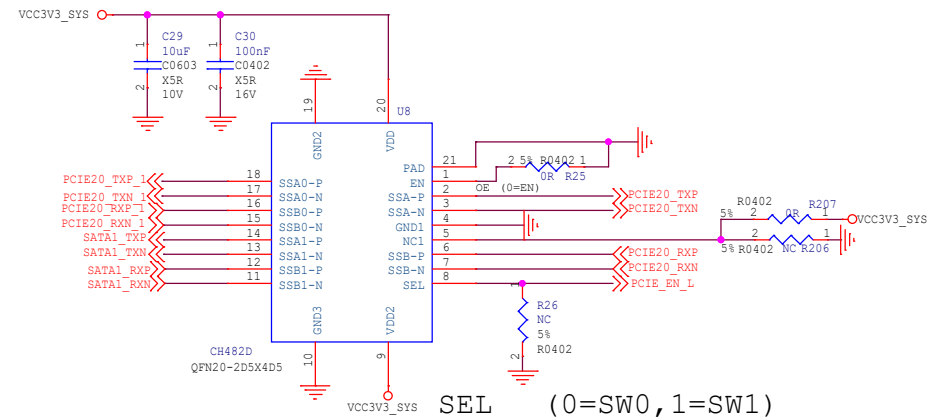
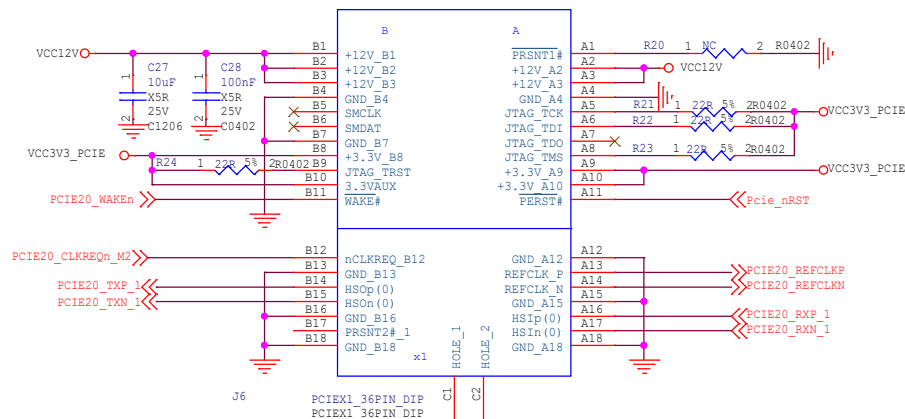
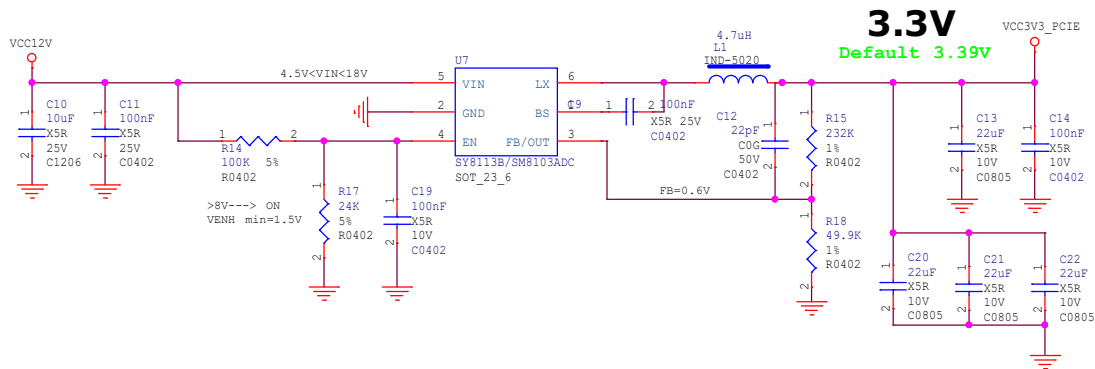
Cj<=0.4pF





MicroSD Card



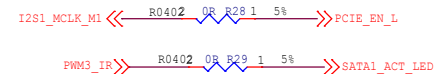
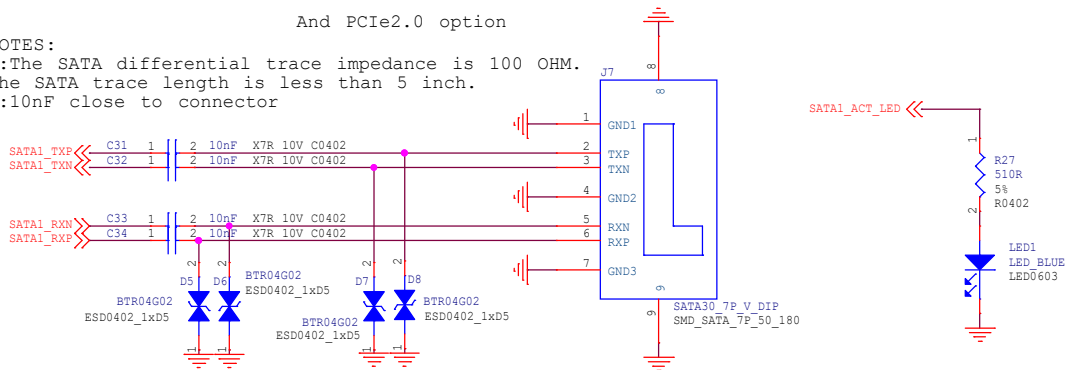


SATA3.0 Port2

And PCIE2.0 option

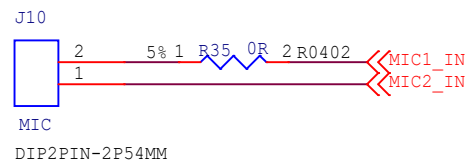
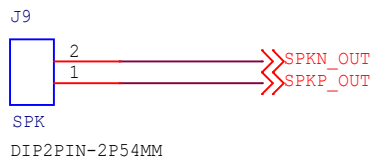
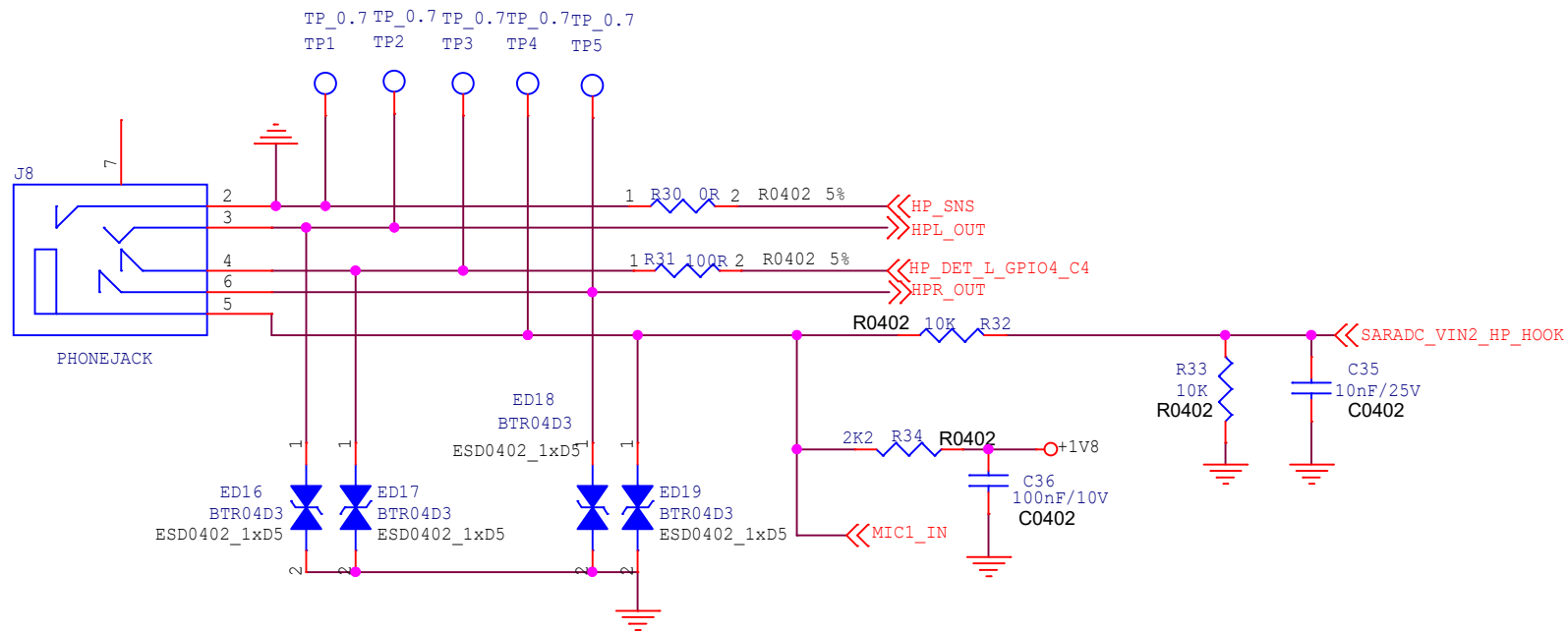
NOTES:

- 1:The SATA differential trace impedance is 100 OHM.
- 2:The SATA trace length is less than 5 inch.
- 3:10nF close to connector



radxa

Size	Title:	REV
B	Page Name:	V1.1
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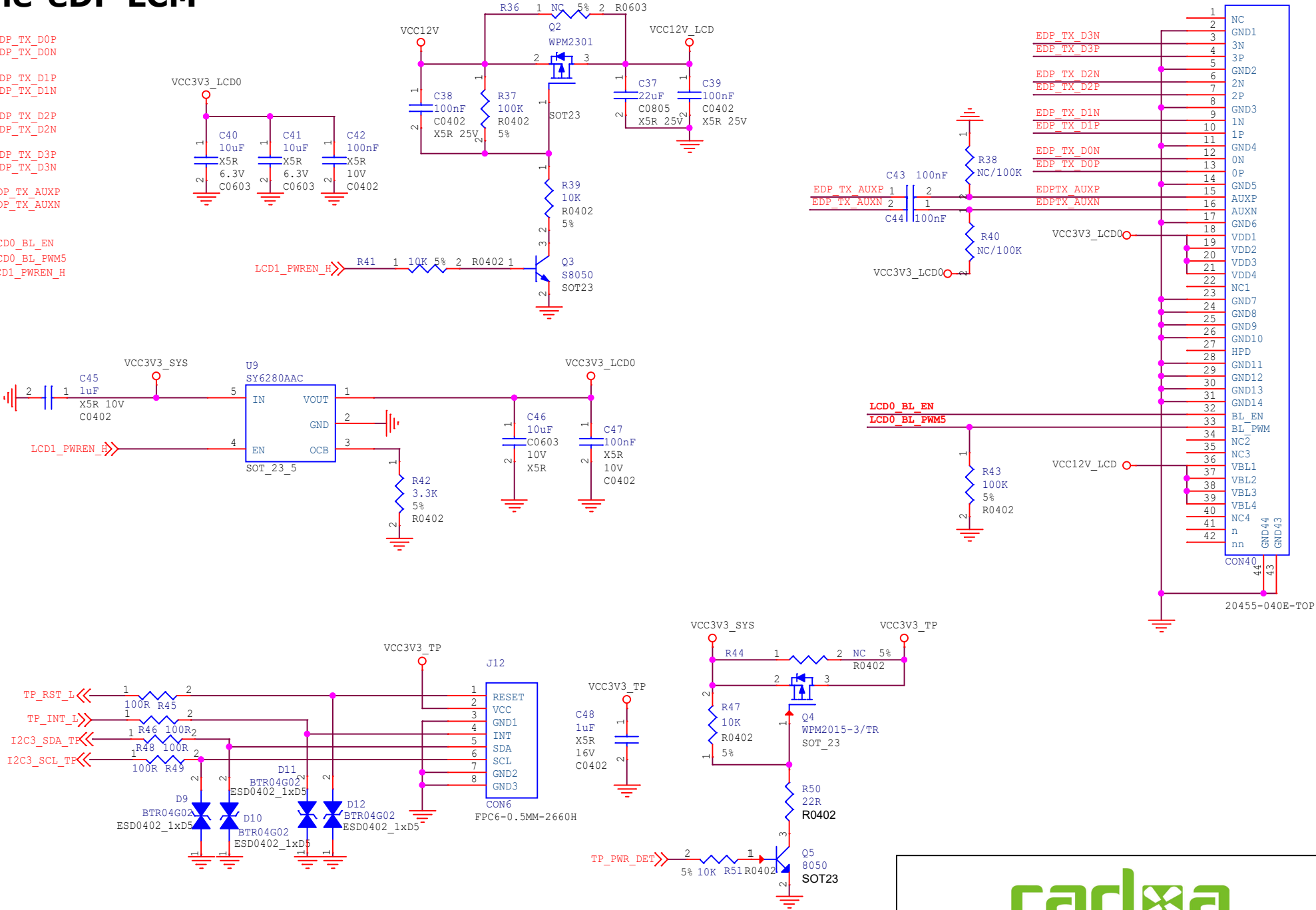


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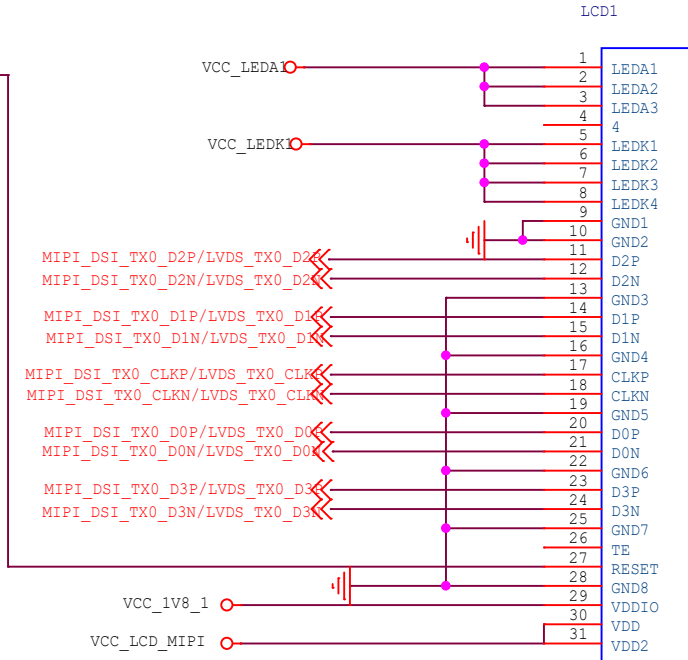
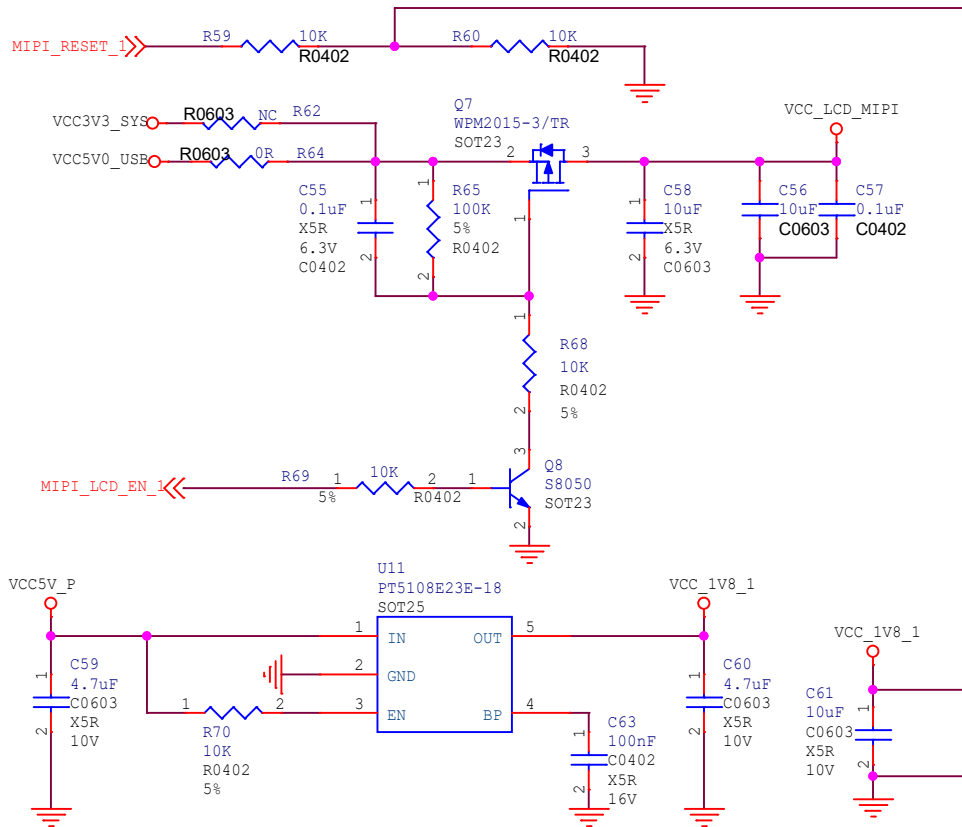
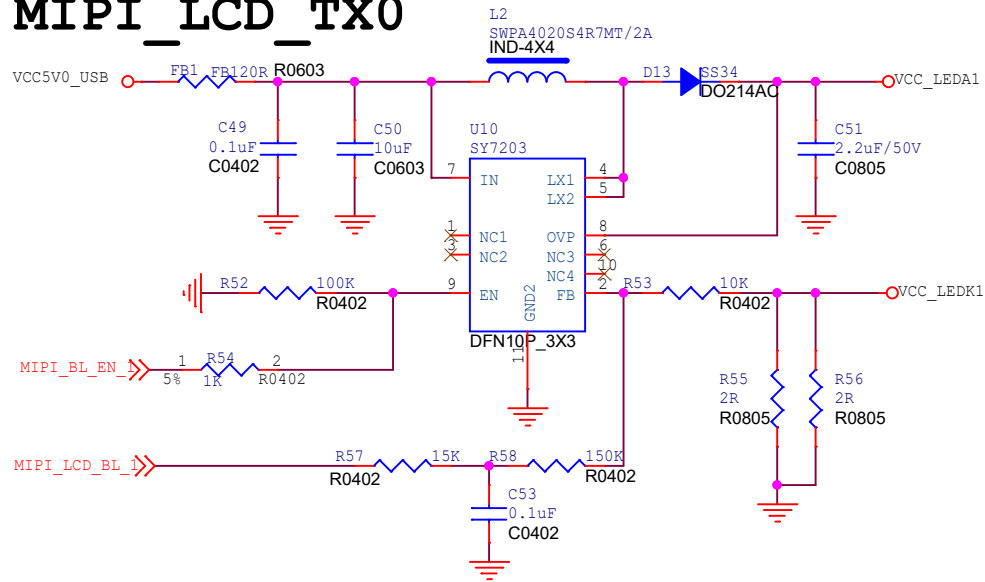
Size	Title: ROCK3 Compute Module IO Board	REV
A	Page Name: AUDIO	V1.1
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Single-eDP LCM

- EDP_TX_D0P
- EDP_TX_D0N
- EDP_TX_D1P
- EDP_TX_D1N
- EDP_TX_D2P
- EDP_TX_D2N
- EDP_TX_D3P
- EDP_TX_D3N
- EDP_TX_AUXP
- EDP_TX_AUXN
- LCD0_BL_EN
- LCD0_BL_PWM5
- LCD1_PWREN_H



MIPI_LCD_TX0

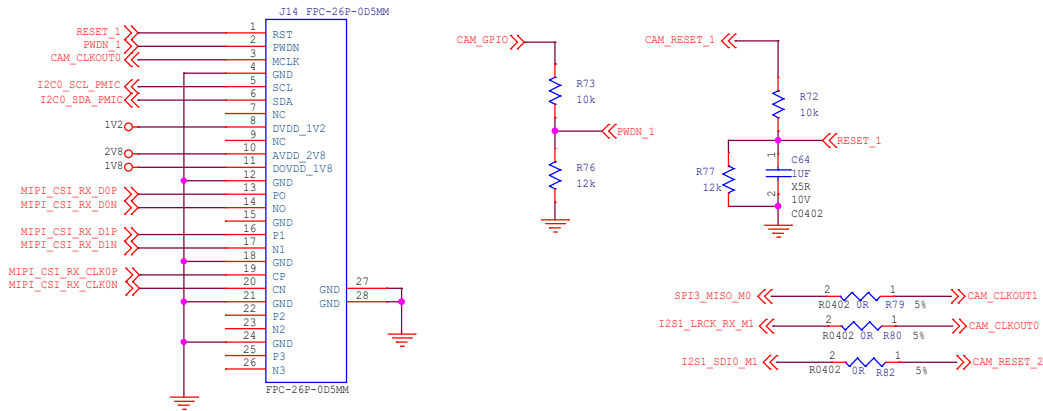


FH35C_31P_0_6SHW
FH35C_31P_0_6SHW

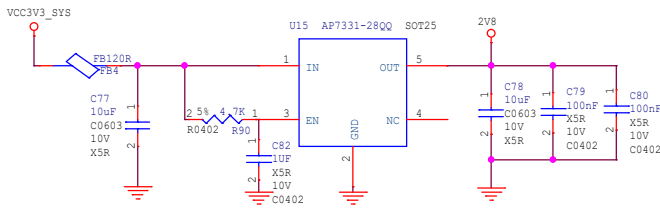
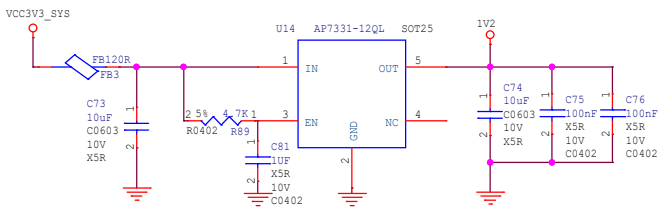
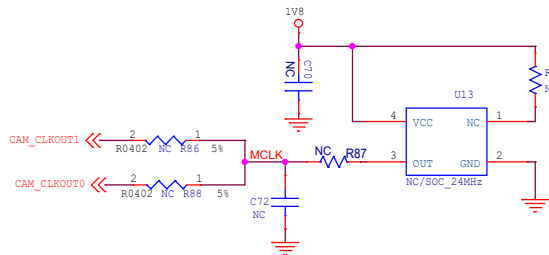
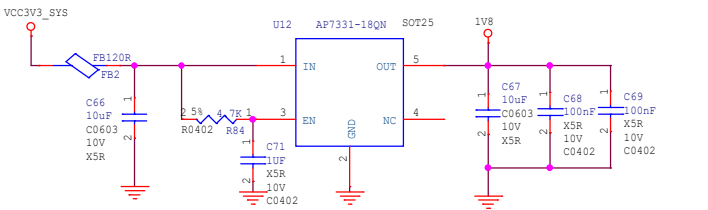
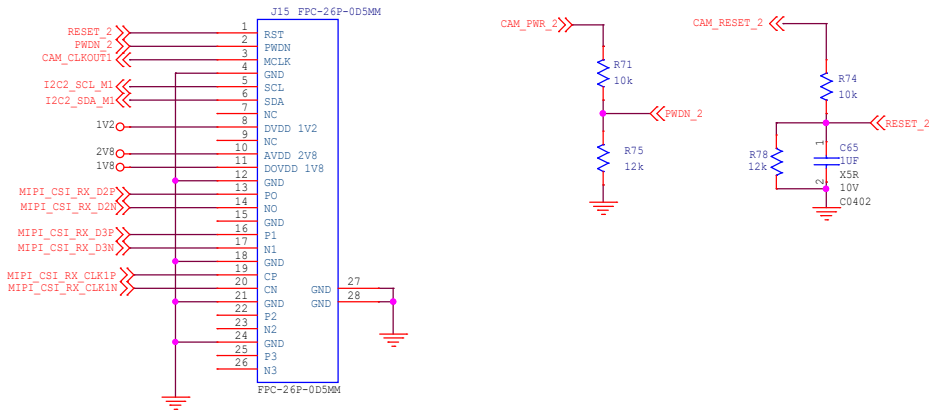


Size	Title: ROCK3 Compute Module IO Board		REV V1.1
	Page Name: MIPI_LCD_TX0		
A4	Date: Friday, December 31, 2021		Sheet 10 of 20
2		1	

MIPI_CSI_RX 2Lanes

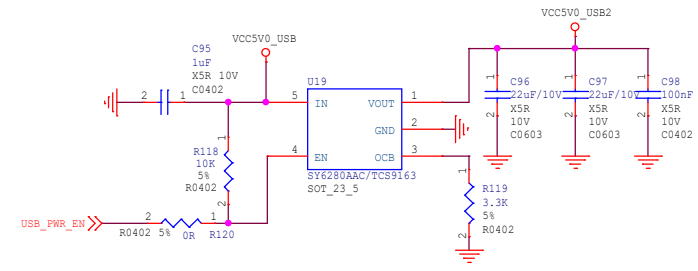
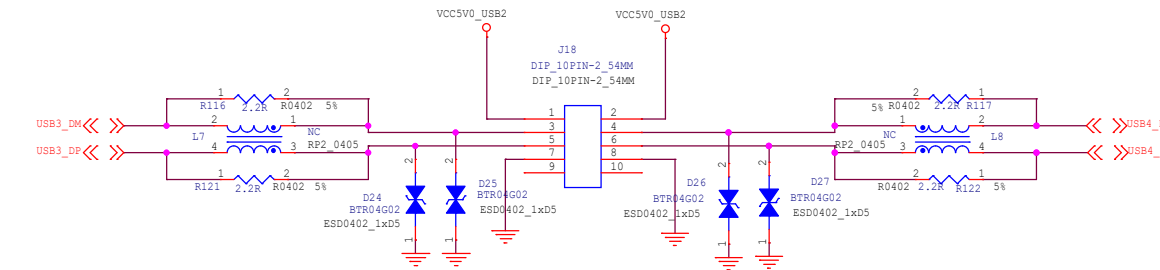
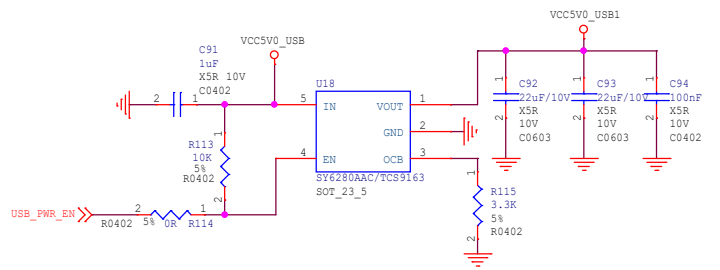
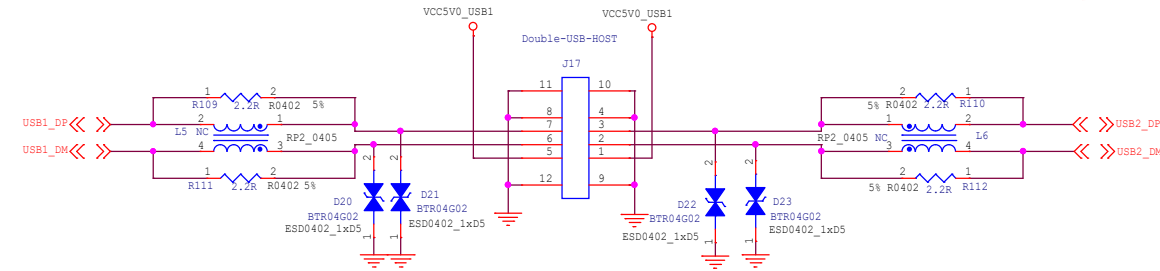
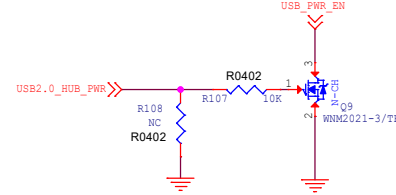
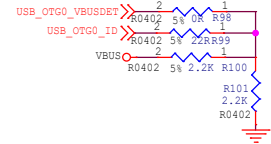
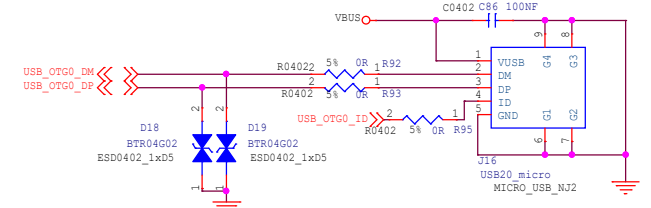
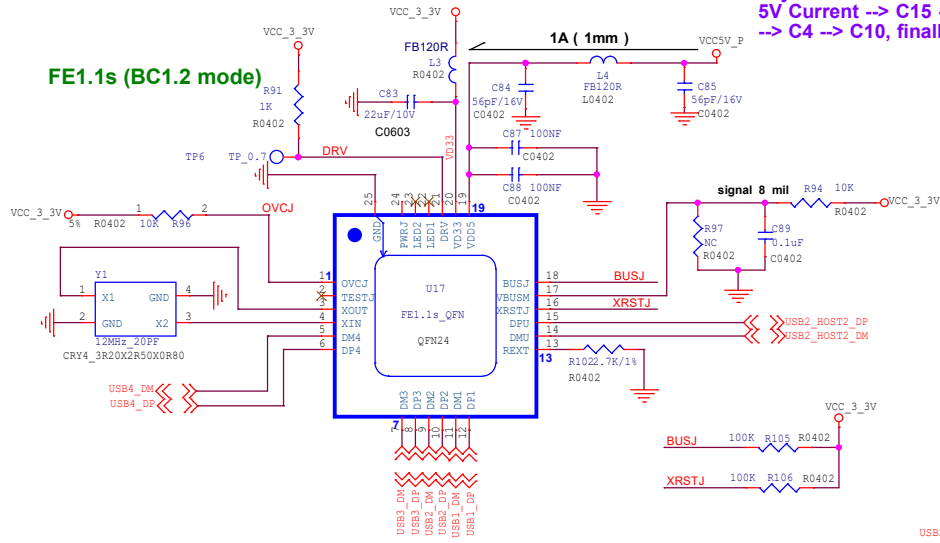


MIPI_CSI_RX 2Lanes



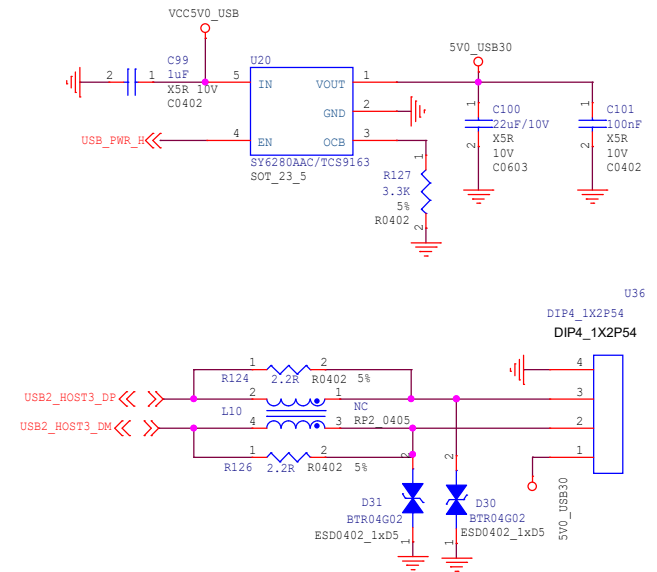
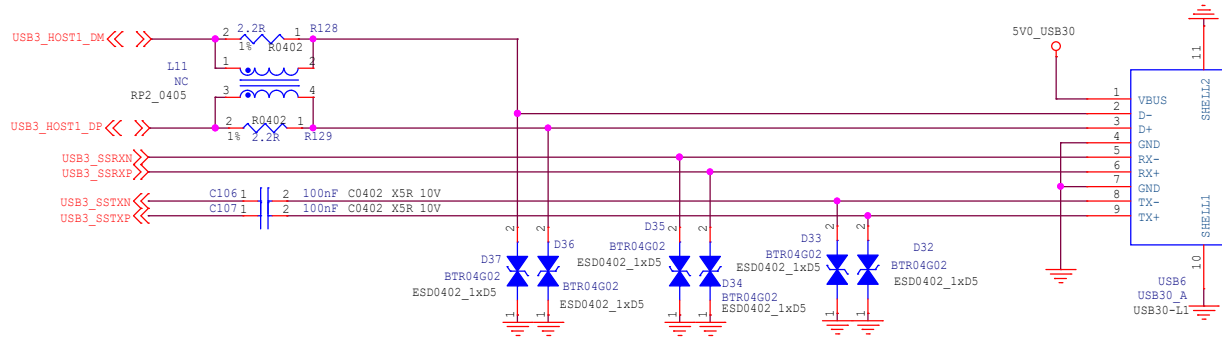
FE1.1s (BC1.2 mode)

Layout checked :
5V Current -> C15 -> L1 -> C14
-> C4 -> C10, finally to the pin 20 (VDD5).



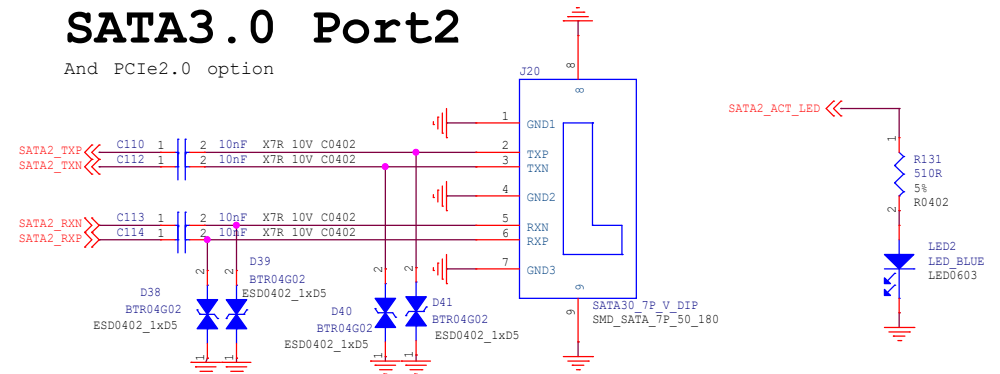
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Size	Title	REV
A3	ROCK3 Compute Module IO Board	V1.1
Page Name: USB OTG/ HUB		
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SATA3.0 Port2

And PCIe2.0 option



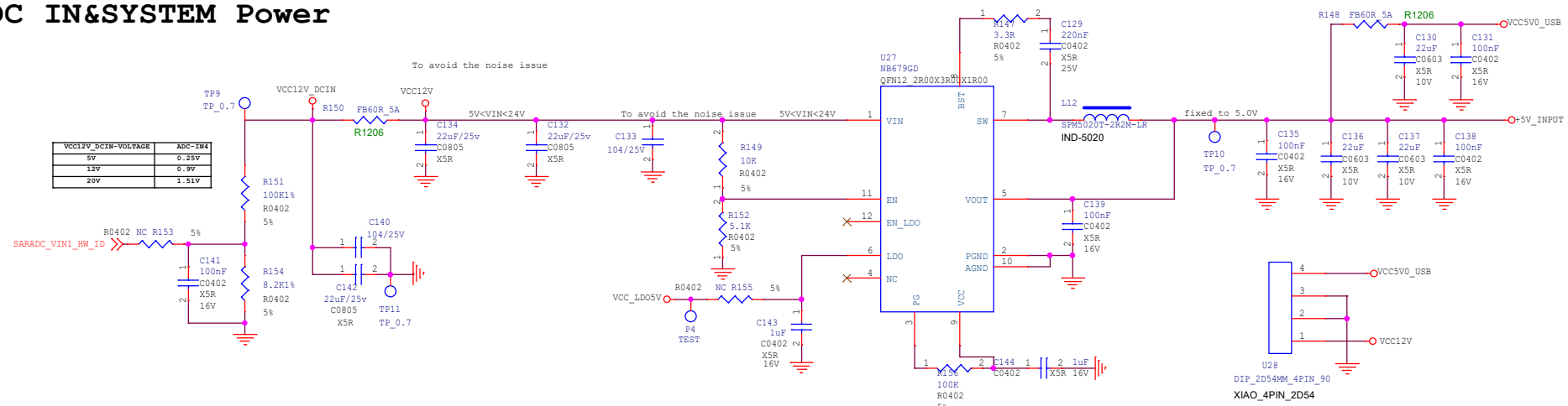
NOTES:

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- 2:The SATA trace length is less than 5 inch.
- 3:10nF close to connector

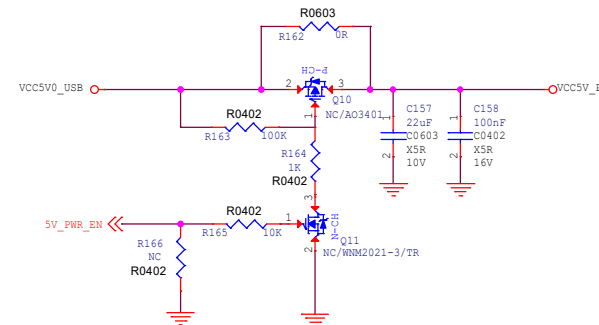
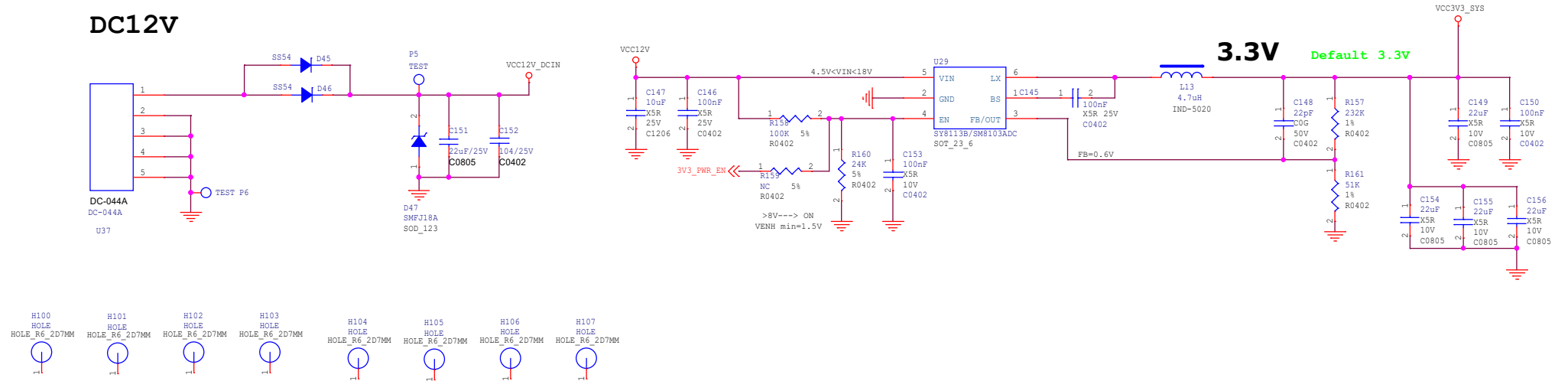
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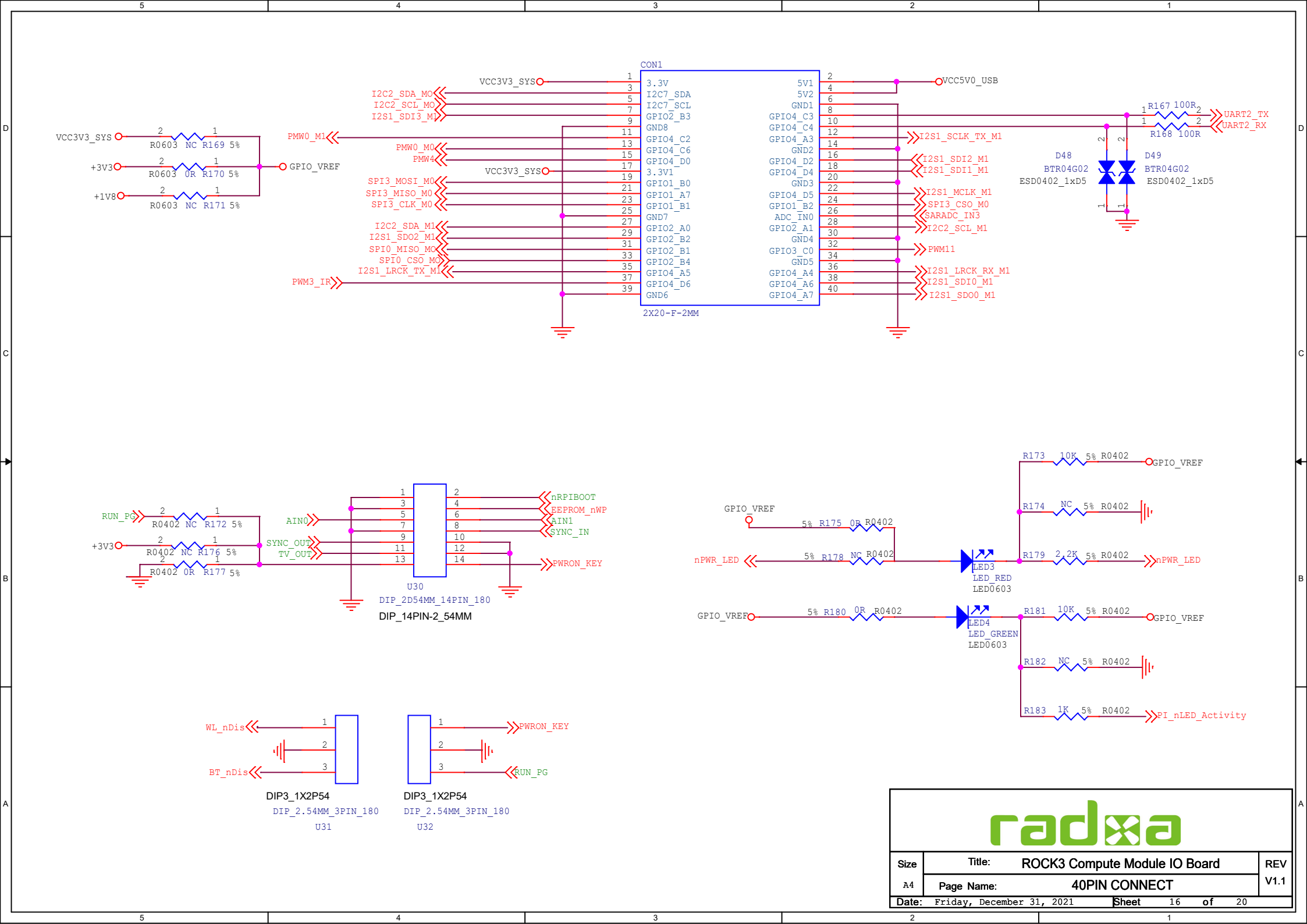
Size B	Title: ROCK3 Compute Module IO Board	REV V1.1
	Page Name: USB HOST	
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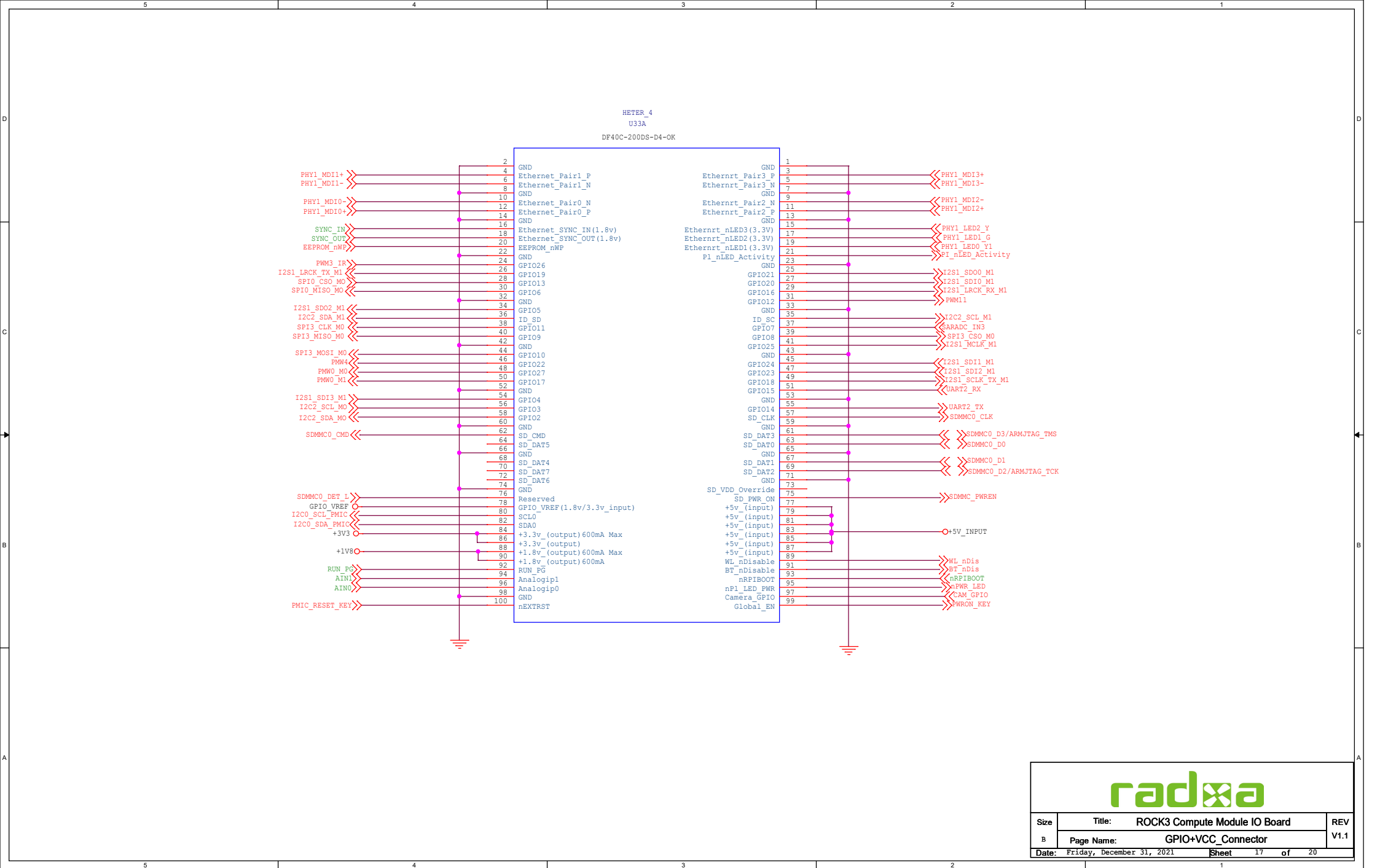
DC IN&SYSTEM Power

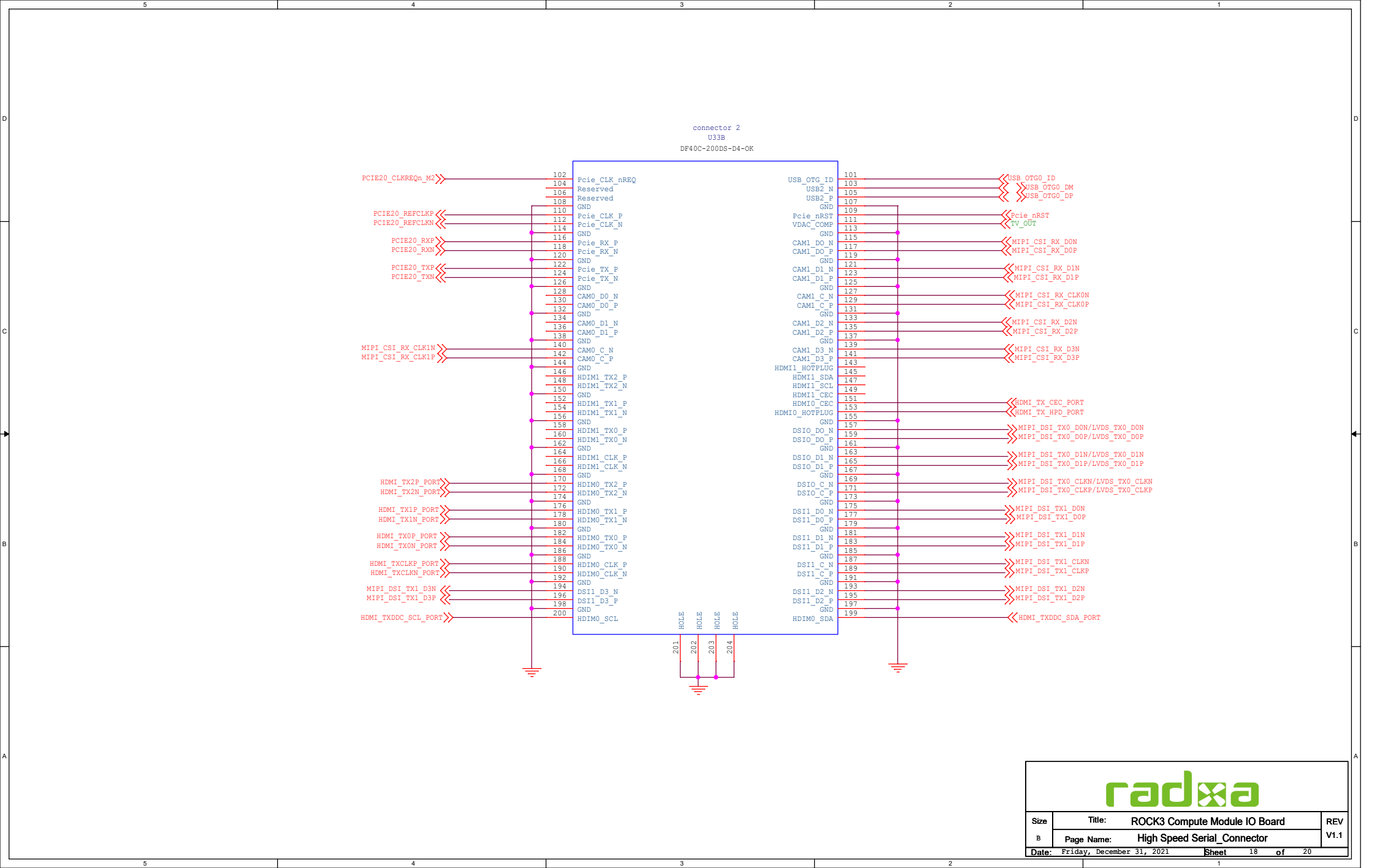


DC12V









Size	Title: ROCK3 Compute Module IO Board	REV V1.1
	Page Name: High Speed Serial_Connector	
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