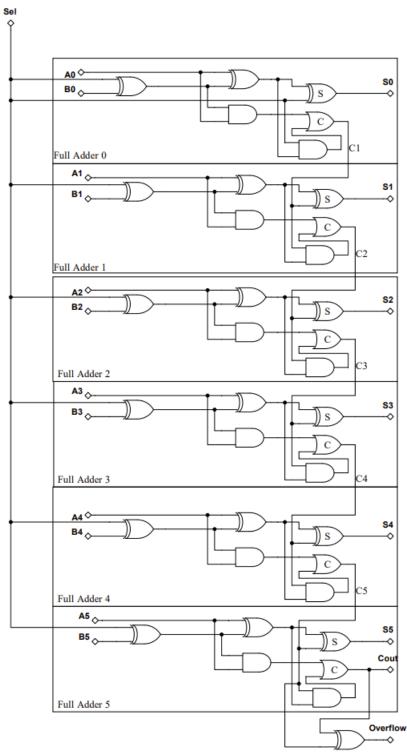
Lab C

4) Block Diagram for the Ripple Adder



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Test	Cal	X		Cout	Overflow	Sum
Vector ID	Sel		У	Expected	Expected	Expected
1	0	000001	000010	0	0	000011
2	1	000001	000010	0	0	111111
3	0	100000	100000	1	1	000000
4	1	101010	101010	1	0	000000
5	0	111101	111111	1	0	111100
6	1	111101	111111	1	0	111011

Name	Value	0 ns	50 ns	100 ns	150 ns	200 ns	250 ns
✓ ▼ TB Vectors							
> 🕨 vec1[5:0]	111101	000001		100000	101010	111	101
> W vec2[5:0]	000010	000010		100000	101010	111111	000010
	1						
✓ I≡ Output							
> 💆 result[5:0]	111011	000011 111111		000000		111100	111011
¹⊌ c_out	1						
ovflow	0						

9) The circuit has passed all the test vectors we have given it. However, this is definitely not a fully exhaustive test bench. But we have gone through all the important test cases (ie ones that indicate carry and overflow and addition and subtraction with negative numbers)

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