

ELC 2137 Lab 7: Binary Coded Decimal

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Summary

Type the summary of your experiment and results here.

Q&A

Answer questions posed in the lab assignment here.

Results

Code

Include all of the code you wrote or modified here.

Figure 1: ERT for Add 3 Module

num	modnum
0000	0000
0001	0001
0010	0010
0011	0011
0100	0100
0101	1000
0110	1001
0111	1010
1000	1011
1001	1100
1010	1101
1011	1110
1100	1111
1101	0000
1110	0001
1111	0010

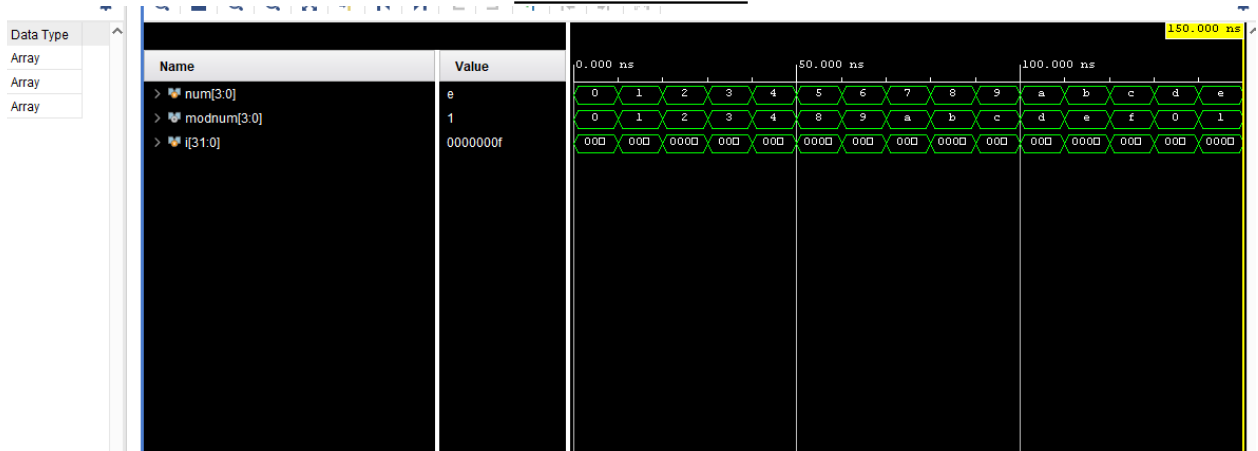


Figure 2: Add 3 Module Simulation

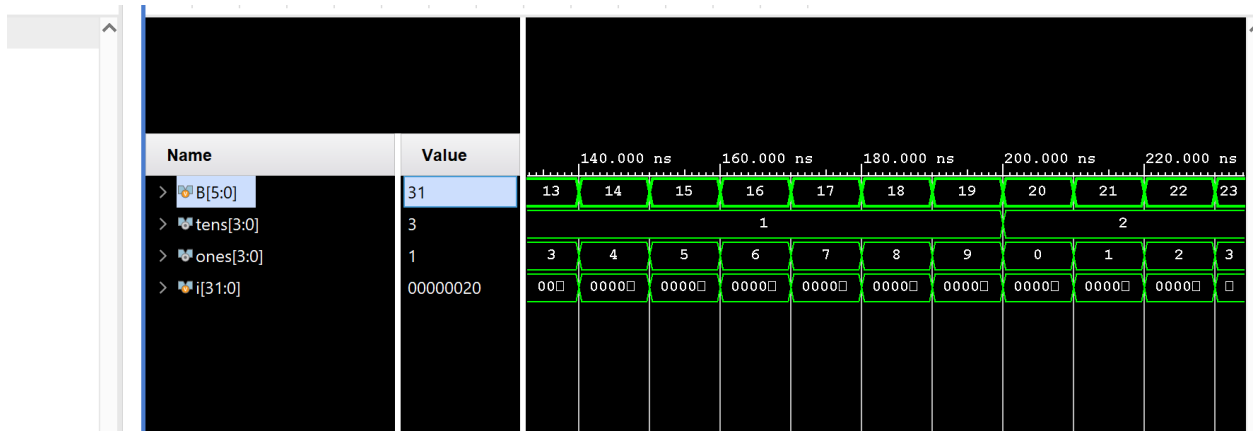


Figure 3: Six Bit Double-Dabble Module Simulation

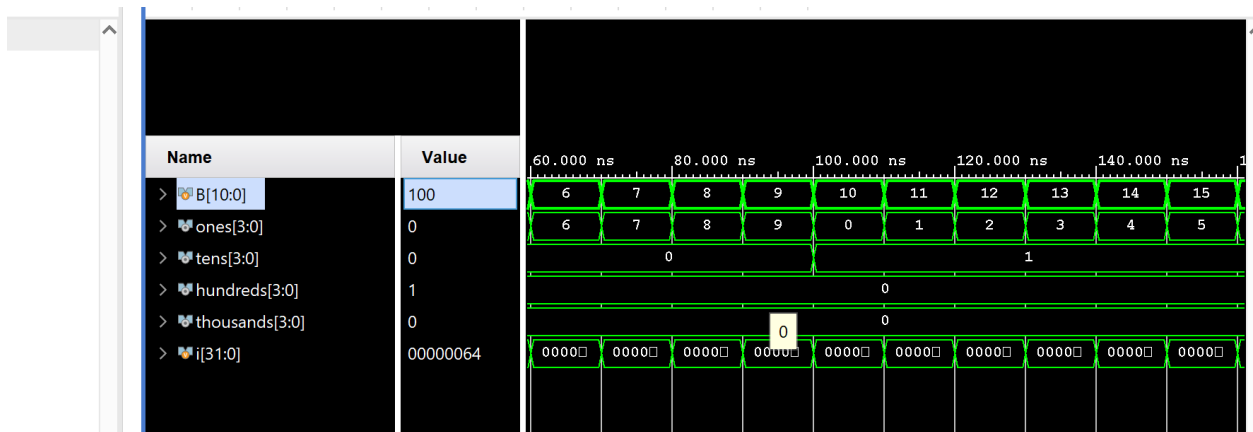


Figure 4: Eleven Bit Double-Dabble Module Simulation