

//TITLE

By

Aaron Ward

Supervisor(s): Stephen Sheridan

SUBMITTED IN PARTIAL FULFILLMENT OF THE
REQUIREMENTS FOR THE DEGREE OF
B.SC IN COMPUTING
AT
INSTITUTE OF TECHNOLOGY BLANCHARDSTOWN
DUBLIN, IRELAND
2018

Declaration

I hereby certify that this material, which I now submit for assessment on the programme of study leading to the award of **B.Sc in Computing** in the Institute of Technology Blanchardstown, is entirely my own work except where otherwise stated, and has not been submitted for assessment for an academic purpose at this or any other academic institution other than in partial fulfillment of the requirements of that stated above.

Dated: 2018

Author:

Aaron Ward

Abstract

Acknowledgements

/***** Insert acknowledgment here *****/

Table of Contents

| | |
|--------------------------------------|-------------|
| Abstract | ii |
| Acknowledgements | iii |
| Table of Contents | iv |
| List of Tables | vi |
| List of Figures | vii |
| Abbreviations | viii |
| | |
| 1 Introduction and Background | 1 |
| 1.1 section header 1 | 1 |
| | |
| 2 Literature Review | 2 |
| 2.1 section header 1 | 2 |
| 2.1.1 Subsection header 1 | 2 |
| | |
| 3 Methodology Chapter | 3 |
| 3.1 section header 1 | 3 |
| 3.1.1 Subsection header 1 | 3 |

| | | |
|----------|------------------------------------|-----------|
| 4 | System Design | 4 |
| 4.1 | section header 1 | 4 |
| 4.1.1 | Subsection header 1 | 4 |
| 5 | Implementation | 5 |
| 5.1 | section header 1 | 5 |
| 5.1.1 | Subsection header 1 | 5 |
| 6 | Testing and Evaluation | 6 |
| 6.1 | section header 1 | 6 |
| 6.1.1 | Subsection header 1 | 6 |
| 7 | Discussion | 7 |
| 7.1 | section header 1 | 7 |
| 7.1.1 | Subsection header 1 | 7 |
| 8 | Conclusion and Further Work | 8 |
| 8.1 | section header 1 | 8 |
| 8.1.1 | Subsection header 1 | 8 |
| | Bibliography | 9 |
| | Appendices | 10 |
| A | sample section header | 10 |

List of Tables

List of Figures

Abbreviations

| | |
|-----|---------------------------|
| ANN | Artificial Neural Network |
|-----|---------------------------|

Chapter 1

Introduction and Background

1.1 section header 1

Chapter 2

Literature Review

2.1 section header 1

2.1.1 Subsection header 1

Chapter 3

Methodology Chapter

3.1 section header 1

3.1.1 Subsection header 1

Chapter 4

System Design

4.1 section header 1

4.1.1 Subsection header 1

Chapter 5

Implementation

5.1 section header 1

5.1.1 Subsection header 1

Chapter 6

Testing and Evaluation

6.1 section header 1

6.1.1 Subsection header 1

Chapter 7

Discussion

7.1 section header 1

7.1.1 Subsection header 1

Chapter 8

Conclusion and Further Work

8.1 section header 1

8.1.1 Subsection header 1

Bibliography

Appendices

Appendix A

sample section header