# BIRLA INSTITUTE OF TECHNOLOGY AND SCIENCE, PILANI HYDERABAD CAMPUS

# FIRST SEMESTER 2019-2020 Course Handout (Part-II)

01.08.2019

In addition to general handout for all courses appended to the time table, this portion gives further specific details regarding the course.

**Course No.** : EEE F111

**Course Title** : ELECTRICAL SCIENCES

**Instructor-in-charge**: Sudha Radhika

Instructors: Lectures: Sudha Radhika, Parikshit Sahatiya

**Instructors:** Tutorials: Sudha Radhika, Parikshit Sahatiya, Sourav Nandi, Sandeep Kumar

#### 1. Course Description:

Course covers basic passive and active circuit elements; network theorems and analysis; introduction to single and three phase systems; magnetic circuits; transformers; electrical machines; semi-conductor diodes and applications; transistors and applications; Digital electronics and commonly used measuring instruments.

#### 2. Scope and objective of the Course:

A basic understanding of the working of electrical and electronic circuits and instruments is essential for all engineers and scientists. This course is designed to give the students of all branches a preliminary exposure to this field. The need for basic understanding in this field will come for non-electrical or electronic students at a later stage in their career growth. For EEE, ECE and E&I students this course acts as a good starting point for their CDCs.

To obtain basic knowledge on:

- a. Electrical and Magnetic Circuits.
- b. Electrical machines.
- c. Semiconductor Diodes and BJTs; Digital electronics.
- **3. Text Book**: Leonard S. Bobrow: Fundamentals of Electrical Engineering, Oxford University Press, Second Edition, 2005.

### 4. Reference Book:

Hughes: Electrical and Electronic Technology, Pearson Education, Ninth Edition, 2008.

#### 5. Course Plan:

Lect. No.	Learning Objectives	Topics to be covered	Chapter in the Text Book
1	Introduction	Introduction	
2	To study basic circuit elements and the laws;	Voltage and current sources, Independent and Dependent sources resistors and ohm's law, KCL, KVL; Current divider, Voltage divider rule, Instantaneous power	1.1 to 1.5
3-4	To study circuit analysis techniques and theorems.	Nodal and Mesh Analysis	2.1, 2.3

Lect. No.	Learning Objectives	Topics to be covered	Chapter in the Text Book
5-6	To study circuit analysis	Thevenin's and Norton's Theorems; Maximum	2.4, 2.5
_	techniques and theorems.	Power Transfer Theorem,	_
7-8	To study circuit analysis	Linearity and Superposition application in circuit	2.6
	techniques and theorems.	analysis, Source transformation	
9	Inductors and Capacitors	Inductors and capacitors and their integral relationships;	3.1 to 3.2
11-13	To study response of	First order circuits and natural response; First	3.3 to 3.6
	circuits having energy	order circuits and complete response	
	storing elements	Second Order Circuits	
14-18	Alternating current	A.C. Voltage & Current, Complex numbers,	4.1-4.3
	circuits	Frequency and Time Domain analysis	
19-21	Alternating current circuits	Power and Power-factors, Poly-Phase circuits	4.4 to 4.7
22-23	Magnetic Circuits	Fundamentals of Electromagnetics, Magnetic	14.1-14.2
		fields and their effects, Magnetic Circuits and Materials	
24-25	Transformers	Introduction , Ideal transformer; Equivalent circuit; Non-ideal transformer;	14.3 -14.5
26-27	Electrical Machines	Motors and generators	15.4
28-31	Principles and	Semiconductors, doping, Diodes, Zener diodes,	6.1-6.7
	Applications of	effects of capacitance, Half-wave and full wave	
	Semiconductor Diodes,	rectifiers	
	Diode Circuits		
32-35	Bipolar Junction	pnp and npn transistors, Characteristics and	7.1-7.4
	Transistors	Applications of BJTs, Modelling of BJT	
36-38	Field Effect Transistors	JFET, MOSFET	8.1-8.2
39-40	Transistor Amplifiers	BJT amplifiers	9.1
41-43	Digital Systems	Binary numbers, Binary Arithmetic, Digital	11.1-11.6,
		logic circuits, Boolean Algebra	12.1

### **6.** Evaluation Scheme:

Component	Duration	Perecentage weightage	Maximum Marks	Date & Time	Nature of Component
Midsem Test	1.5 hour	30%	90M	1/10, 1.30 3.00 PM	СВ
Quizzes surprise/announced	45 minutes	30%	90M		СВ
Comprehensive	1.5 hours	20%	60M	07/12 (FN)	СВ
Examination	1.5 hours	20%	60M	07/12 (FN)	OB

- **7. Make-up policy**: Make-up will be given only under **exceptional circumstances** and with **prior permission**. No Makeup will be given for a Quiz evaluation component.
- **8. Chamber consultation hour:** To be announced in the class
- **9. Notices**: Notices concerning the course will be displayed in the CMS.
- 10. Academic Honesty and Integrity Policy:

Academic honesty and integrity are to be maintained by all the students throughout the semester and no type of academic dishonesty is acceptable.

Dr. Sudha Radhika INSTRUCTOR-IN-CHARGE EEE F111