2007
EE:Electrical Engineering

Duration:Three hours   Maximum Marks :150

Read the following instructions carefully.

1. This question paper contains 85 objective type questions. Q.1 to Q.20 carry **one mark** each and Q.21 to Q.85 carry **two marks** each.

2. Attempt all the questions.

3. Questions must be answered on **Objective Response Sheet (ORS)** by darkening the appropriate bubble (marked A, B, C, D) using HB pencil against the question number on the left-hand side of the **ORS**. **Each question has only one correct answer.** In case you wish to change an answer, erase the old answer completely.

4. Wrong answers will carry **NEGATIVE** marks. In Q.1 to Q.20, **0.25** mark will be deducted for each wrong answer. In Q.21 to Q.76, Q.76, Q.78, Q.80, Q.82 and in Q.84, **0.5** mark will be deducted for each wrong answer. However, there is no negative marking in Q.77, Q.79, Q.81, Q.83 and in Q.85. More than one answer bubbled against a question will be taken as an incorrect response. Unattempted questions will not carry any marks.

5. Write your registration number, your name and name of the examination centre at the specified locations on the right half of the **ORS**.

6. Using HB pencil, darken the appropriate bubble under each digit of your registration number and the letters corresponding to your paper code.

7. Calculator is allowed in the examination hall.

8. Charts, graph sheets or tables are **NOT** allowed in the examination hall.

9. Rough work can be done on the question paper itself. Additionally blank pages are given at the end of the question paper for rough work.

10. This question paper contains **32** printed pages including pages for rough work. Please check all pages and report, if there is any discrepancy.

Q.1 – Q.20 carry one mark each.

1. The common emitter forward current gain of the transistor shown is $\beta_F = 100$ (GATE EE 2007)
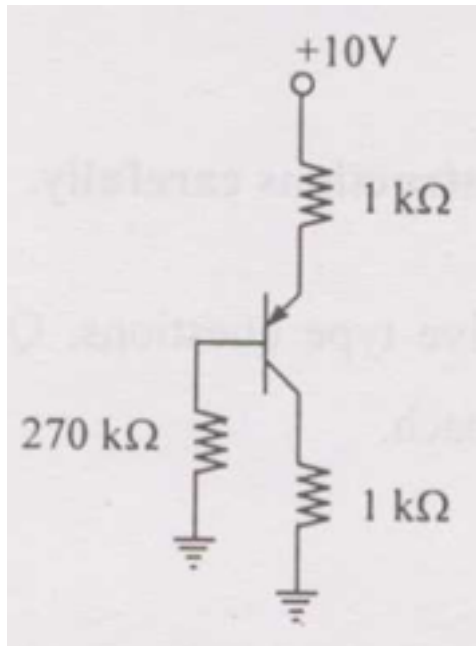
The transistor is operating in

(a) Saturation region   (c) Reverse active region

(b) Cutoff region   (d) Forward active region

2. The three-terminal linear voltage regulator is connected to a 10Ω load resistor as shown in the figure. If V_{in} is 10 V, what is the power dissipated in the transistor?   (GATE EE 2007)
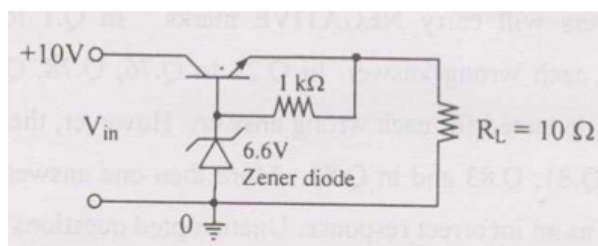
(a) 0.6 W   (b) 2.4 W   (c) 4.2 W   (d) 5.4 W

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3. Consider the transformer connections in a part of a power system shown in the figure. The nature of transformer connections and phase shifts are indicated for all but one transformer.
Which of the following connections, and the corresponding phase shift θ, should be used for the transformer between A and B? (GATE EE 2007)

Autotransformer
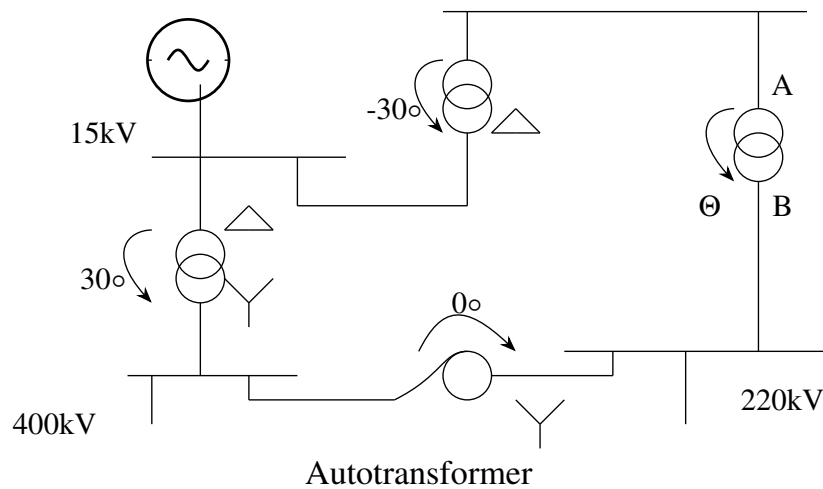
(a) Star - Star ($\theta = 0°$)

(b) Star - Delta($\theta = -30°$)

(c) Delta - Star($\theta = 30°$)
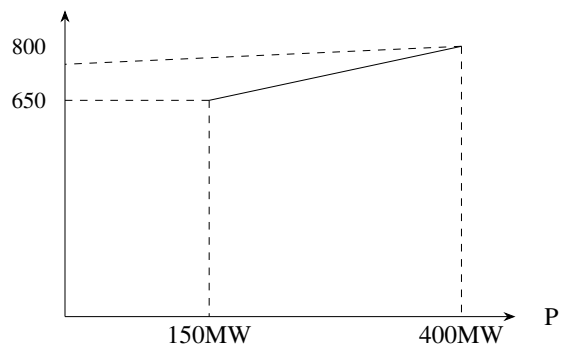
(d) Star - Zigzag($\theta = 30°$)

4. The incremental cost curves in Rs/MWhr for two generators supplying a common load of 700 MW are shown in the figures. The maximum and minimum generation limits are also indicated. The optimum generation schedule is: (GATE EE 2007)

Incremental Cost Rs/MWhr
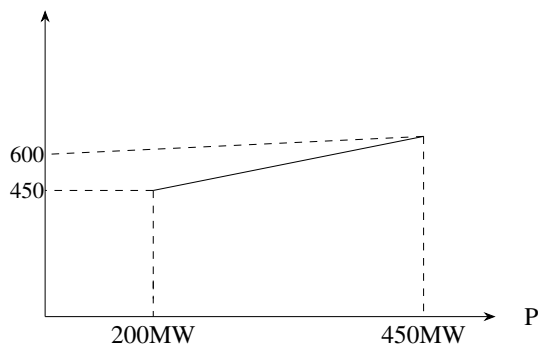

GENERATOR A

Incremental Cost Rs/MWhr

GENERATOR B

(A) Generator A: 400 MW, Generator B: 300 MW

(B) Generator A: 350 MW, Generator B: 350 MW

(C) Generator A: 450 MW, Generator B: 250 MW

(D) Generator A: 425 MW, Generator B: 275 MW

5. Two regional systems, each having several synchronous generators and loads are interconnected by an ac line and a HVDC link as shown in the figure. Which of the following statements is true in the steady state: (GATE EE 2007)
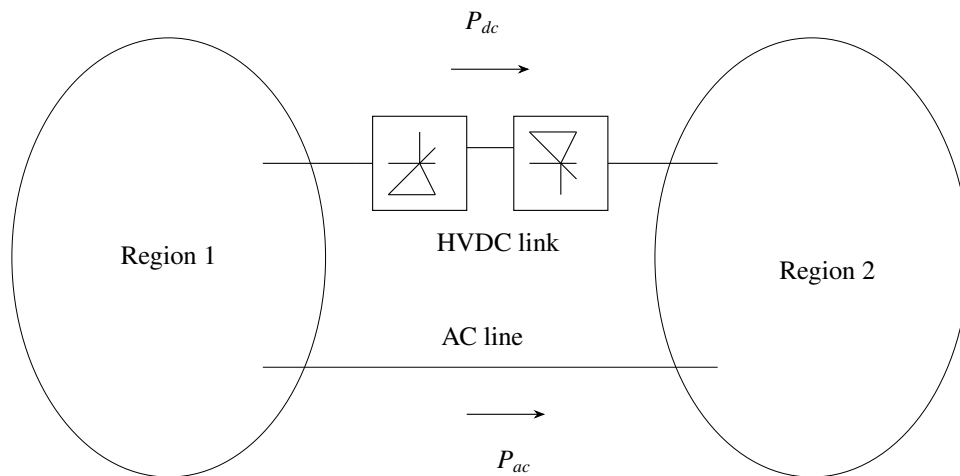

(A) Both regions need not have the same frequency

(B) The total power flow between the regions ($P_{ac} + P_{dc}$) can be changed by controlling the HVDC converters alone

(C) The power sharing between the ac line and the HVDC link can be changed by controlling the HVDC converters alone.

(D) The direction of power flow in the HVDC link (P) cannot be reversed.

6. Consider a bundled conductor of an overhead line, consisting of three identical sub-conductors placed at the corners of an equilateral triangle as shown in the figure. If we neglect the charges on the other phase conductors and ground, and assume that spacing between sub-conductors is much larger than their radius, the maximum electric field intensity is experienced at (GATE EE 2007)
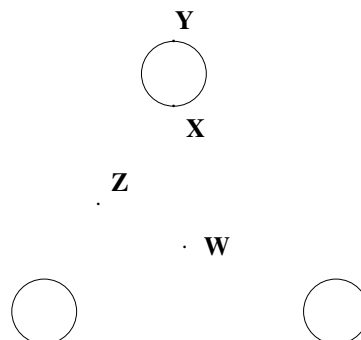
(a) Point X   (b) Point Y   (c) Point Z   (d) Point W

7. The circuit shown in the figure is   (GATE EE 2007)
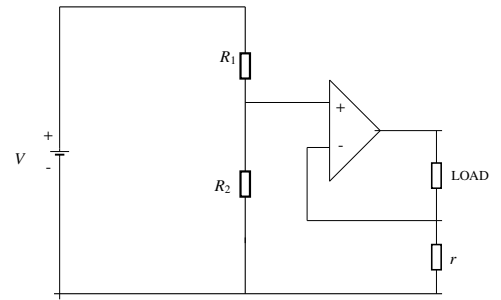
(a) a voltage source with voltage

$$\frac{rV}{R_1 \parallel R_2}$$

(b) a voltage source with voltage

$$\frac{r \parallel R_2}{R_1} V$$

(c) a current source with current

$$\frac{r \parallel R_2}{R_1 + R_2} \cdot \frac{V}{r}$$

(d) a current source with current

$$\frac{R_2}{R_1 + R_2} \cdot \frac{V}{r}$$
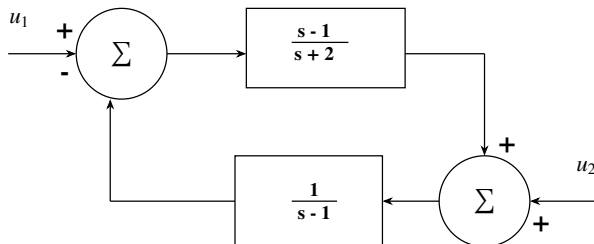
8. The system shown in the figure is   (GATE EE 2007)

(a) stable

(b) unstable

(c) conditionally stable

(d) stable for input u_1, but unstable for input u_2

9. Let a signal $a_1 \sin(\omega_1 t + \phi_1)$ be applied to a stable linear time-invariant system. Let the corresponding steady state output be represented as $a_2 F(\omega_1 t + \phi_2)$. Then which of the following statements is true?
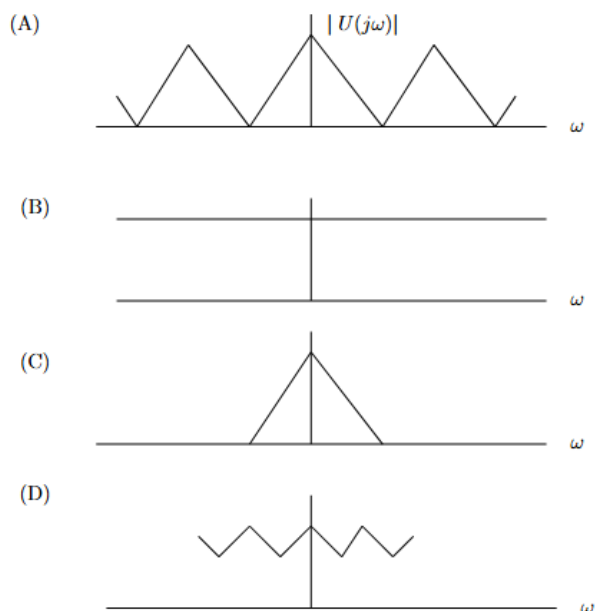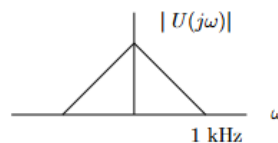(GATE EE 2007)

(a) F is not necessarily a "sine" or "cosine" function but must be periodic with $\omega_1 = \omega_2$

(b) F must be a "sine" or "cosine" function with $a_1 = a_2$

(c) F must be a "sine" function with $\omega_1 = \omega_2$ and $\phi_1 = \phi_2$

(d) F must be a "sine" or "cosine" function with $\omega_1 = \omega_2$

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10. The frequency spectrum of a signal is shown in the figure. If this signal is ideally sampled at intervals of 1 ms, then the frequency spectrum of the sampled signal will be   (GATE EE 2007)

11. Divergence of the vector field   (GATE EE 2007)

$$\mathbf{V}(x, y, z) = -(x\cos xy + y)\mathbf{i} + (y \cos xy)\mathbf{j} + (\sin z^2 + x^2 + y^2)\mathbf{k} \, is \tag{1}$$

(a) $2z \cos z^2$ (c) $x \sin xy - \cos z$

(b) $\sin xy + 2z \cos z^2$ (d) none of these

12. $\mathbf{x} = [x_1 \; x_2 \; \ldots \; x_n]^T$ is an n-tuple nonzero vector. The $n \times n$ matrix $V = \mathbf{x}\mathbf{x}^T$ (GATE EE 2007)

(a) has rank zero   (c) is orthogonal

(b) has rank 1   (d) has rank n

13. A single-phase fully controlled thyristor bridge ac-dc converter is operating at a firing angle of 25° and an overlap angle of 10° with constant dc output current of 20 A. The fundamental power factor (displacement factor) at input ac mains is   (GATE EE 2007)

(a) 0.78   (c) 0.866

(b) 0.827   (d) 0.9

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14. A three-phase, fully-controlled thyristor bridge converter is used as line commutated inverter to feed 50 kW power at 420 V dc to a three-phase, 415 V (line), 50 Hz ac mains. Consider dc link current to be constant. The rms current of the thyristor is (GATE EE 2007)

   (a) 119.05 A   (b) 79.37 A   (c) 68.73 A   (d) 39.68 A

15. In a transformer, zero voltage regulation at full load is (GATE EE 2007)

   (a) not possible
   (b) possible at unity power factor load
   (c) possible at leading power factor load
   (d) possible at lagging power factor load

16. The dc motor, which can provide zero speed regulation at full load without any controller, is (GATE EE 2007)

   (a) series   (b) shunt   (c) cumulative compound   (d) differential compound

17. The probes of a non-isolated, two-channel oscilloscope are clipped to points A, B and C in the circuit of the adjacent figure. V_{in} is a square wave of a suitable low frequency.
The display on Ch_1 and Ch_2 are as shown on the right. Then the "Signal" and "Ground" probes S_1, G_1 and S_2, G_2 of Ch_1 and Ch_2 respectively are connected to points: (GATE EE 2007)
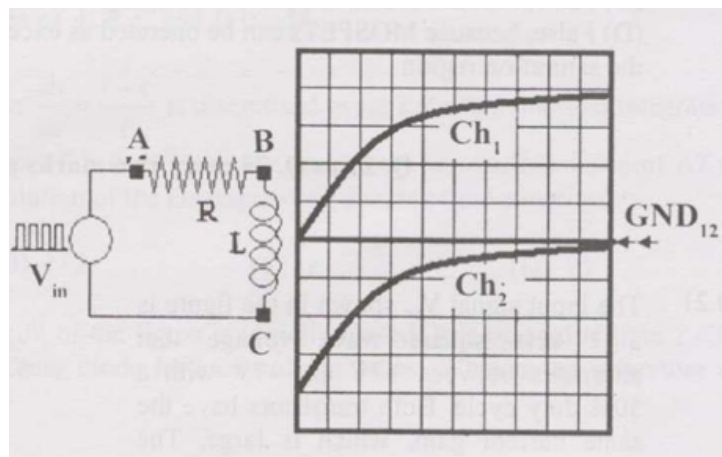
   (a) A,B,C,A   (b) A,B,C,B   (c) C,B,A,B   (d) B,A,B,C

18. A single phase full-wave half-controlled bridge converter feeds an inductive load. The two SCRs in the converter are connected to a common DC bus. The converter has to have a freewheeling diode (GATE EE 2007)

   (a) because the converter inherently does not provide for free-wheeling

   (b) because the converter does not provide for free-wheeling for high values of triggering angles

   (c) or else the free-wheeling action of the converter will cause shorting of the AC supply

   (d) or else if a gate pulse to one of the SCRs is missed, it will subsequently cause a high load current in the other SCR

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19. The electromagnetic torque T_e of a drive, and its connected load torque T_L are as shown below. Out of the operating points A, B, C and D, the stable ones are: (GATE EE 2007) [1
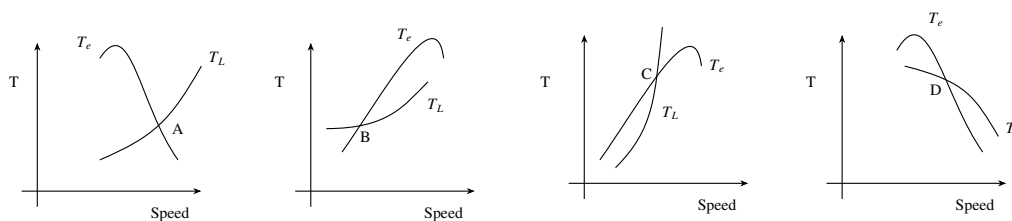
(a) A, C, D (b) B, C (c) A, D (d) B, C, D

20. "Six MOSFETs connected in a bridge configuration (having no other power device), MUST be operated as a Voltage Source Inverter (VSI)". This statement is: (GATE EE 2007)

   (a) True, because being majority carrier devices, MOSFETs are voltage driven

   (b) True, because MOSFETs have inherently anti-parallel diodes

   (c) False, because it can be operated both as Current Source Inverter (CSI) or a VSI

   (d) False, because MOSFETs can be operated as excellent constant current sources in the saturation region

Q.21 to Q.75 carry two marks each.

21. The input signal V_{in} shown in the figure is a 1 kHz square wave voltage that alternates between +7V and -7V with a 50% duty cycle. Both transistors have the same current gain, which is large. The circuit delivers power to the load resistor R_L. What is the efficiency of this circuit for the given input? Choose the closest answer. (GATE EE 2007)
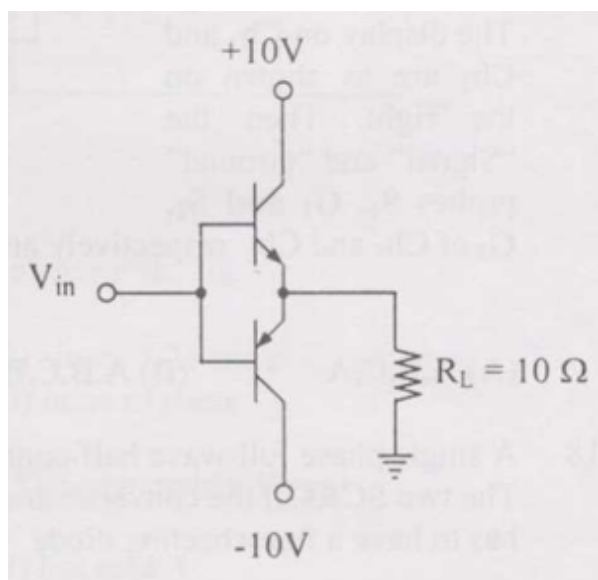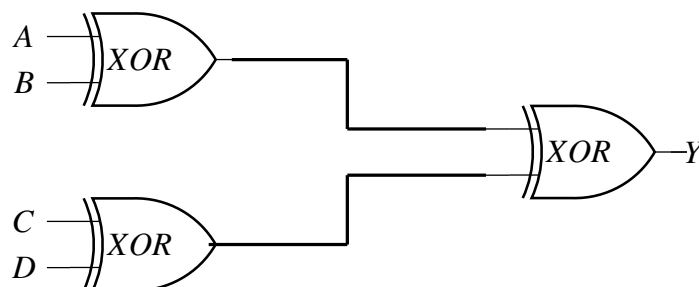
(a) 46% (b) 55% (c) 63% (d) 92%

22. A, B, C and D are input bits, and Y is the output bit in the XOR gate circuit of the figure below. Which of the following statements about the sum S of A, B, C, D and Y is correct? (GATE EE 2007)

(a) S is always either zero or odd

(b) S is always either zero or even

(c) s = 1 only if the sum of A, B, C and D is even

(d) s = 1 only if the sum of A, B, C and D is odd

23. The differential equation $\frac{dx}{dy} = \frac{1-x}{\tau}$ is discretised using Euler's numerical integration method with a time step triangle $\Delta T > 0$. What is the maximum permissible value of ΔT to ensure stability of the solution of the corresponding discrete time equation? (GATE EE 2007)

(a) 1

(b) $\frac{\tau}{2}$

(c) τ

(d) 2τ

24. The switch S in the circuit of the figure is initially closed. It is opened at time t=0. You may neglect the Zener diode forward voltage drops. What is the behaviour of V_{out} for t > 0? (GATE EE 2007)
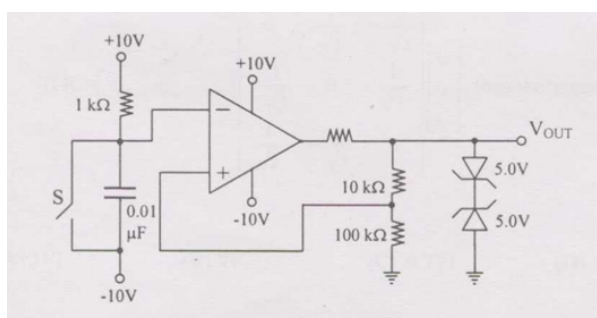

(a) It makes a transition from -5 to +5 V at t= 12.98μs

(b) It makes a transition from -5 to +5 V at t=2.57μs

(c) It makes a transition from +5 to -5 V at t=12.98μs

(d) It makes a transition from +5 to -5 V at t=2.57μs

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25. A solid sphere made of insulating material has a radius R and has a total charge Q distributed uniformly in its volume. What is the magnitude of the electric field intensity, E, at a distance r$(0 < r < R)$ inside the sphere? (GATE EE 2007)

(a) $\frac{1}{4\pi\epsilon}\frac{Qr}{R^3}$

(b) $\frac{3}{4\pi\epsilon}\frac{Qr}{R^3}$

(c) $\frac{1}{4\pi\epsilon}\frac{Q}{r^3}$

(d) $\frac{1}{4\pi\epsilon}\frac{QR}{r^3}$

26. The figure below shows a three phase self-commutated voltage source converter connected to a power system. The converter's de bus capacitor is marked as C in the figure. The circuit is initially operating in steady state with $\delta = 0$ and the capacitor de voltage is equal to V_{dc0}. You may neglect all losses and harmonics. What action should be taken to increase the capacitor de voltage slowly to a new steady state value? (GATE EE 2007)
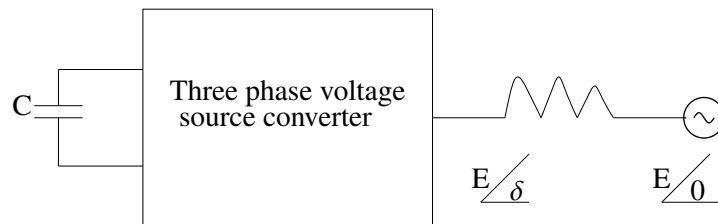
(a) Make δ positive and maintain it at a positive value

(b) Make δ positive and return it to its original value

(c) Make δ negative and maintain it at a negative value

(d) Make δ negative and return it to its original value

27. The total reactance and total susceptance of a lossless overhead EHV line, operating at 50 Hz, are given by 0.045 pu and 1.2 pu respectively. If the velocity of wave propagation is 3×10^5 km/s, then the approximate length of the line is (GATE EE 2007)

(a) 122 km

(b) 172km

(c) 222km

(d) 272km

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28.   Consider the protection system shown in the figure below. The circuit breakers, numbered from 1 to 7 are of identical type. A single line to ground fault with zero fault impedance occurs at the midpoint of the line (at point F), but circuit breaker 4 fails to operate ("stuck breaker"). If the relays are coordinated correctly, a valid sequence of circuit breaker operations is   (GATE EE 2007)
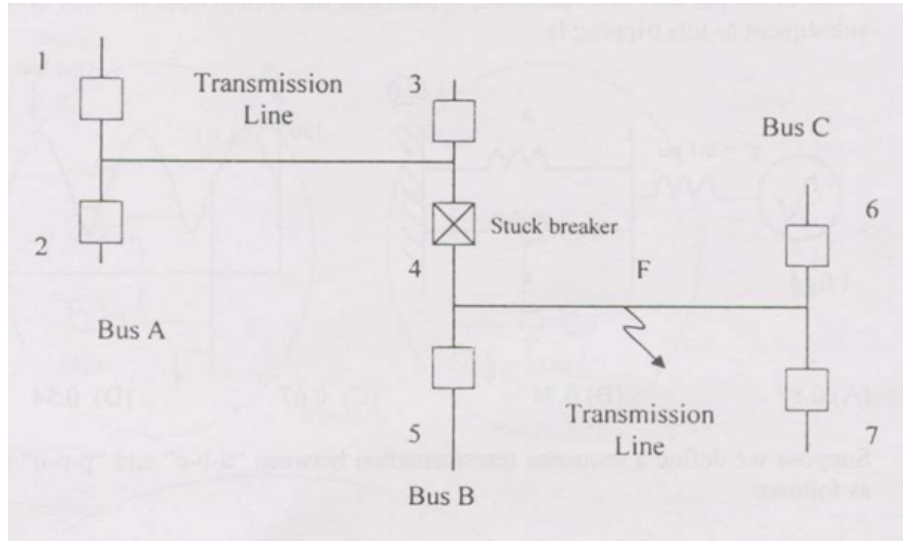
(a) 1, 2, 6, 7, 3, 5   (b) 1, 2, 5, 6, 7, 3   (c) 5, 6, 7, 3, 1, 2   (d) 5, 1, 2, 3, 6, 7

29.   A three phase balanced star connected voltage source with frequency ω rad/s is connected to a star connected balanced load which is purely inductive. The instantaneous line currents and phase to neutral voltages are denoted by (i_a, i_b, i_c) and (v_{an}, v_{bn}, v_{cn}) respectively, and their rms values are denoted by V and I.   (GATE EE 2007)

$$ \text{If } R = \begin{pmatrix} v_{an} & v_{bn} & v_{cn} \end{pmatrix} \begin{pmatrix} 0 & \frac{1}{\sqrt{3}} & -\frac{1}{\sqrt{3}} \\ -\frac{1}{\sqrt{3}} & 0 & \frac{1}{\sqrt{3}} \\ \frac{1}{\sqrt{3}} & -\frac{1}{\sqrt{3}} & 0 \end{pmatrix} \begin{pmatrix} i_a \\ i_b \\ i_c \end{pmatrix}, \text{ then the magnitude of } R \text{ is} $$

(a) $3VI$ (b) VI (c) $0.7VI$ (d) 0

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30. Consider a synchronous generator connected to an infinite bus by two identical parallel transmission lines. The transient reactance x' of the generator is 0.1 pu and the mechanical power input to it is constant at 1.0 pu. Due to some previous disturbance, the rotor angle (δ) is undergoing an undamped oscillation, with the maximum value of $\delta(t)$ equal to 130°. One of the parallel lines trips due to relay maloperation at an instant when $\delta(t) = 130°$ as shown in the figure. The maximum value of the per unit line reactance, x, such that the system does not lose synchronism subsequent to this tripping is   (GATE EE 2007)
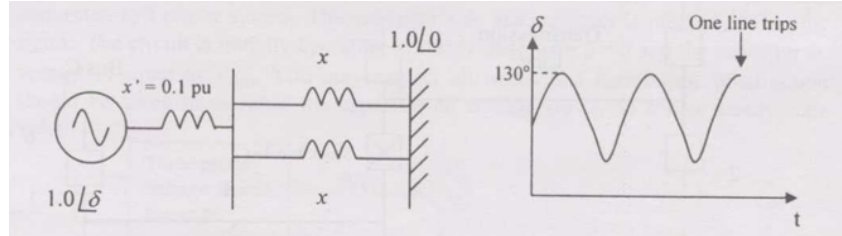
(a) 0.87   (b) 0.74   (c) 0.67   (d) 0.54

31. Suppose we define a sequence transformation between "a-b-c" and "p-n-o" variables as follows:

$$\begin{pmatrix} f_a \\ f_b \\ f_c \end{pmatrix} = k \begin{pmatrix} 1 & 1 & 1 \\ \alpha^2 & \alpha & 1 \\ \alpha & \alpha^2 & 1 \end{pmatrix} \begin{pmatrix} f_p \\ f_n \\ f_o \end{pmatrix} \quad \text{where } \alpha = e^{j\frac{2\pi}{3}} \text{ and } k \text{ is a constant.}$$

Now, if it is given that:

$$\begin{pmatrix} V_p \\ V_n \\ V_o \end{pmatrix} = \begin{pmatrix} 0.5 & 0 & 0 \\ 0 & 0.5 & 0 \\ 0 & 0 & 2.0 \end{pmatrix} \begin{pmatrix} i_p \\ i_n \\ i_o \end{pmatrix} \quad \text{and} \quad \begin{pmatrix} V_a \\ V_b \\ V_c \end{pmatrix} = \begin{pmatrix} i_a \\ i_b \\ i_c \end{pmatrix} \text{ then,} \qquad \text{(GATE EE 2007)}$$

(a) $Z = \begin{pmatrix} 1.0 & 0.5 & 0.75 \\ 0.75 & 1.0 & 0.5 \\ 0.5 & 0.75 & 1.0 \end{pmatrix}$

(b) $Z = \begin{pmatrix} 1.0 & 0.5 & 0.5 \\ 0.5 & 1.0 & 0.5 \\ 0.5 & 0.5 & 1.0 \end{pmatrix}$

(c) $Z = 3k^2 \begin{pmatrix} 1.0 & 0.75 & 0.5 \\ 0.5 & 1.0 & 0.75 \\ 0.75 & 0.5 & 1.0 \end{pmatrix}$

(d) $Z = \dfrac{k^2}{3} \begin{pmatrix} 1.0 & -0.5 & -0.5 \\ -0.5 & 1.0 & -0.5 \\ -0.5 & -0.5 & 1.0 \end{pmatrix}$

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32. Consider the two power systems shown in figure A below, which are initially not interconnected, and are operating in steady state at the same frequency. Separate loadflow solutions are computed individually for the two systems, corresponding to this scenario. The bus voltage phasors so obtained are indicated on figure A. These two isolated systems are now interconnected by a short transmission line as shown in figure B, and it is found that $P_1 = P_2 = Q_1 = Q_2 = 0$.   (GATE EE 2007)

Figure A

Figure B
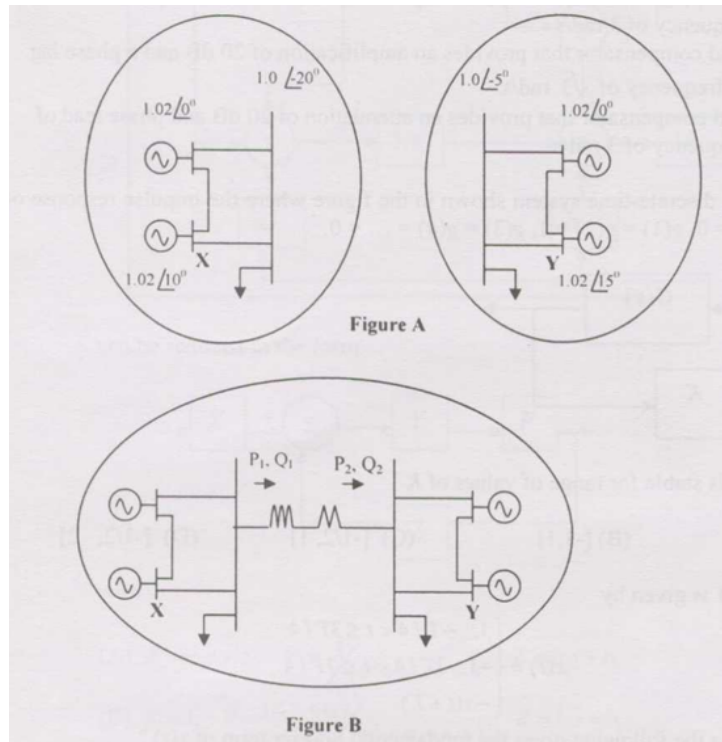
The bus voltage phase angular difference between generator bus X and generator bus Y after the interconnection is

(a) 10^0 (b) 25^0 (c) -30^0 (d) 30^0

33. The octal equivalent of the HEX number **AB.CD** is   (GATE EE 2007)

(a) 253.314   (b) 253.632   (c) 526.314   (d) 526.632

34. If $x = \mathrm{Re}\, G(j\omega)$, and $y = \mathrm{Im}\, G(j\omega)$ then for $\omega \to 0^+$, the Nyquist plot for

$G(s) = \dfrac{1}{s(s+1)(s+2)}$ becomes asymptotic to the line   (GATE EE 2007)

(a) $x = 0$ (b) $x = -3/4$ (c) $x = y - 1/6$ (d) $x = y/\sqrt{3}$

35. The system $\dfrac{900}{s(s+1)(s+9)}$ is to be compensated such that its gain-crossover frequency becomes same as its uncompensated phase-crossover frequency and provides a 45^0 phase margin. To achieve this, one may use   (GATE EE 2007)

   (a) a lag compensator that provides an attenuation of 20 dB and a phase lag of 45^0 at the frequency of $3\sqrt{3}$ rad/s

   (b) a lead compensator that provides an amplification of 20 dB and a phase lead of 45^0 at the frequency of 3 rad/s

   (c) a lag-lead compensator that provides an amplification of 20 dB and a phase lag of 45^0 at the frequency of $\sqrt{3}$ rad/s

   (d) a lag-lead compensator that provides an attenuation of 20 dB and phase lead of 45^0 at the frequency of 3 rad/s

36. Consider the discrete-time system shown in the figure where the impulse response $G(z)$ is $g(0) = 0$, $g(1) = g(2) = 1$, $g(3) = g(4) = \cdots = 0$.   (GATE EE 2007)
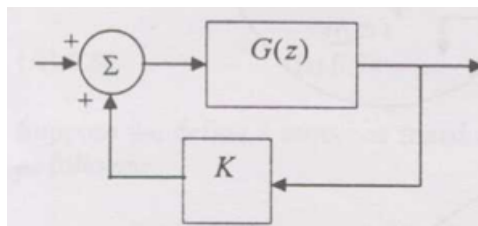
This system is stable for range of values of K

   (a) $[-1, 1/2]$ (b) $[-1, 1]$=   (c) $[-1/2, 1]$ (d) $[-1/2, 2]$

37. A signal $x(t)$ is given by $x(t) = \begin{cases} 1, & -\dfrac{T}{4} < t \le \dfrac{3T}{4} \\[2mm] -1, & \dfrac{3T}{4} < t \le \dfrac{7T}{4} \\[2mm] -x(t+T) \end{cases}$

Which among the following gives the fundamental Fourier term of $x(t)$?   (GATE EE 2007)

   (a) $\dfrac{4}{\pi}\cos\left(\dfrac{\pi t}{T} - \dfrac{\pi}{4}\right)$ (c) $\dfrac{4}{\pi}\sin\left(\dfrac{\pi t}{T} - \dfrac{\pi}{4}\right)$

   (b) $\dfrac{\pi}{4}\cos\left(\dfrac{\pi t}{2T} + \dfrac{\pi}{4}\right)$ (d) $\dfrac{\pi}{4}\sin\left(\dfrac{\pi t}{2T} + \dfrac{\pi}{4}\right)$

38. If the loop gain K of a negative feedback system having a loop transfer function $\dfrac{K(s+3)}{(s+8)^2}$ is to be adjusted to induce a sustained oscillation then   (GATE EE 2007)

   (a) The frequency of this oscillation must be $\dfrac{4}{\sqrt{3}}$ rad/s

   (b) The frequency of this oscillation must be 4 rad/s

   (c) The frequency of this oscillation must be 4 or $\dfrac{4}{\sqrt{3}}$ rad/s

   (d) such a K does not exist

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39. The system shown in figure below   (GATE EE 2007)

can be reduced to the form
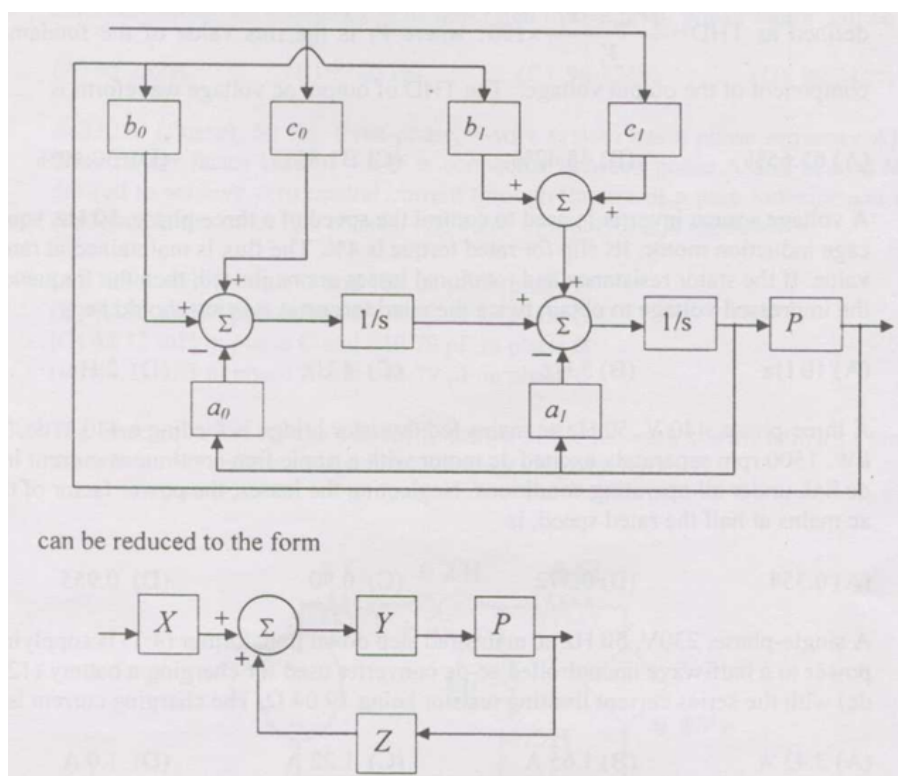
with

(a) $X = c_0 s + c_1$, $Y = \dfrac{1}{s^2 + a_0 s + a_1}$, $Z = b_0 s + b_1$

(b) $X = 1$, $Y = \dfrac{c_0 s + c_1}{s^2 + a_0 s + a_1}$, $Z = b_0 s + b_1$

(c) $X = c_1 s + c_0$, $Y = \dfrac{b_1 s + b_0}{s^2 + a_1 s + a_0}$, $Z = 1$

(d) $X = c_1 s + c_0$, $Y = \dfrac{1}{s^2 + a_1 s + a_0}$, $Z = b_1 s + b_0$

40.   The value of $\oint_C \dfrac{dz}{1+z^2}$ where C is the contour $|z - i/2| = 1$ is   (GATE EE 2007)

(a) $2\pi i$ (b) π (c) $\tan^{-1} z$ (d) $\pi i \tan^{-1} z$

41. A single-phase voltage source inverter is controlled in a single pulse-width modulated mode with a pulse width of 150° in each half cycle. Total harmonic distortion is defined as THD = $\frac{\sqrt{V_{rms}^2 - V_1^2}}{V_1} \times 100$, where V_1 is the rms value of the fundamental component of the output voltage. The THD of output ac voltage waveform is (GATE EE 2007)

(a) 65.65% (b) 48.42% (c) 31.83% (d) 30.49%

42. A voltage source inverter is used to control the speed of a three-phase, 50 Hz, squirrel cage induction motor. Its slip for rated torque is 4%. The flux is maintained at rated value. If the stator resistance and rotational losses are neglected, then the frequency of the impressed voltage to obtain twice the rated torque at starting should be (GATE EE 2007)

(a) 10 Hz (b) 5 Hz (c) 4 Hz (d) 2 Hz

43. A three-phase, 440 V, 50 Hz ac mains fed thyristor bridge is feeding a 440 V dc, 15 kW, 1500 rpm separately excited dc motor with a ripple free continuous current in the dc link under all operating conditions. Neglecting the losses, the power factor of the ac mains at half the rated speed, is (GATE EE 2007)

(a) 0.354 (b) 0.372 (c) 0.90 (d) 0.955

44. A single-phase, 230V, 50 Hz ac mains fed step down transformer (4:1) is supplying power to a half-wave uncontrolled ac-dc converter used for charging a battery (12 V dc) with the series current limiting resistor being 19.04 Ω. The charging current is (GATE EE 2007)

(a) 2.43 A (b) 1.65 A (c) 1.22 A (d) 1.0 A

45. A three-phase synchronous motor connected to ac mains is running at full load and unity power factor. If its shaft load is reduced by half, with field current held constant, its new power factor will be (GATE EE 2007)

(a) unity (c) lagging
(b) leading (d) dependent on machine parameters

46. A 100 kVA, 415 V (line), star-connected synchronous machine generates rated open circuit voltage of 415 V at a field current of 15 A. The short circuit armature current at a field current of 10 A is equal to the rated armature current. The per unit saturated synchronous reactance is (GATE EE 2007)

(a) 1.731 (b) 1.5 (c) 0.666 (d) 0.577

47. A three-phase, three-stack, variable reluctance step motor has 20 poles on each rotor and stator stack. The step angle of this step motor is (GATE EE 2007)

(a) 3° (b) 6° (c) 9° (d) 18°

48. A single-phase 50 kVA, 250V/500V two winding transformer has an efficiency of 95% at full load, unity power factor. If it is reconfigured as a 500V/750V autotransformer, its efficiency at its new rated load at unity power factor will be (GATE EE 2007)

   (a) 95.752%   (b) 97.851%   (c) 98.276%   (d) 99.241%

49. A 230 V (Phase), 50 Hz, three-phase, 4-wire system has a phase sequence ABC. A unity power-factor load of 4 kW is connected between phase A and neutral N. It is desired to achieve zero neutral current through the use of a pure inductor and a pure capacitor in the other two phases. The value of inductor and capacitor is (GATE EE 2007)

   (a) 72.95 mH in phase C and 139.02 μF in phase B

   (b) 72.95 mH in phase B and 139.02 μF in phase C

   (c) 42.12 mH in phase C and 240.79 μF in phase B

   (d) 42.12 mH in phase B and 240.79 μF in phase C

50. The state equation for the current I_1 shown in the network shown below in terms of the voltage V_x and the independent source V, is given by (GATE EE 2007)
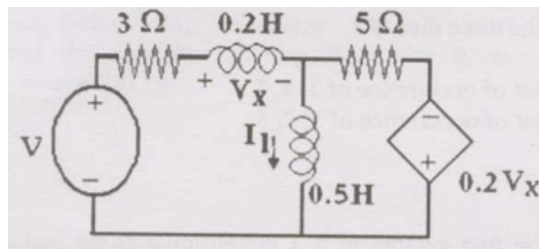
   (a) $\dfrac{dI_1}{dt} = -1.4V_x - 3.75I_1 + \dfrac{5}{4}V$

   (b) $\dfrac{dI_1}{dt} = 1.4V_x - 3.75I_1 - \dfrac{5}{4}V$

   (c) $\dfrac{dI_1}{dt} = -1.4V_x + 3.75I_1 + \dfrac{5}{4}V$

   (d) $\dfrac{dI_1}{dt} = -1.4V_x + 3.75I_1 - \dfrac{5}{4}V$

51. If $u(t)$, $r(t)$ denote the unit step and unit ramp functions respectively and $u(t) * r(t)$ their convolution, then the function $u(t + 1) * r(t - 2)$ is given by (GATE EE 2007)

   (a) $(1/2)(t - 1)(t - 2)$ (b) $(1/2)(t - 1)(t - 2)$ (c) $(1/2)(t - 1)^2 u(t - 1)$ (d) none of the above

52. The integral $\frac{1}{2\pi} \int_0^{2\pi} \sin(t - \tau) \cos \tau \, d\tau$ equals (GATE EE 2007)

   (a) $\sin t \cos t$ (b) 0 (c) $(1/2) \cos t$ (d) $(1/2) \sin t$

53. $X(z) = 1 - 3z^{-1}$, $\quad Y(z) = 1 + 2z^{-2}$ are Z-transforms of two signals $x[n]$, $y[n]$ respectively. A linear time invariant system has the impulse response $h[n]$ defined by these two signals as $h[n] = x[n-1] * y[n]$ where $*$ denotes discrete time convolution. Then the output of the system for the input $\delta[n-1]$ (GATE EE 2007)

    (a) has Z-transform $z^{-1}X(z)Y(z)$

    (b) equals $\delta[n-2] - 3\delta[n-3] + 2\delta[n-4] - 6\delta[n-5]$

    (c) has Z-transform $1 - 3z^{-1} + 2z^{-2} - 6z^{-3}$

    (d) does not satisfy any of the above three.

54.   A loaded dice has following probability distribution of occurrences

Dice value	1	2	3	4	5	6
Probability	1/4	1/8	1/8	1/8	1/8	1/4

  If three identical dice as the above are thrown, the probability of occurrence of values 1, 5 and 6 on the three dice is   (GATE EE 2007)

    (a) same as that of occurrence of 3, 4, 5

    (b) same as that of occurrence of 1, 2, 5

    (c) 1/128

    (d) 5/8

55.   Let $\mathbf{x}$ and $\mathbf{y}$ be two vectors in a 3 dimensional space and $\langle x, y \rangle$ denote their dot product.
Then the determinant $\det \begin{pmatrix} \langle x, x \rangle & \langle x, y \rangle \\ \langle y, x \rangle & \langle y, y \rangle \end{pmatrix}$ (GATE EE 2007)

    (a) is zero when x and y are linearly independent

    (b) is positive when x and y are linearly independent

    (c) is non-zero for all non-zero x and y

    (d) is zero only when either x or y is zero

56.   The linear operation $L(\mathbf{x})$ is defined by the cross product $L(\mathbf{x}) = \mathbf{b} \times \mathbf{x}$, where $\mathbf{b} = [0\ 1\ 0]^T$ and $\mathbf{x} = [x_1\ x_2\ x_3]^T$ are three dimensional vectors. The 3×3 matrix M of this operation satisfies $L(\mathbf{x}) = M \begin{pmatrix} x_1 \\ x_2 \\ x_3 \end{pmatrix}$
Then the eigenvalues of M are   (GATE EE 2007)

    (a) $0, +1, -1$

    (b) $1, -1, 1$

    (c) $i, -i, 1$

    (d) $i, -i, 0$

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57. In the figure, transformer T_1 has two secondaries, all three windings having the same number of turns and with polarities as indicated. One secondary is shorted by a $10\,\Omega$ resistor R, and the other by a $15\,\mu F$ capacitor. The switch SW is opened ($t = 0$) when the capacitor is charged to 5V with the left plate as positive. At $t = 0+$ the voltage V_p and current I_R are   (GATE EE 2007)
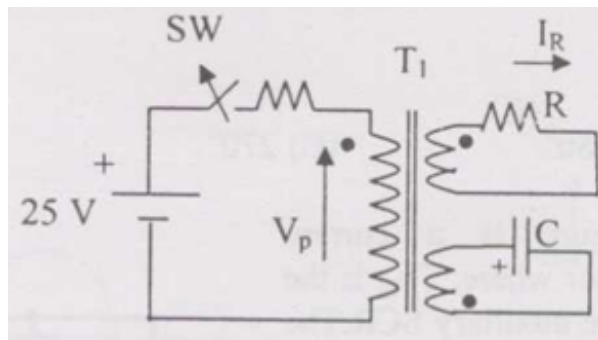
(a) -25V,0.0

(c) 5.0V, 0.5

(b) very large voltage, very large current

(d) -5.0V ,-0.5 A

58. IC 555 in the adjacent figure is configured as an astable multivibrator. It is enabled to oscillate at $t = 0$ by applying a high input to pin 4. The pin description is: 1 and 8 – supply; 2–trigger; 4–reset; 6–threshold; 7–discharge. The waveform appearing across the capacitor starting from $t = 0$, as observed on a storage CRO is   (GATE EE 2007)

(A)

(B)

(C)

(D)

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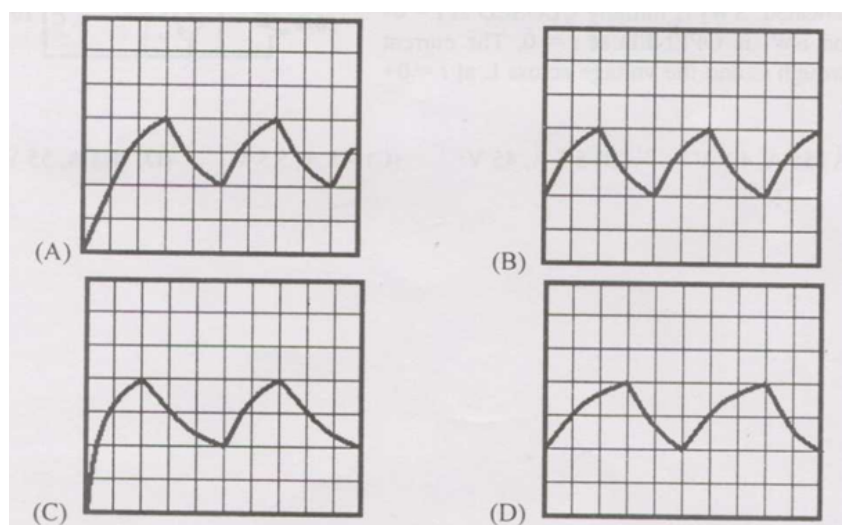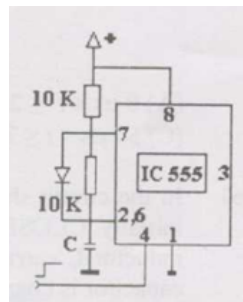
59. In the circuit of adjacent figure the diode connects the ac source to a pure inductance L.
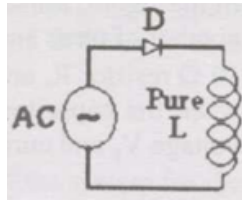
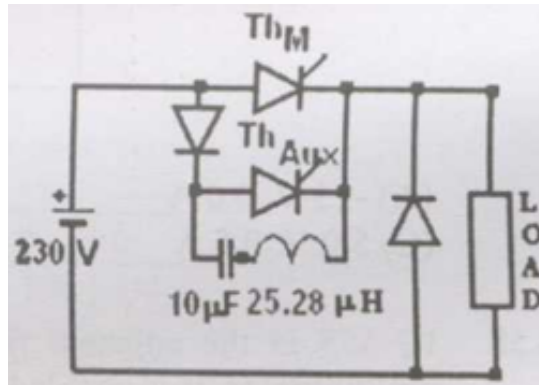The diode conducts for   (GATE EE 2007)

(a) The angle is 90°   (b) 180°   (c) 270°   (d) 360°

60. The circuit in the figure is a current commutated dc–dc chopper where, Th_M is the main SCR and Th_{AUX} is the auxiliary SCR. The load current is constant at 10 A. Th_M is ON. Th_{AUX} is triggered at $t = 0$. Th_M is turned OFF between   (GATE EE 2007)

(A) $0\,\mu s < t \le 25\,\mu s$ (C) $50\,\mu s < t \le 75\,\mu s$
(B) $25\,\mu s < t \le 50\,\mu s$ (D) $75\,\mu s < t \le 100\,\mu s$

61. In the circuit shown in figure, switch SW_1 is initially CLOSED and SW_2 is OPEN. The inductor L carries a current of 10 A and the capacitor is charged to 10 V with polarities as indicated. SW_2 is initially CLOSED at $t = 0-$ and SW_1 is OPENED at $t = 0$. The current through C and the voltage across L at $t = 0+$ is   (GATE EE 2007)
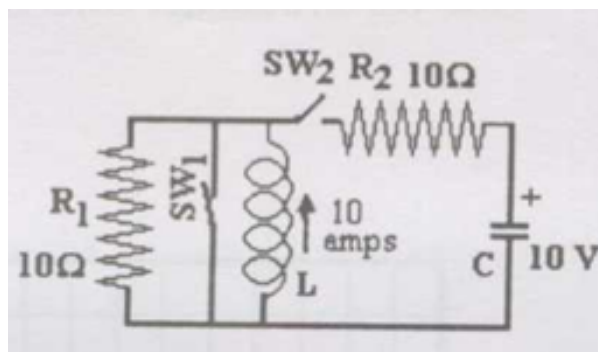
(a) 55A, 4.5V   (b) 5.5A, 45V   (c) 45A, 5.5V   (d) 4.5A, 55V

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62.   The R-L-C series circuit shown is supplied from a variable frequency voltage source. The admittance-locus of the R-L-C network at terminals AB for increasing frequency ω is   (GATE EE 2007)

(A)

(B)

(C)

(D)
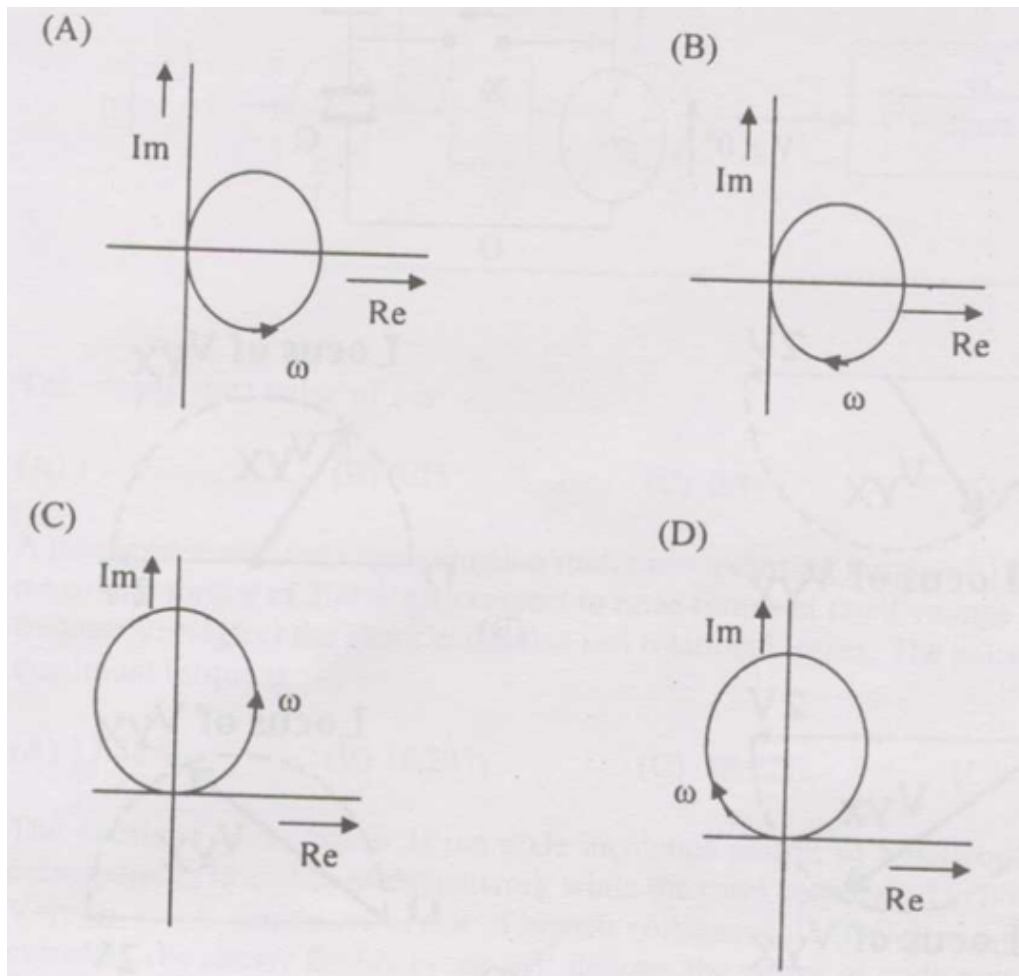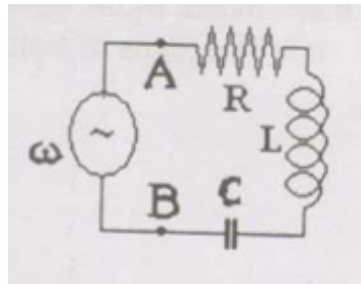
63. In the figure given below all phasors are with reference to the potential at point "O". The locus of voltage phasor V_{YX} as R is varied from zero to infinity is shown by (GATE EE 2007)

(A)

(B)

(C)

(D)
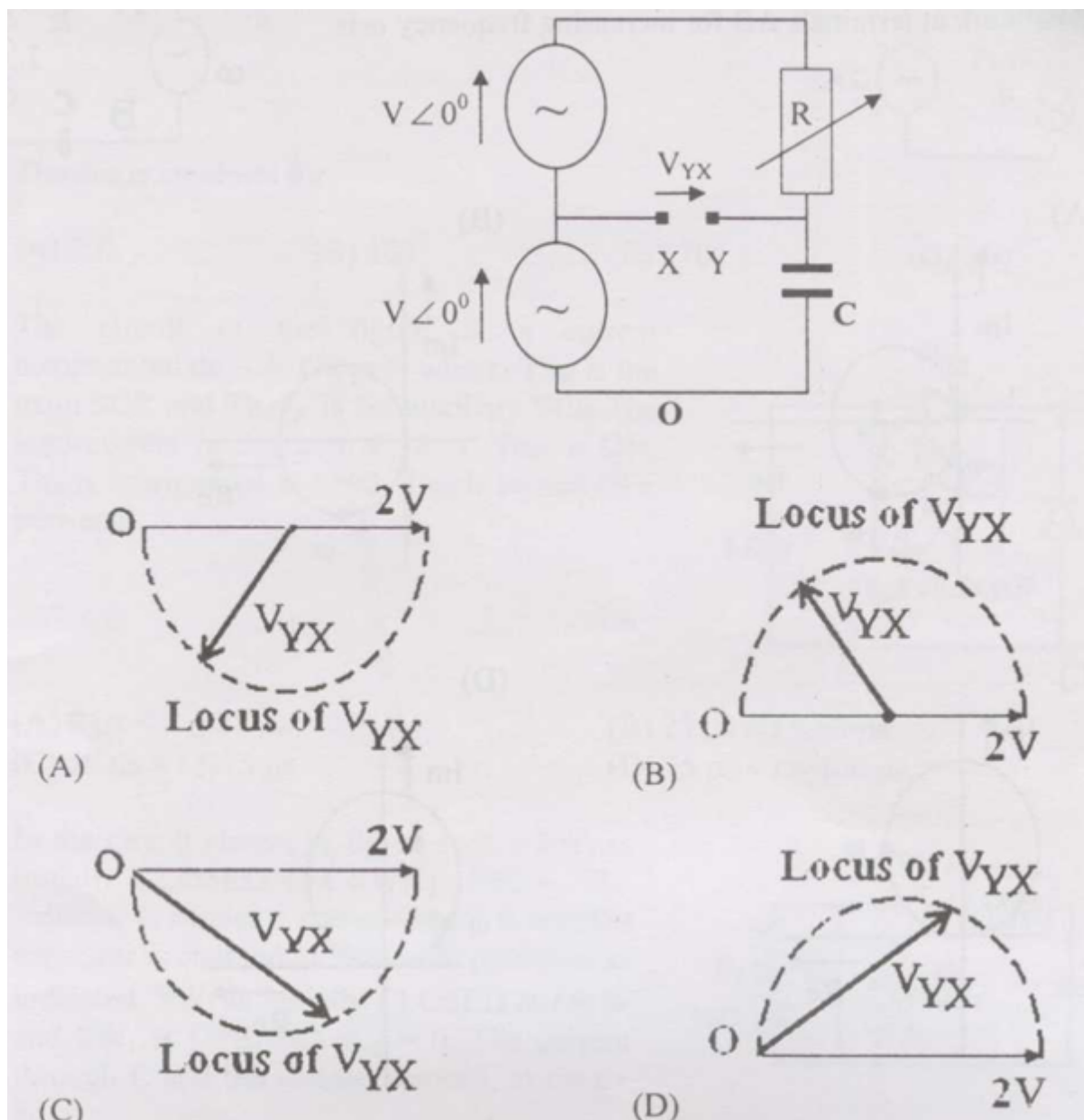
64. A 3V dc supply with an internal resistance of 2 Ω supplies a passive non-linear resistance characterized by the relation $V_{NL} = I_{NL}^2$. The power dissipated in the non-linear resistance is (GATE EE 2007)

(a) 1.0   (b) 1.5   (c) 2.5   (d) 3.0

65. Consider the feedback control system shown below which is subjected to a unit step input. The system is stable and has the following parameters $k_p = 4$, $k_i = 10$, $\omega = 500$ and $\xi = 0.7$.
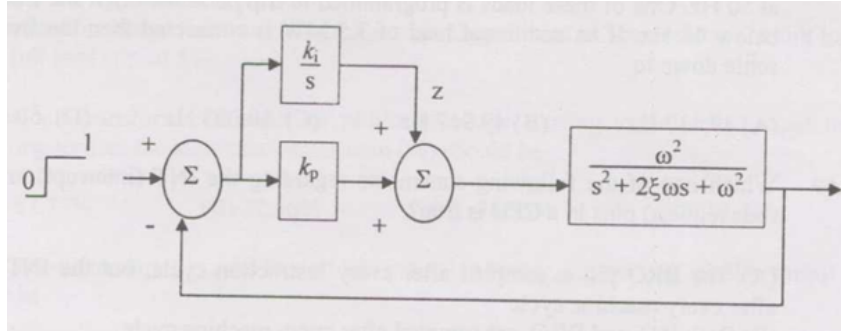
The steady state value of is   (GATE EE 2007)

(a) 1   (b) 0.25   (c) 0.1   (d) 0

66. A three-phase squirrel cage induction motor has a starting torque of 150% and a maximum torque of 300% with respect to rated torque at rated voltage and rated frequency. Neglect the stator resistance and rotational losses. The value of slip for maximum torque is   (GATE EE 2007)

(a) 13.48%   (b) 16.24%   (c) 18.92%   (d) 26.79%

67. The matrix A given below is the node incidence matrix of a network. The columns correspond to branches of the network while the rows correspond to nodes. Let $\mathbf{V} = [v_1\ v_2\ \ldots\ v_6]^T$ denote the vector of branch voltages while $\mathbf{I} = [i_1\ i_2\ \ldots\ i_6]^T$ that of branch currents. The vector $\mathbf{E} = [e_1\ e_2\ e_3\ e_4]^T$ denotes the vector of node voltages relative to a common ground. $A = \begin{pmatrix} 1 & 1 & 1 & 0 & 0 & 0 \\ 0 & -1 & 0 & -1 & 1 & 0 \\ -1 & 0 & 0 & 0 & -1 & -1 \\ 0 & 0 & -1 & 1 & 0 & 1 \end{pmatrix}$ Which of the following statements is true?   (GATE EE 2007)

(A) The equations
$v_1 - v_2 + v_3 = 0$, $v_3 + v_4 - v_5 = 0$
are KVL equations for the network for some loops

(B) The equations
$v_1 - v_3 - v_6 = 0$, $v_4 + v_5 - v_6 = 0$
are KVL equations for the network for some loops

(C) $\mathbf{E} = A\mathbf{V}$

(D) $A\mathbf{V} = 0$ are KVL equations for the network

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68. An isolated 50 Hz synchronous generator is rated at 15 MW which is also the maximum continuous power limit of its prime mover. It is equipped with a speed governor with 5% droop. Initially, the generator is feeding three loads of 4 MW each at 50 Hz. One of these loads is programmed to trip *permanently* if the frequency falls below 48 Hz. If an additional load of 3.5 MW is connected then the frequency will settle down to (GATE EE 2007)

(a) 49.417 Hz   (b) 49.917 Hz   (c) 50.083 Hz   (d) 50.583 Hz

69. Which one of the following statements regarding the INT (interrupt) and the BRQ (bus request) pins in a CPU is true? (GATE EE 2007)

(A) The BRQ pin is sampled after every instruction cycle, but the INT is sampled after every machine cycle

(B) Both INT and BRQ are sampled after every machine cycle

(C) The INT pin is sampled after every instruction cycle, but the BRQ is sampled after every machine cycle

(D) Both INT and BRQ are sampled after every instruction cycle

70. A bridge circuit is shown in the figure below. Which one of the sequences given below is most suitable for balancing the bridge? (GATE EE 2007)
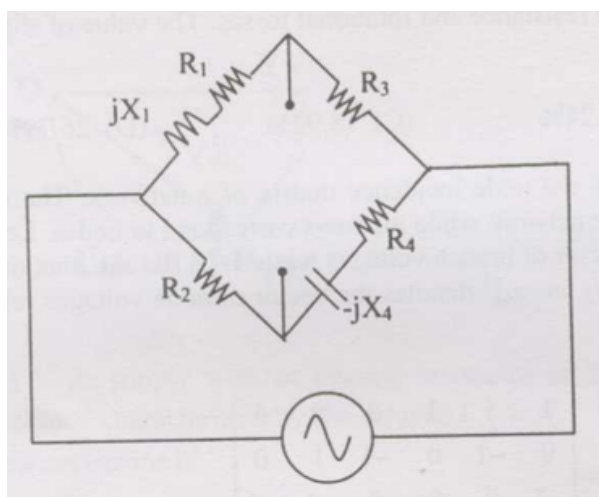
(a) First adjust R_4, and then adjust R_1 (c) First adjust R_2, and then adjust R_4

(b) First adjust R_2, and then adjust R_3 (d) First adjust R_4, and then adjust R_2

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Common Data Questions

Common Data for Questions 71,72,73:

A three phase squirrel cage induction motor has a starting current of seven times the full load current and full load slip of 5%.

71. If an autotransformer is used for reduced voltage starting to provide 1.5 per unit starting torque, the autotransformer ratio (%) should be   (GATE EE 2007)

    (a) 57.77%   (b) 72.56%   (c) 78.25%   (d) 81.33%

72. If a star-delta starter is used to start this induction motor, the per unit starting torque will be   (GATE EE 2007)

    (a) 0.607   (b) 0.816   (c) 1.225   (d) 1.616

73. If a starting torque of 0.5 per unit is required then the per unit starting current should be   (GATE EE 2007)

    (a) 4.65   (b) 3.75   (c) 3.16   (d) 2.13

Common Data for Questions 74, 75:

A 1:1 Pulse Transformer (PT) is used to trigger the SCR in the adjacent figure. The SCR is rated at 1.5 kV, 250 A with I_L = 250 mA, I_H = 150 mA, and I_{Gmax} = 150 mA, I_{Gmin} = 100 mA. The SCR is connected to an inductive load, where L = 150 mH in series with a small resistance and the supply voltage is 200V dc. The forward drops of all transistors/diodes and gate-cathode junction during ON state are 1.0 V.
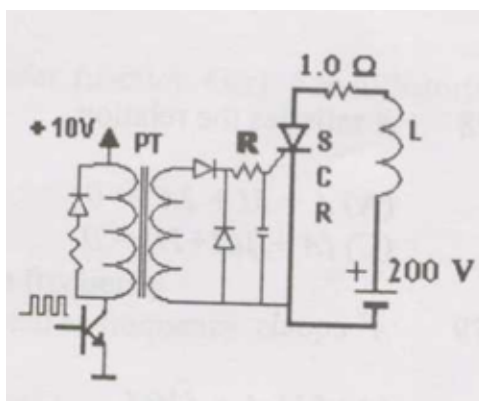
74. The resistance R should be   (GATE EE 2007)

    (a) 4.7 kΩ   (b) 470 Ω   (c) 47 Ω   (d) 4.7 Ω

75. The minimum approximate volt-second rating of the pulse transformer suitable for triggering the SCR should be: (volt–second rating is the maximum of product of the voltage and the width of the pulse that may be applied)   (GATE EE 2007)

    (a) 2000 μV-s   (b) 200 μV-s   (c) 20 μV-s   (d) 2.0 μV-s

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Linked Answer Questions:Q.76 to Q.85 carry two marks each.

Statement for Linked Answer Questions 76 & 77:

An inductor designed with 400 turns coil wound on an iron core of 16 cm^2 cross sectional area and with a cut of an air gap length of 1 mm. The coil is connected to a 230 V, 50 Hz ac supply. Neglect coil resistance, core loss, iron reluctance and leakage inductance. ($\mu_0 = 4\pi \times 10^{-7}$ H/m)

76. The current in the inductor is   (GATE EE 2007)

    (a) 18.08 A   (b) 9.04 A   (c) 4.56 A   (d) 2.28 A

77. The average force on the core to reduce the air gap will be   (GATE EE 2007)

    (a) 832.29 N   (b) 1666.22 N   (c) 3332.47 N   (d) 6664.84 N

Statement for Linked Answer Questions 78 & 79:
Cayley-Hamilton Theorem states that a square matrix satisfies its own characteristic equation.
Consider a matrix $A = \begin{pmatrix} -3 & 2 \\ -1 & 0 \end{pmatrix}$

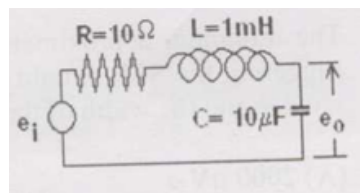78. A satisfies the relation   (GATE EE 2007)

    (a) $A + 3I + 2A^{-1} = 0$ (c) $(A + I)(A + 2I) = 0$
    (b) $A^2 + 2A + 3I = 0$ (d) $\exp(A) = 0$

79. A^9 equals   (GATE EE 2007)

    (a) $511\,A + 510\,I$ (c) $154\,A + 155\,I$
    (b) $309\,A + 104\,I$ (d) $\exp(9A)$

Statement for Linked Answer Questions 80 & 81:
Consider the R-L-C circuit shown in figure.

80. For a step-input e_i, the overshoot in the output e_o will be   (GATE EE 2007)

    (a) 0, since the system   under-damped   (c) 16%
        is not   (b) 5%   (d) 48%

81. If the above step response is to be observed on a non-storage CRO, then it would be best to have the e_i as a (GATE EE 2007)

    (a) step function   (c) square wave of frequency 300 Hz
    (b) square wave of frequency 50 Hz   (d) square wave of frequency 2.0 kHz

Statement for Linked Answer Questions 82 & 83:

The associated figure shows the two types of rotate right instructions R1, R2 available in a microprocessor where Reg is a 8-bit register and C is the carry bit. The rotate left instructions L1 and L2 are similar except that C now links the most significant bit of Reg instead of the least significant one.
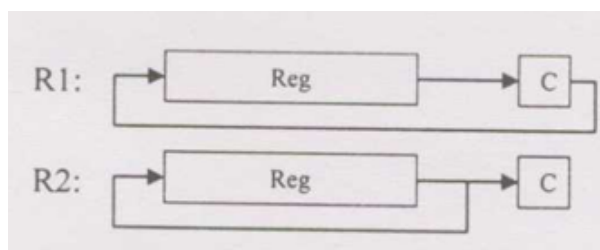
82. Suppose Reg contains the 2's complement number 11010110. If this number is divided by 2 the answer should be   (GATE EE 2007)

   (a) 01101011   (b) 10010101   (c) 11101001   (d) 11101011

83. Such a division can be correctly performed by the following set of operations   (GATE EE 2007)

   (a) L2, R2, R1   (b) L2, R1, R2   (c) R2, L1, R1   (d) R1, L2, R2

Statement for Linked Answer Questions 84 & 85:

84. A signal is processed by a causal filter with transfer function $G(s)$. For a distortion free output signal waveform, $G(s)$ must   (GATE EE 2007)

   (A) provide zero phase shift for all frequency
   (B) provide constant phase shift for all frequency
   (C) provide linear phase shift that is proportional to frequency
   (D) provide a phase shift that is inversely proportional to frequency

85. $G(z) = \alpha z^{-1} + \beta z^{-3}$ is a low-pass digital filter with a phase characteristics same as that of the above question if   (GATE EE 2007)

   (a) $\alpha = \beta$ (b) $\alpha = -\beta$ (c) $\alpha = \beta^{(1/3)}$ (d) $\alpha = \beta^{-(1/3)}$

END OF THE QUESTION PAPER