

INDIAN INSTITUTE OF TECHNOLOGY PATNA

CS226- Lab 7

Q1: Design a 4 bit asynchronous counter and test the output using a seven segment display. (40 points)

Q2: Design a 2-bit counter with following behavior

C0C1: 00 Stop counting

C0C1: 01 count up

C0C1:10 Count down

C0C2:11 : count by 2.

(C0 & C1 are the control inputs)

(60 points)

Design the above in paper before doing the experiment. Submit your paper work after demo.