

ELYSE PARKER

HARDWARE TEST ENGINEER

Contact

elyse.x.parker@gmail.com

978-477-8422

linkedin.com/in/elyse-parker

Summary

Professional hardware test engineer with over 8 years of experience in the electronics industry. Skilled in developing, testing, and debugging hardware components for various applications. Experienced at troubleshooting problems and finding solutions to them quickly. Expertise includes working with high-end microcontrollers and embedded systems as well as analog/digital circuit design & analysis. Committed to providing quality assurance through meticulous testing programs that ensure product reliability and customer satisfaction.

Employment

Hardware Test Engineer at Employer A

Las Vegas | Jan 2018 to Present

- Tested and verified system hardware functionality, including storage devices, motherboards and network cards; identified over 200 bugs within the first month of joining.
- Structured test plans for all new product launches to ensure quality performance standards were met before release; reduced customer complaints by 25% in under 6 months.
- Improved existing designs and developed prototype testing methods for various components such as processors, GPUs and displays; increased overall efficiency by 35%.
- Upgraded software & firmware versions across multiple platforms with a 95+ success rate per upgrade cycle; saved an average of 10 hours on each project timeline due to faster implementation speeds.
- Consistently monitored test results throughout development cycles to identify potential issues early on in order to reduce costs related to rework or defects later down the line.

Hardware Test Engineer at Employer B

San Bernardino | Mar 2012 to Dec 2017

- Facilitated the testing of over 200 hardware components per day and verified performance data; successfully identified faulty parts, resulting in a 98% reduction in defective items shipped.
- Independently conducted research to discover new methods for increasing accuracy during the test process and implemented them, decreasing overall test time by 10%.
- Reorganized existing testing protocols to ensure that all hardware products were evaluated thoroughly before being sent out; improved customer satisfaction rates by 7%.
- Introduced automated processes into the software which enabled engineers to remotely monitor tests from their workstations across various locations; reduced staff costs by \$5,000 annually.
- Expedited product launch timelines through rapid bug-fixing strategies and timely debugging activities; completed projects up to 12 hours ahead of schedule on average each month.

Education

Bachelor of Science in Electrical Engineering at Educational Institution XYZ

Nov 2011

Skills