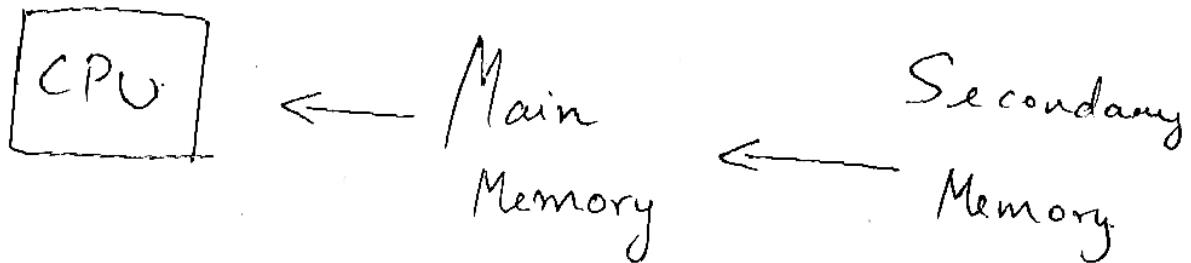
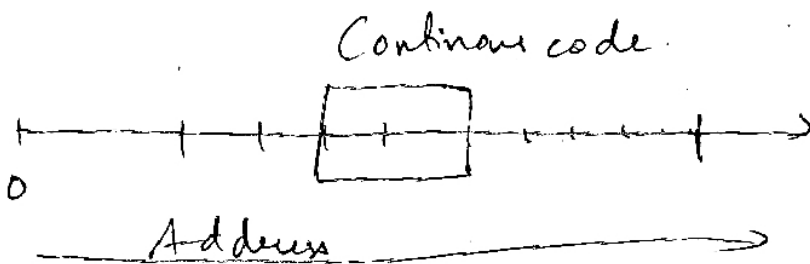


Memory Management. ①



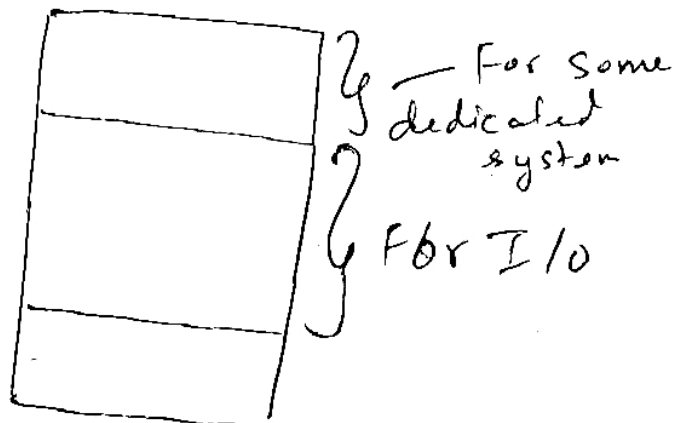
- Generally, instructions / code is sequential.
⇒ We can therefore bring a lot of code (or data) beforehand.



Base System :

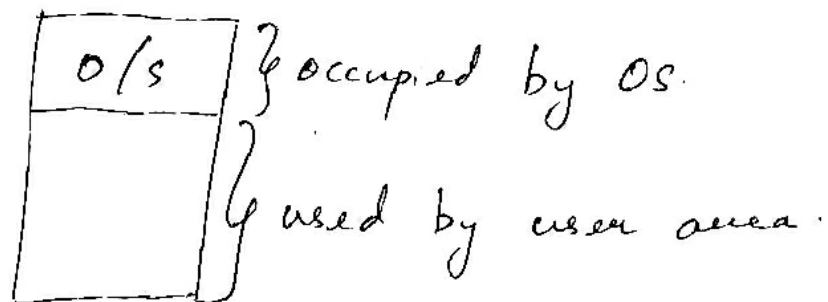
or

Dedicated system



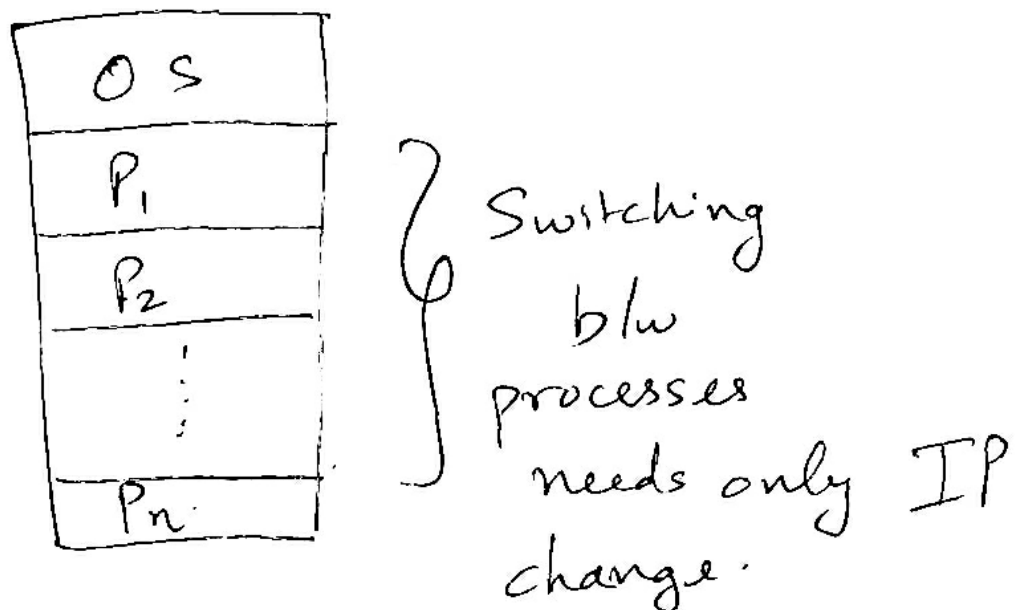
All parts of the memory are fixed. (main) ②

These days we have the O/s residing in the memory.



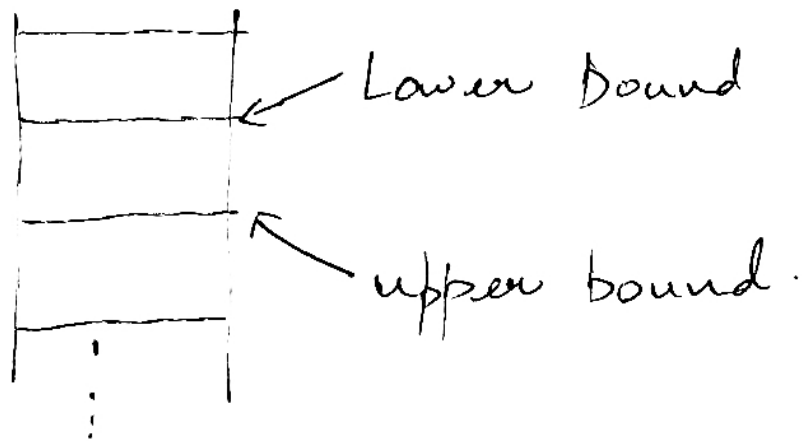
Note: There is a ^{clear} distinction b/w OS area and user area.

Multiprogramming with fixed tasks



IP: Instruction Pointing. (3)

For every partition, there must be an upper & a lower bound.

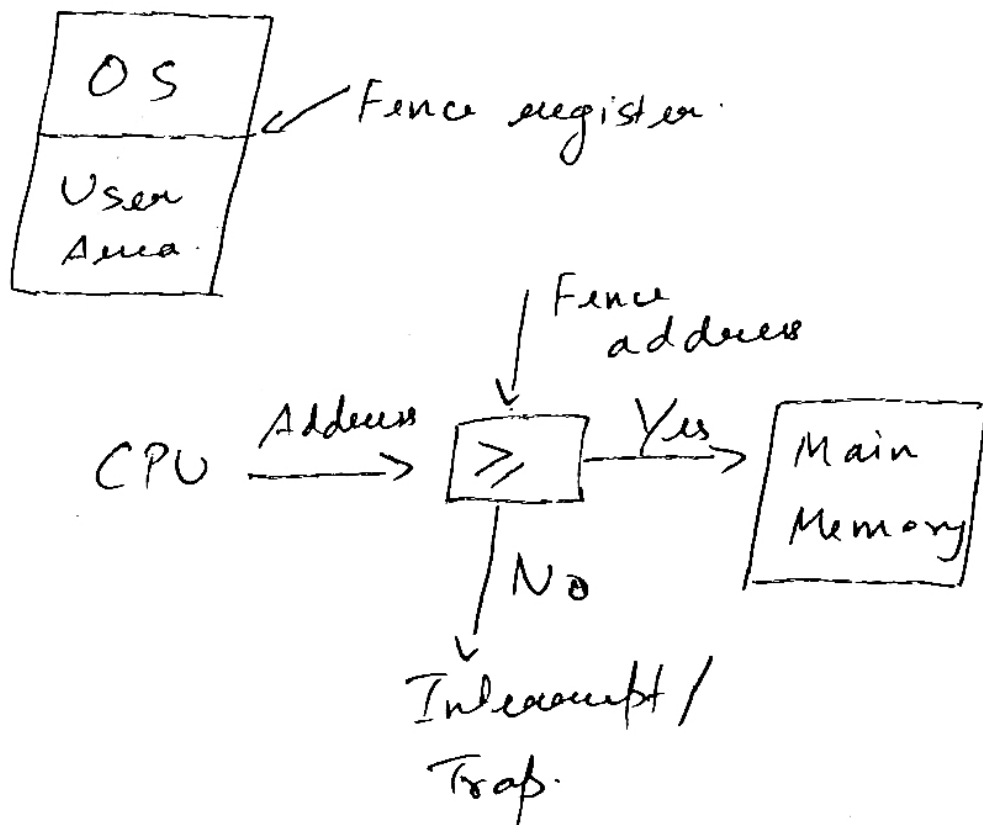


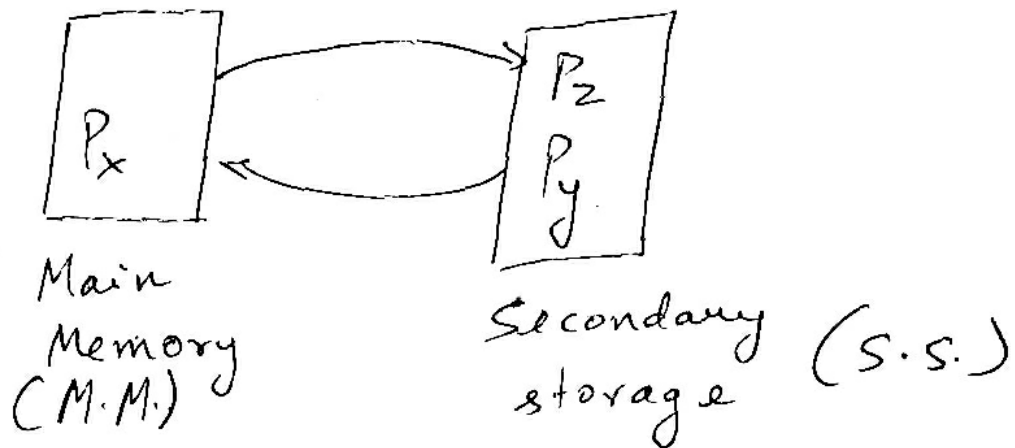
Q: How does CPU access something residing at the physical address.

A: Use logical address and memory management unit.

(4) Logical address is the address at which an item appears to reside from the perspective of an executing application program.

Address Translation
Mode.
For ~~dedicated~~ systems





If P_x is modified in M.M.
 {
 copy in S.S. should be updated

}

If P_x goes into trap mode

 {
 1. P_x must be swapped out

another process P_z should be swapped in }.

④
If P_x is finished

⑥
{ P_x in M.M. can be overwritten

by P_y (e.g.) since copy of

P_x is already there in S.S.

g.

If, we have only two partitions.

1) OS area

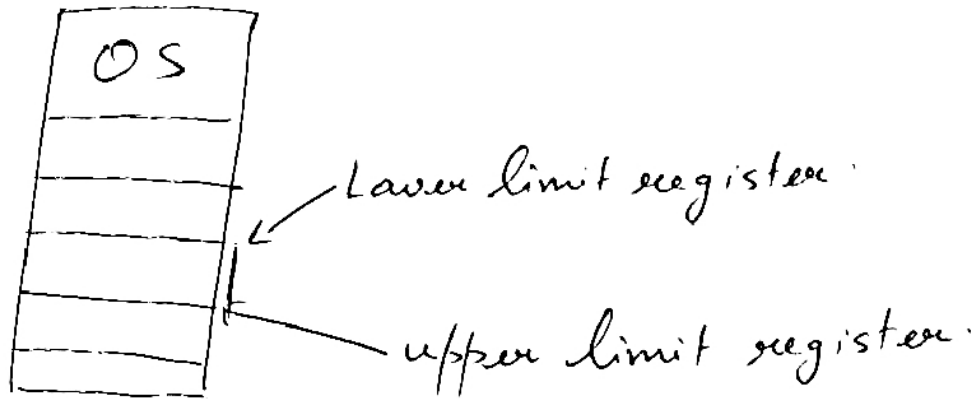
2) Process area

CPU utilization is bad.

↳ Many processes will go
for I/O.
etc.

16)

Fix: Have multiple partitions in the main memory.

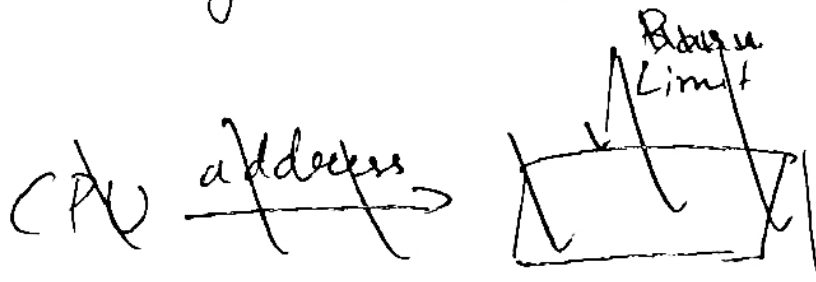


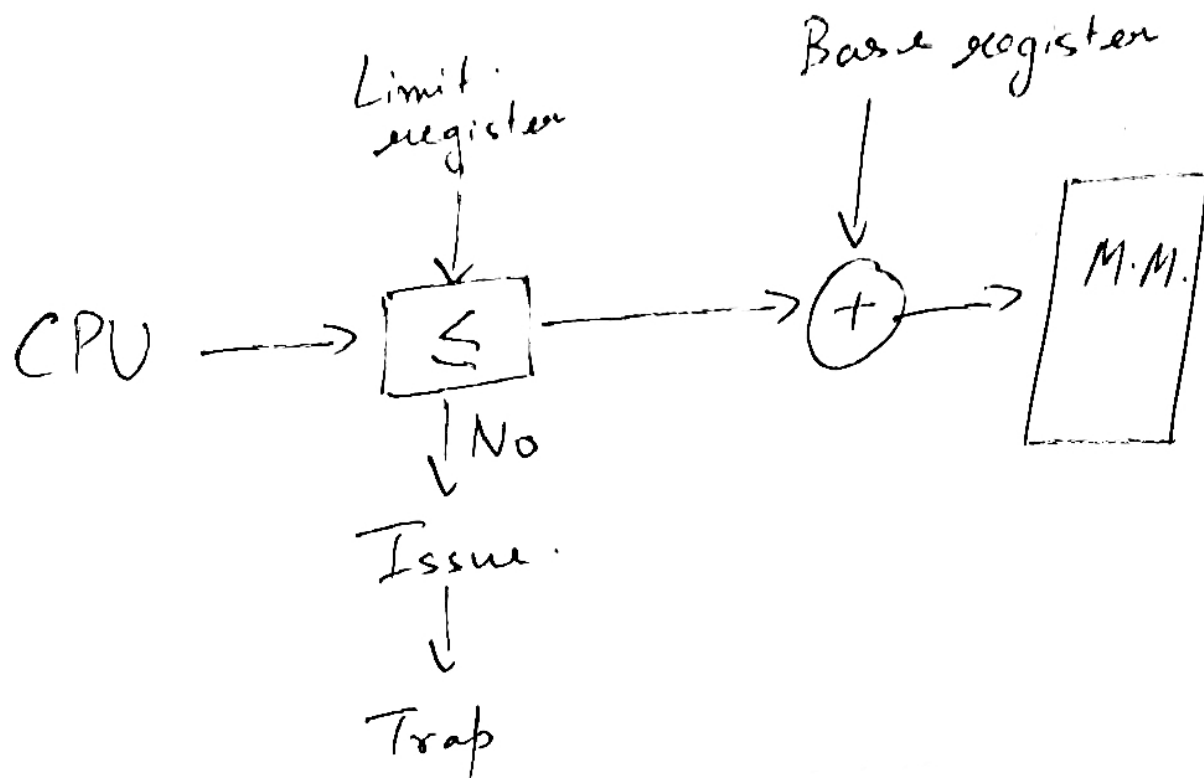
Modern systems use this idea.

They use the following:

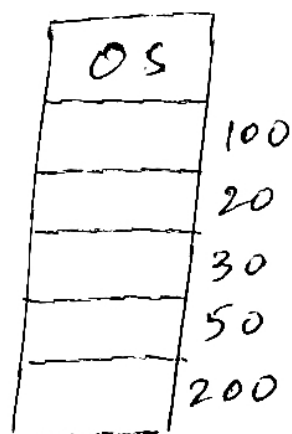
- 1) Base register (starting point)
- 2) Limit register (Permissible range).

Memory access scheme now becomes:





Partition allocation.



Q: Multiple jobs have to assigned memory in. How to do that?

Ans: Use memory allocation algorithms

1) First fit algorithm.

Scan memory partitions sequentially and find the partition with equal or more space than that required by a job.

e.g. J_1 need 50 kb

P_1 is free and has size 100 kb

∴ Assign P_1 to J_1

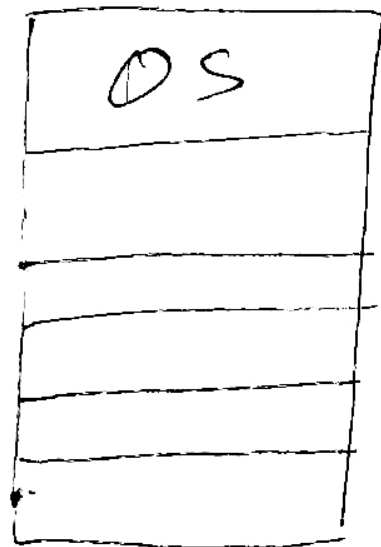
(P_1) 100

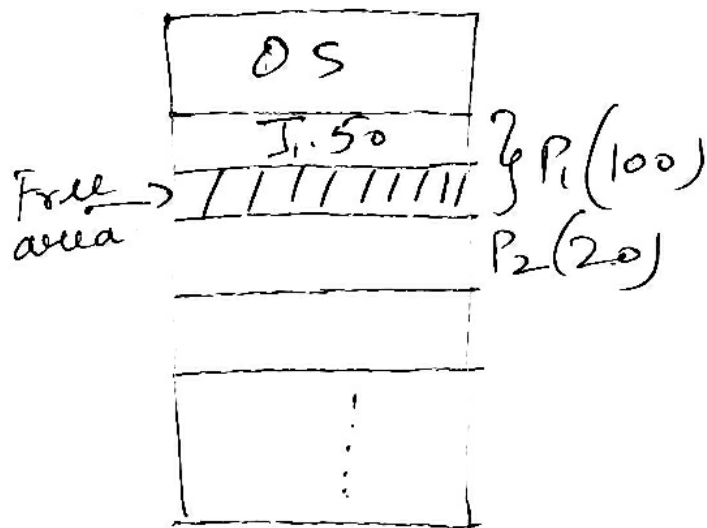
(P_2) 20

(P_3) 30

(P_4) 50

(P_5) 200





Issue: The area in |||| is wasted.

This is called as internal fragmentation.

2) Best fit algorithm.

Find the best size for the job.

Expensive takes time.

3) Worst fit

Find the largest block. to fit the job (Bad).

4) Next fit algorithm.

Similar to first fit.

↳ It however starts scanning partitions from the node where it previously allocated the partition.

To accomplish partition assignment, additional info has to be maintained.

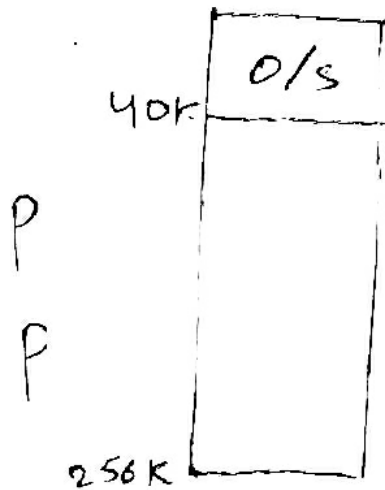
- i) Starting location of the partition.
- ii) Size.
- iii) Status.

The info is stored in partition allocation table (PAT).

The above ^{table} is used memory mgmt unit to assign partition.

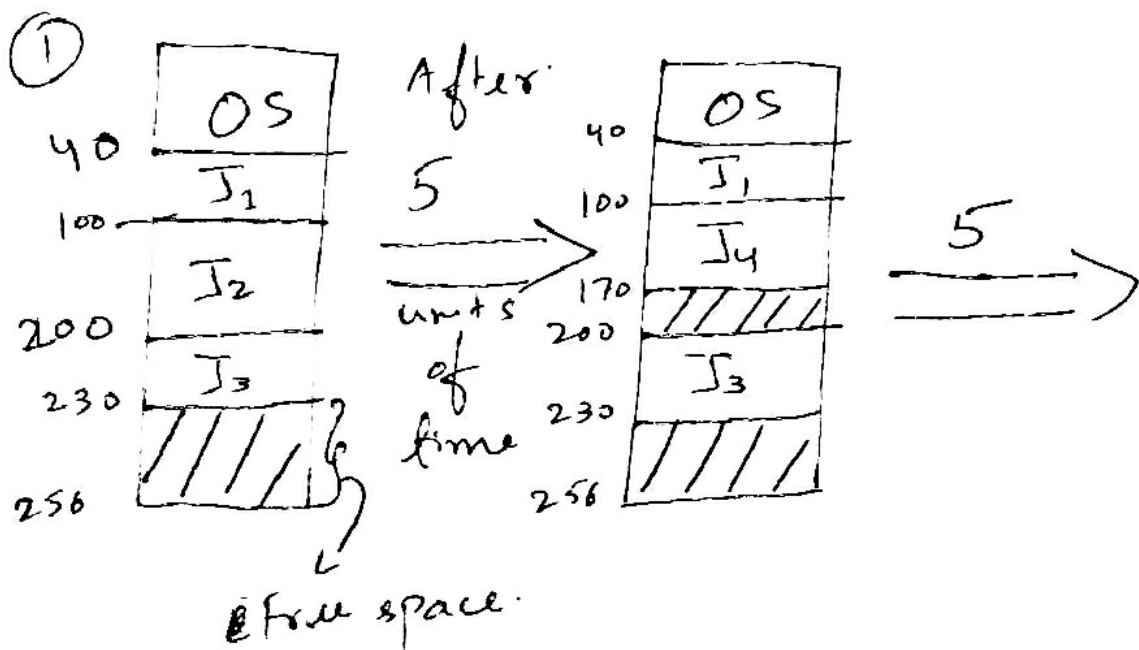
4) Example (Taken from Internet) ①

Variable number of tasks

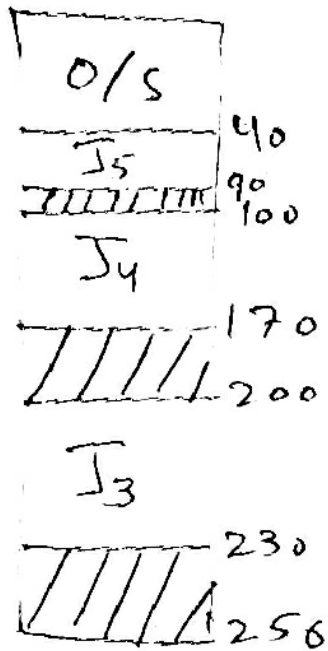


	Memory	B.T.
J_1	60	10
J_2	100	5
J_3	30	20
J_4	70	8
J_5	50	15
J_6	60	9

Say scheduling is F.C.R.S



(B)

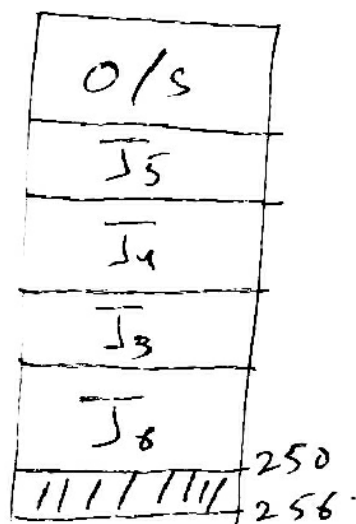


Total free memory 66 KB.

Memory requirement of $\overline{J_6}$ 60 KB.

However, no continuous space is available

\therefore do compaction.



Compaction makes things easy.

However, need more resources

- o Compaction module
- o Job/Process execution must pause.
- o Additional info must be maintained.

E-g. ① which partitions are free.

② Start & End address of each partition has to be rescheduled.

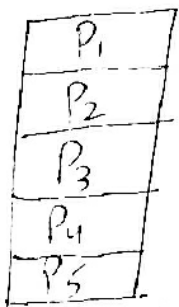
③ Entire memory structure is must be changed.

To Avoid the problems of compaction and maintaining additional information and fragmentation, we use page based memory mgmt.

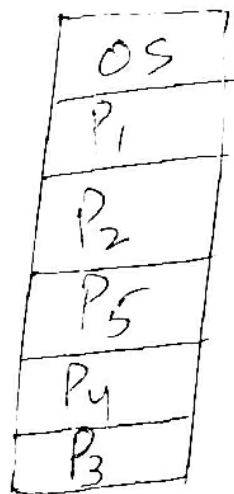
Job : You divide this into multiple pages

Memory : You divide the memory into multiple pages.

These are pages.



Process/
Job



Main Memory

Here, we call the partitions as frames

$P_i = i^{th}$ page.

Let assume that the page size is P
" " " " " number is p
and the offset within the page
is d .

Let's say that the logical address
generated is L .

Now, using common sense

$$p = L/P$$

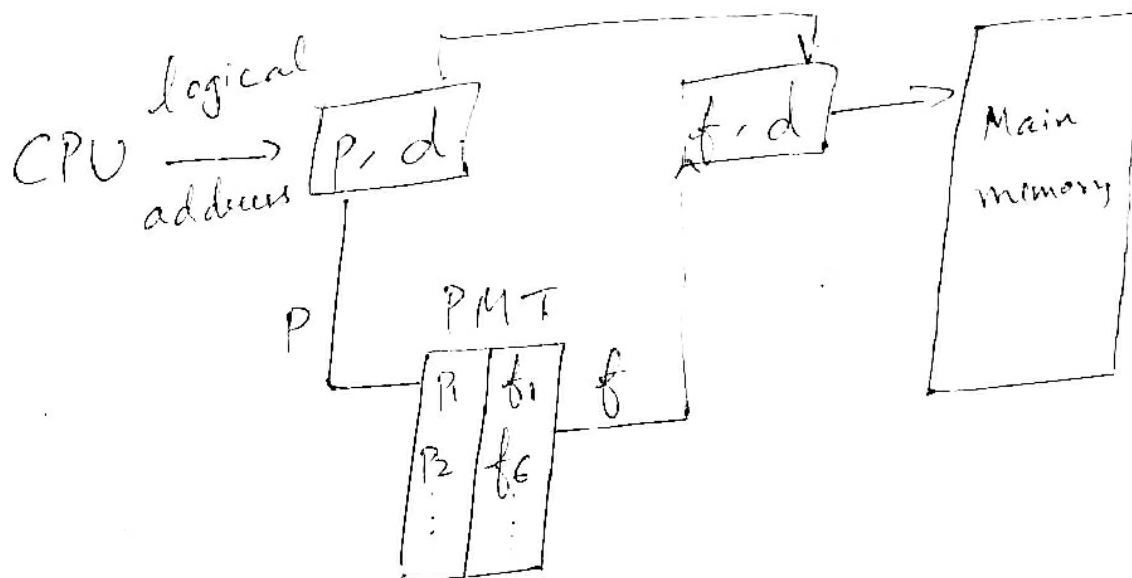
$$d = L \bmod P$$

To connect pages (in CPU) with frames
(in memory), we need a Page Map Table
(PMT).

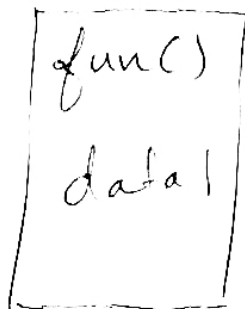
PMT

Page no.	Frame number

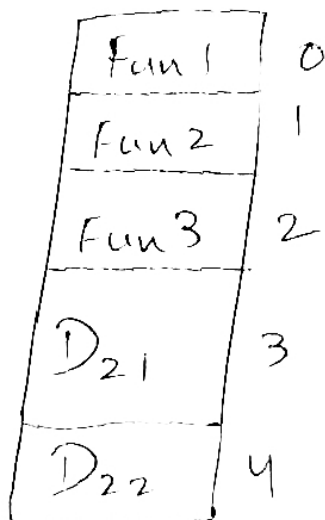
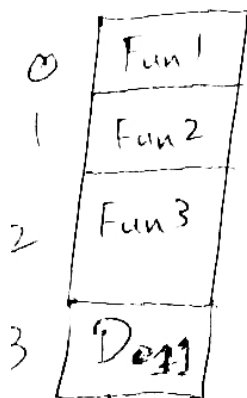
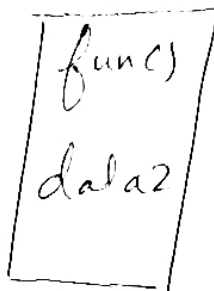
Hence, the mapping becomes



Prog 1



Prog 2



PMT.

	P.No.	Frame No.
1	0	0
1	1	1
1	2	3
1	3	6

PMT.

T	P.No.	F.No.
1	0	0
1	1	1
1	2	3
1	3	11
1	4	8
0		
0		

O/S	
Fun 1	0
Fun 2	1
	2
Fun 3	3
	4
	5
D ₁₁	6
	7
D ₂₂	8
	9
	10
D ₂₁	11
	12
	13

Pages can be shared given that
fun() function does not modify
itself while executing.

If prog. changes itself then you
have to load different copies of
the same program.