Tutorial 11

ECSE104L

Ques 1. Design 4 bit register using D flip flop (SISO, PIPO)

Ques 2. Design left shift register and draw the waveform of each flipflop output with clock pulse while you are storing 1001 into the register. You have to draw wave form at least 4 clock pulse.

Ques 3. Design store and right shift register in a single unit using 2-1 MUX.

Ques 4. Design Universal register which has capability of store, left shift, right shift, Parallel input.