Addressing Mode.	Effective Address	Content of AC.
Car	in a horasper elle	12.00
Immediate operand.	201.	500
Airect de la	: ald 500 black lash	1800
Indirect	800	300
Relative	702	325
Indened	600	900
Register	400	400
Register Indirect	(FFN 8400 Lassace	700.
7.3.20	· ka	

1 Two address instruction.

/ immediate addressing -> effective address -> \$201.

lotal bits neguined a 3 (mita) + 0 3 miles of bits

- 1 Direct check content of location 500 -> 800
- / Indirect -> check content of 800 -> 300.
- Relative address -> In the indirect mode the effective address is stored in memory at address 500.

 So in relative mode effective address is

 At PC = 500 + 202 = 702

content of 702 -> 325

Indeped address -> A+ XR = 500 + 100 = 600.

XR is given as index register content.

- Register In the register mode the operand is in Register mode R1. so the apparatual loaded into Ae is 400. No effective address as directly loaded from negit
- / Register Indirect > In register indirect effective address will 400 so, content of to will be 700.