

Tutorial 6

Encoder, Decoder, MUX, DMUX

Ques 1. Design 3-8 Decoder using 2-4 decoder with enable.

Ques 2. Design 4-16 Decoder using 2-4 decoder with enable.

Ques 3. Perform following operation using only **one adder and 4-1 MUX and 1-4 DMUX**. Here a, b, c, d, e are input and Y is output.

