

Draw and ~~flow~~ explain the flowchart for the following instruction.

① Substruction:

SUBTRACT 2, 179

$MAR \leftarrow IC$

$MBR \leftarrow M(MAR)$
 $M(2)$

$IR \leftarrow MBR$

Test instruction type
subtract

$MAR \leftarrow IR(\text{address})$
 $IR(179)$

$MBR \leftarrow M(2)$

$WR \leftarrow R(IR(2))$

$WR \leftarrow WR - MBR$

$R(IR(2)) \leftarrow WR$

$IC \leftarrow IC + 1$

- 1) Memory location of current instruction is stored in MAR (Memory address register).
- 2) Read instruction from memory and stored MBR (Memory Buffer register).
- 3) Put instruction in instruction register (IR).
- 4) choose test instruction type (SUBTRACT).
- 5) Read data from memory & stored in MBR.
- 6) Copy designed general register into working (WR).

②

MULTIPLY 5, 189.

$$MAR \leftarrow IC$$

$$MBR \leftarrow M(MAR)$$

$$M(5).$$

$$IR \leftarrow MBR$$

Test instruction type multiply.

$$MAR \leftarrow IR(\text{address})$$

$$IR(189)$$

$$MBR \leftarrow M(5).$$

$$WR \leftarrow R(IR(5)).$$

$$WR \leftarrow WR * MBR$$

$$R(IR(3)) \leftarrow WR$$

$$IC \leftarrow IC + 1$$

- 1) Memory location of current instruction is stored in MAR (memory address register).
- 2) Read instruction from memory and stored MBR (memory buffer register).
- 3) put instruction in instruction register (IR)
- 4) choose test instruction type (MULTIPLY).
- 5) Read data from memory and stored in MBR
- 6) perform operation (* multiply)
- 7) Copy designed general register into working register (WR).
- 8) Leave resulting ans in general register
- 9) increment instruction Counter
($IR \leftarrow IR + 1$)