

**ANITA.P**

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CAREER OBJECTIVE:

“To begin my lecturer career at a highly-reputed and eminent educational institute and to gain exceptional career move ahead through long efforts and performance regularity”

SUMMARY OF EXPERIENCE AND QUALIFICATIONS:

- Worked as a System Analyst with iCAL SOLUTIONS for **1.4** years from 7th July 2016 – 30th Nov 2017.
- Worked as an Assistant Professor in the Department of Telecommunication Engineering with C.M.R.I.T, Bangalore for **3.5** years from Aug.2012 – Jan 2016.
- Worked as a lecturer in the Department of Telecommunication Engineering with C.M.R.I.T, Bangalore for **5** years from Aug.2006 – july.2011.
- Worked as a lecturer in the Department of Electronics and communication with Dr.TIT (GVIT), KGF for **2** years form 2004-2006.
- Highly innovative in depicting the subject matter to the students/audience, using traditional methods as well as modern aids.
- In-depth knowledge of various subjects and techniques.
- Perfect knowledge about the common job duties of a lecturer and ability to perform them efficiently.

ACADEMIC EDUCATION:

Registered for Ph. D under VTU for the 2016 (course work written).

Course Name	Specialization	Institute/College Name	Board/ University	Year of Completion	Percentage/ Grade
M. Tech	VLSI Design & Embedded system	CMRIT, BANGALORE	V.T.U	2013	71%
B.E	Electronics & Communication	GVIT,KGF	Bangalore	2001	64%

SUBJECT KNOWLEGDE:

Information theory and Coding , Analogy electronic circuit ,Digital system design, Embedded system , HDL, Microprocessor, System on chip, Electronic instrumentation, Digital switching system, Basic electronic, Linear integrated circuits, Logic design, , Cadence ,lab view,

PERSONAL SKILLS:

- Good communication skills and Rich Technical skills.
- Optimistic nature.
- Hard working and Willingness to learn.

TECHNICAL PRESENTATION:

“AIRCRAFT DETECTION USING PASSIVE MULTISTATIC RADAR”, International Journal of Advanced Research in Science and engineering (IJARSE) ISSN: 2319:8354 Vol.4, Issue No.12, Dec 2015.

“Hardware Implementation of Wave Pipelining Backtracking Switch for Optimizing Nocs”, **International** Journal of Advanced Information Science and Technology (IJAIST) ISSN: 2319:2682 Vol.12, No.12, April 2013.

“Design of Binary CSD multiplier using ASIC front end”, International conference on communication, VLSI & Signal processing”, Feb. 20- 22, 2013.

“Flexible TCP/IP Packet Generation framework”, Nation Conference May 2013, New Horizon, B’ lore

“Implementation of Binary canonical signed digit multiplier using application specific Integration Circuits” International Journal of engineering research and application (IJERA) –March 5th Issue-2013.

M.Tech PROJECT

Project Title: Hardware Implementation of Wave Pipelining Backtracking Switch for Optimizing Nocs.

Language of Implementation: Verilog.

Description:

This paper presents the Hardware implementation of Wave-Pipelined Backtracking Switch for optimizing Nocs support guaranteed throughput. The proposed circuit-switched switch, based on a backtracking probing path setup, operates with a source-synchronous wave-pipeline approach. The switch can support a dead and live-lock free dynamic path-setup scheme and can achieve high area and energy efficiency. Here we use 8*8 Irregular mesh

topology, path takes the shortest path to transmit the data from the source to destination with backtracking of switches. It is a challenging task in a network-on-chip to design an on-chip switch/router to dynamically support (hard) guaranteed throughput under very tight on-chip constraints of power, timing, area, and time-to-market, and coding is done using verilog.

PERSONAL PROFILE:

Name : Anita P
Date of Birth : 20-07-1979
Mother name : Mrs. Shasikala
Father's Name : Mr. R. Ponnuswamy
Gender : Female
Marital Status : Married
Nationality : Indian
Hobbies : Organizing various cultural events, playing carom, Cooking, Surfing.
Languages known : English, Kannada, Hindi, Telugu.

DECLARATION:

I hereby declare that the above-furnished details are true to the best of knowledge and belief.

Date:

Place: Bangalore

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