



## Bio-Data

Name : Dr. DINESHA .P

Father Name : C. PUTTARAJE GOWDA

Date of Birth : 02-02-1968

Marital status : Married

Address : 1380/I, Pragnya Nilaya,TF-4,  
Srikanth Residency, 9<sup>th</sup> Main,  
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Bengaluru-560 050

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Educational Qualification : **B.E-** Electronics & Communication Engineering  
**M.E** - Power Electronics  
**Ph.D.** – Electronics (Nanoelectronics)

Educational Details:					
Course	Institution Studied	Board/ University	Year of passing	% of Marks obtained	Class obtained
<b>Ph.D.</b>	<b>Title of the Research work:</b> “Investigation on Polymer Nanocomposites for Electromagnetic Interference Shielding and Positive Temperature Coefficient of Resistance Applications in Electronics”				
	JSS Research Foundation, S J College of Engineering Mysore	<b>University of Mysore, Mysore</b>	2014		
<b>M. E</b>	U.V.C.E Bengaluru	<b>Bengaluru University</b>	Aug.1997	66.67	First
<b>B.E</b>	P.E.S.C.E Mandya	<b>University of Mysore, Mysore</b>	Mar 1991	55.76	Second
<b>P.U.C</b>	Sharada Vilas College, Mysore	P.U. Board Karnataka	Jun1985	62.67	First
<b>S.S.L.C</b>	P.S.S.K. High School, Pandavapura R.S	K.S.E.E.B Karnataka	Apr 1983	71.5	First

### **Area of Research Interest:**

- VLSI Design
- Nano Electronics (Conducting composites for electronics applications, sensors, etc.,)
- Digital System Design

### **Academic Experience:**

**Twenty Five (25)** years of total teaching experience

1. Currently working as **Professor** in the Department of E & C, Dayananda Sagar College of Engineering, Bengaluru, from December 2014.
2. Worked as **Professor** in the department of E & C, Nagarjuna College of Engg. & Technology, Bengaluru, from September 2007 to November 2014.
3. Worked as **Assistant Professor** in the department of E & C, Nagarjuna College of Engg. & Technology, Bengaluru, from August 2006 to August 2007.
4. Worked as **Assistant Professor** in the department of E & C, A I T, Chikkamagaluru, from May 2004 to August 2006.
5. Worked as **Lecturer** in the department of E & C, A I T, Chikkamagaluru, from May 1998 to April 2004.
6. Worked as **Lecturer** in the department of E & TC, AST Polytechnic, Chikkamagaluru from August 1992 to March 1996.

### **Professional Bodies Associated With:**

1. Life Member of Indian Society for Technical Education (I S T E)
2. Life Member of Systems Society of India (S S I)

### **Subjects Taught for U.G and P.G Courses**

#### **For P.G**

1. Digital VLSI Design
2. CMOS VLSI Design.
3. Design of VLSI systems.
4. Advances in VLSI Design

#### **For U.G**

1. Digital Electronics Circuits.
2. Analog Electronics Circuits.
3. Basic Electronics
4. Microprocessors
5. Microcontrollers
6. V L S I Design & Circuits.
7. Fundamentals of CMOS VLSI.
8. Fundamentals of HDL.

9. Digital Systems Design using V H D L and Verilog
10. Control Engineering
11. Nanoelectronics
12. Linear Integrated Circuits

#### **Ph. D Guidance:**

04 Research Scholars

#### **Projects Guided**

1. More than 14 projects for Post Graduate students.
2. More than **40** projects for under Graduate students.  
(4 projects are sponsored by Karnataka State Council for Science and Technology (KSCST), IISc, Bengaluru)

#### **Research Funding Project:**

1. **Co-Principal investigator** for the research project titled “Development of Hybrid Conducting Nanocomposites for Engineering and Electronics Packaging Applications”, financial assistance granted by Visvesvaraya Technological University (VTU), Belgaum, under Research Grants Scheme (Completed).
2. **Co-Principal investigator** for the research project titled “Design and development of fault tolerant technique for Reconfigurable Computing Applications” (Defence Research and Development Organization (**DRDO**), **Govt. of India**- On going)
3. **Program Coordinator** for the project proposal titled “Centre of Excellence for Nanotechnology & Applications” **submitted** to the VGST, Govt. of Karnataka, in the month of October 2017

#### **Recent Paper Publications and Presentations**

##### **International Journals**

1. **P. Dinesh**, N.M. Renukappa, T. Pasang, Meghala Dinesh, C. Ranganathaiah, “Effect of nanofillers on conductivity and electromagnetic interference shielding effectiveness of high density polyethylene and polypropylene nanocomposites”, **European Journal of Advances in Engineering and Technology**, Vol.1 (2), pp.16-28, Dec 2014.
2. **P. Dinesh**, N. M. Renukappa, Siddaramaiah and SundaraRajan, “Electrical Properties and EMI Shielding Characteristics of Multiwalled Carbon Nanotubes

Filled Carbon Black-High Density Polyethylene Nanocomposites” **Composite Interfaces (Taylor & Francis, Scopus indexed)**, Vol. 19 ( 2), pp.121-133, March 2012

3. **P. Dinesh**, N. M. Renukappa, Siddaramaiah, “Impedance and Susceptance Characterization of Multiwalled carbon Nanotubes with High Density Polyethylene-carbon Black Nanocomposites”, **Integrated Ferroelectrics (Taylor & Francis, Scopus indexed)**, Vol.116 (01), pp.128-136, Dec. 2010.
4. **P. Dinesh**, N.M. Renukappa, Siddaramaiah, J.H.Lee, T.Jeevananda, “ A Comparative Study on AC Conductivity and Dielectric behavior of Multiwalled Carbon Nanotubes and Polyaniline coated Multiwalled Carbon Nanotubes Filled High Density Polyethylene-Carbon Black Nanocomposites”, **American Institute of Physics (AIP) Conf. Proc.**, Vol. 1276, pp.189-197, Oct. 2010.
5. Rajanala Mamatha Rani, **Dinesha P**, “Design of Folded Tree Topology by Parallel Prefix Operations for Wireless Applications” **International Journal of Advanced Research Trends in Engineering and Technology (IJARTET)**, Vol. 2, Special Issue XXVII, pp. 137-140, 2015.
6. Pavithra B.U, **Dinesha.P** and Arpitha Shankar S.I, “Covert Communication Using Mosaic Images”, **International Journal of Scientific Development and Research (IJS DR)**, Vol.1 (8), pp.309-311, Aug. 2016.
7. Ashit S Chitta, **Dinesha P**, “Priority management of emergency vehicles using IoT approach”, **International Journal of Advanced Research in Computer and Communication Engineering**, Vol. 5(9), pp. 465-470, Sept.2016.
8. S.Ramesh Babu, Dinesha.P, A.Sudhakar, “Synthesis of Shaped Beams Patterns using Amplitude Control Method”, **International Journal of Engineering Science and Computing**, Vol.7 (12), pp. 15709-15711,Dec. 2017

#### **Book Chapter:**

1. Dinesha P, N M Renukappa, “PTC of resistance and EMI shielding effectiveness of HDPE and PP nanocomposites, “Materials for advanced technological

applications”, pp 81-99, Edited by S C Sharma, H Nagabhusan, K S Ananth Raju and H B Premkumar, 2016. ( ISBN: 978-93-85682-12-4)

### **International Conferences**

1. **P. Dinesh** and N.M. Renukappa, “Investigation of the conductive properties of polypropylene-carbon black-multiwalled carbon nanotube composites using positron annihilation technique”, **International conference on nanomaterials: science, technology and applications (ICNM’13) held at B.S. Abdur Rahman University, Tamil Nadu**, 5-7 December 2013.
2. **P. Dinesh**, N.M. Renukappa, Siddaramaiah and J. SundaraRajan, “Electrical resistivity and electromagnetic interference shielding effectiveness of multiwalled carbon nanotubes filled carbon black-high density polyethylene nanocomposites”, **International conference on electronics, biomedical engineering and its applications (ICEBEA’2012) held at Dubai, UAE**, pp. 336-339, 7-8 January 2012.
3. **P. Dinesh**, N.M. Renukappa, Siddaramaiah and J. SundaraRajan, “Temperature dependence of electrical resistivity of multiwalled carbon nanotubes filled carbon black-high density polyethylene nanocomposites”, **International Conference NMD ATM 2011 held at Hyderabad**, 15-16 November 2011.
4. **P. Dinesh**, N.M. Renukappa, Siddaramaiah and T. Jeevananda, “Thermal and impedance behaviors of PANi coated MWNT filled HDPE-CB nanocomposites”, **International conference on nanomaterials and nanotechnology (NANO-2010) held at KSR institute of technology, Tamil Nadu**, pp. 76, 13-16 December 2010.
5. **P. Dinesh**, N.M. Renukappa, Siddaramaiah, “Dielectric properties of short carbon fiber filled polymer composites”, **International conference on advanced materials, manufacturing, management and thermal sciences (AMMMT-2010)**, held at Siddaganga Institute of Technology, **Tumkur**, pp. 42, 18-19 November 2010.
6. **P. Dinesh**, N.M. Renukappa, Siddaramaiah and T. Jeevananda, “Study on ac conductivity and impedance properties of short carbon fiber filled polypropylene composites”, **International conference on communication, computation, controls and nanotechnology (ICN-2010)**, held at Rural Engineering College, **Bhalki**, pp. 936-939, 29-30 October 2010.

## **National Conferences**

1. **P. Dinesh**, N.M. Renukappa, Siddaramaiah, T. Jeevananda, “Improvements in Dielectric Properties of High Density Polyethylene-Carbon Black Composites using Multiwalled Carbon Nanotubes”, National Conference on “Plastic and Rubber Technology” (NCPRT-2011), 25-26 April 2011, Sri Jayachamarajendra College of Engineering, Mysore, pp.119.
2. Manjunatha B, **Dinesha P**, “Version-3 transfer frame data structure for proximity-1 space link protocol”, National Conference on Emerging Mobile Technology and Policies, on 28-30 May 2012 at MSR Institute of Technology, Bengaluru.
3. Prasanna Kumar, **Dinesha P**, “Designing of subthreshold voltage high-k CMOS devices at the minimum energy point”, National Conference on Communication and Soft Computing”, on 9<sup>th</sup> August 2012 at KS Institute of Technology, Bengaluru.
4. M. Naveen, **P. Dinesh**, S I Arpitha Shankar, “Implementation of 4 bit ALU using reversible logic”, National Conference on Communication and Image Processing (NCCIP-2013), held 7<sup>th</sup> August 2014 at T John College of Engineering, Bengaluru, Karnataka.
5. D. Monika Sharma, **P Dinesh**, “Design and Implementation of N-bit Multiplier and discrete Cosine Transform Using Vedic Mathematics”, National Conference on ‘TECHNOTRONICS-14’, held 25<sup>th</sup> June 2014 at Maratha Mandal Engineering College, Belagaavi, Karnataka.
6. RajanalaMamatha Rani, **P. Dinesha** “DSP Architecture for Wireless Applications Using Different Types of Adders” in the “First National Conference on Green Computing Technologies (NCGCT-2015)”, held on 7<sup>th</sup> May 2015 at Dayananda Sagar Academy of Technology & Management, Bengaluru.
7. P K Sandesha Karanth, **P Dinesh**, Arpitha Shankar S I, “Designing of AXI4 to AHB-Lite Bridge using Verilog”, National Conference on “Sustainable Emerging Intelligent Technologies (SEITCON-2016)” on 22<sup>nd</sup> & 23<sup>rd</sup> February, 2016 at Dayananda Sagar College of Engineering, Bengaluru, Karnataka. ISBN: 978-93-84935-79-5
8. B U Pavithra, **P Dinesha**, Arpitha Shankar S I, “Secret image transmission using mosaic fragment visible image”, National Conference on Convergence of

- Science, Technology and Management (NCCSTM-2016) on 05<sup>th</sup> May 2016, at Dayananda Sagar Academy of Technology and Management, Bengaluru, Karnataka.
9. Darshan G V, **Dinesha P**, “ Parallel processing Deblocking filter hardware for high efficiency video coding”, National Conference on Control, Communication and Power Systems (NC3PS-2016) on 29<sup>th</sup>, 30<sup>th</sup> July 2016, at B M S Institute of Technology and Management, Bengaluru.
  10. Akshay A Jagajampi, Anusha S, Chandanna Tumkur, Chetan Patil, Arpitha Shankar S I, **Dinesha P**, “ Soilless Vertical Farming”, Second National Conference on “Sustainable Emerging Intelligent Technologies (SEITCON-2017)” on 26-28, May 2017 at Dayananda Sagar College of Engineering, Bengaluru, Karnataka. ISBN: 978 – 93 – 86238 – 25 -2.
  11. Pavan V. Ghat, Karthik B. R, Manjunatha N, Roja S, **Dinesha P**, “Automatic Traffic Law Enforcement” System Second National Conference on “Sustainable Emerging Intelligent Technologies (SEITCON-2017)” on 26-28, May 2017 at Dayananda Sagar College of Engineering, Bengaluru, Karnataka. ISBN: 978 – 93 – 86238 – 25 -2.

**Activities Involved:**

1. Chairperson for the Department **BOS** to frame the autonomous syllabus for Bachelor of Engineering Course (2015 to till date).
2. **Editor** for the Second National Conference “Sustainable Emerging Intelligent Technologies (SEITCON 2017)” proceedings.
3. Development of Digital Electronics Lab, VLSI & HDL Lab.
4. I S O work as an Internal Auditor.
5. N B A/NAAC Department Coordinator.
6. **Resource person** for the M S program (Corporate) in Manipal University, Bengaluru, for more than five years.
7. Worked as an Assistant Coordinator for K S O U (2.5 years).

**Additional Positions Held:**

1. Presently working as Dean (2<sup>nd</sup> Shift) for Department of Electronics & Communication Engineering

2. Visited as a member of local Inspection committee for Visvesvaraya Technological University (VTU), Belagavi.
3. Visvesvaraya Technological University (VTU) nominated Board of Studies (BOS) member for Department of E&C, Nagarjuna College of Engineering & Technology, Bengaluru (Autonomous 2015-2017 )
4. Board of Examiner (BOE) member for Department of E&C, New Horizon College of Engineering Bengaluru (Autonomous 2015-2017).
5. BOS and BOE member for Department of E&C, Jain University , Bengaluru
6. BOS member for Department of TE, Dr AIT, Bengaluru (Autonomous)
7. BOE member for Department of E & C, SIT, Tumkur, Karnataka (Autonomous).
8. **Advisory Board/Technical Programme Committee/ Organizing Committee member** for the
  - National Conference NCEVENT-15
  - National Conference on Role of Innovative Engineering Solutions for Medical Sciences” (RIESMS-2015)
  - Faculty Development Programme on “Design and Development of Embedded System using MSP430”
  - National Conference on Space ready educational Institutions-A dream come true
  - International Conference ICASET-16
  - International Conference ICMCECE-16.
9. Attended as a **Session Chair** for the **National Conferences** held at
  - BMS College of Engineering, Bengaluru
  - Dayananda Sagar Academy of Technical and Management, Bengaluru
  - KS School of Engineering and Management, Bengaluru
  - Adichunchanagiri Institute Technology, Chikmagalur.

#### **Workshop Organized:**

1. Organized a 3 days’ workshop on “Partial Reconfiguration Flow using Xilinx Vivado Design Suite on Zed Board” 9-11 October 2017 in association with CoreEL, Bengaluru
2. Received “Certificate of Appreciation” from Texas Instruments for being the organizer for the DrishTI online contest 2017.



3. Organized a 5 days' workshop on "Analog and Digital IC Design Flow using Mentor Graphics EDA Tool" 10-15, March 2018 in association with CoreEL, Bengaluru

**Workshops Attended:**

1. Two weeks short term training programme on "Recent advances in chemical, electrochemical and nuclear energy systems" held at PES College of Engineering, Mandya, 21-31, October 1999.
2. National level workshops on "V L S I & CAD for V L S I" held at Bengaluru Institute of Technology, Bengaluru, 04-09 Sept.2000.
3. Workshop on "Digital Signal Processor" organized by VTU-MOTOROLA, at BMSCE, Bengaluru, 18 -22 Oct. 2000.
4. Orientation Workshop on "V L S I DESIGN" held at B V B C E, Hubli, 15-22 Sept. 2001.
5. Short term course on "Theory & Practice of CAD for V L S I" held at NMAMIT, Nitte, 02-14 Sept.2002.
6. Work shop on "DSP & its Applications", held at AIT, Chikmagalur, on 14 - 15, May 2004.
7. Workshop on "V L S I Design Lab" held at Bengaluru Institute of Technology, Bengaluru, 07-09 Aug. 2004.
8. "V L S I Design & Test" workshop held at Infosys, Mysore, 26-28 Aug. 2004.
9. ISTE Annual convention and National seminar on the theme "Disaster-Prediction, Prevention & Management" organized by Bapuji Institute of Engineering and Technology, Davangere, 15-17 December 2005.
10. Work shop on "Image & Speech Processing", held at AIT, Chikmagalur, on 25-26, April 2006.
11. IMAPS India National Conference on "Technologies & Applications of Micro Nano Electronics" held at BMSCE, Bengaluru, on 23-24, November 2007.
12. Short term training program on "Fundamentals of Cryptography and Applications" held at Nagarjuna College of Engg. & Technology, Bengaluru, on 7-11, July, 2008.
13. Winter School on "Polymers: Synthesis, Characterization and Applications" held at IISc, Bengaluru, 20-24 December, 2010.

14. Faculty Empowerment Programme- “Mission 10X” conducted by WIPRO held at Nagarjuna College of Engg. & Technology, Bengaluru, 18-22, April 2011.
15. Work shop on “Mentoring and Counseling for the Youth” conducted by Banjara Academy, at Nagarjuna College of Engg. & Technology on 30<sup>th</sup> January 2012.
16. Two days seminar on “Role of Nanocomposites in Dielectrics and Devices” at S. J. College of Engineering, Mysore, 16-17, March 2012.
17. Two days hands on workshop on “Real Time Control Systems” held at PES Institute of Technology, Bengaluru, on 30-31, July 2012.
18. Two days national workshop on “Dielectric materials and devices” held at S. J. College of Engineering, Mysore, 25-26, July 2014.
19. One week faculty development program on “Emerging trends in PLC and SCADA based industrial automation” held at Dayananda Sagar College of Engineering, Bengaluru, on 27-31, January 2015.
20. One week faculty development programme on “Virtual Instrumentation in Technical Education” held at Dayananda Sagar College of Engineering, Bengaluru, on 9-13, February 2015.
21. Attended DIDAC India 2016: International Education & Skill Summit at Bengaluru, on 27 -30, September 2016.
22. Attended CDN Live – Cadence User Conference 2017 at Bengaluru on 7-8 September 2017.
23. Attended “20<sup>th</sup> ISTE state level faculty convention” on 9<sup>th</sup> Dec 2017 at Dayananda Sagar College of Engineering, Bengaluru.
24. Participated “31<sup>st</sup> International Conference on VLSI and 17<sup>th</sup> International Conference on Embedded System” held from 6-10, January 2018 in Pune, India.
25. Attended one day seminar “Tanner EDA Seminar: Upgrade to Custom ASIC” on 26-04-2018 at Bengaluru.

### **Others**

1. Post graduate Diploma in **Nanobiotechnology** from Department of Nanotechnology, Life Science Foundation India, Karnataka.
2. One day training program on “**Internal Audit**” as per ISO 9001:2000 International Standards for quality management systems conducted on 26-10-2004 by ACME (India) Quality Management Services, Chennai.
3. Four months training program in the area of **Material Technology and Characterization** from 01-03-2009 to 31-07-2009 at Central Power Research Institute (CPRI), Bengaluru.

Date:

Place:

A handwritten signature in blue ink, appearing to read "Dewid", written over a horizontal line.

Signature