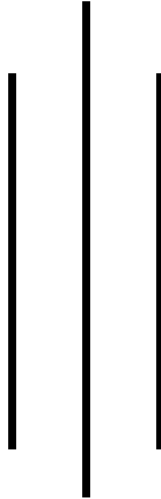TRIBHUVAN UNIVERSITY

PATAN MULTIPLE CAMPUS

PATAN DHOKA, LALITPUR

MICROPROCESSOR AND COMPUTER ARCHITECTURE (BIT 151)

LAB …….

SUBMITTED BY   **SUBMITTED TO**

NAME: SURESH DAHAL   JYOTI PRAKASH CHAUDHARY

CLASS: BIT – I/II

ROLL NO: 23   …………………………

DATE: 2081/…../……   CHECKED BY