Embedded OS Implementation, Fall 2022

Project #3 (due Dec. 18, 2022 (Sunday) 12:00)

[PART I] NPCS Implementation

Objective:

Implement the non-preemptible critical section (NPCS) based on the RM scheduler in uC/OS-II.

Problem Definition:

uC/OS-II uses a variation of the priority inheritance protocol to deal with priority inversions. In this assignment, you are going to implement the NPCS based on the RM scheduler in uC/OS-II.

Consider the two examples and observe how the task suffers the scheduler delay.

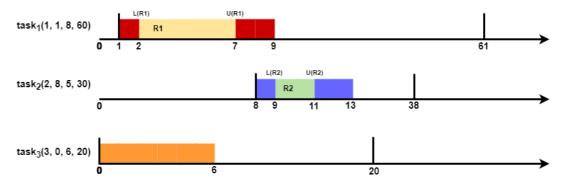
Periodic Task Set = { task_{ID} (ID, arrival time, execution time, period, R1 lock, R1 unlock, R2 lock, R2 unlock) }

X L(**R**#): Lock resource #, **U**(**R**#): Unlock resource #

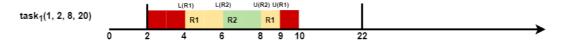
Example Task Set $1 = \{ task_1(1, 1, 8, 60, 1, 6, 0, 0), \}$

 $task_2(2, 8, 5, 30, 0, 0, 1, 3),$

task₃ (3, 0, 6, 20, 0, 0, 0, 0) }



Example Task Set 2 = { $task_1 (1, 2, 8, 20, 2, 7, 4, 6), task_2 (2, 0, 11, 40, 5, 8, 1, 9)}$





The correctness of schedule results of examples. Note the testing task set might not be thesame as the given example task set.

EX 1:

Tick		urrentTask ID	NextTask ID
6	Completion	task(3)(0)	task(1)(0)
7	Task1 get R1		
12	Task1 release R1		
12	Preemption	task(1)(0)	task(2)(0)
13	Task2 get R2		
15	Task2 release R2		
17	Completion	task(2)(0)	task(1)(0)
19	Completion	task(1)(0)	task(63)
20	Preemption	task(63)	task(3)(1)
26	Completion	task(3)(1)	task(63)
38	Preemption	task(63)	task(2)(1)
39	Task2 get R2		
41	Task2 release R2		
41	Preemption	task(2)(1)	task(3)(2)
47	Completion	task(3)(2)	task(2)(1)
49	Completion	task(2)(1)	task(63)
60	Preemption	task(63)	task(3)(3)
66	Completion	task(3)(3)	task(1)(1)
67	Task1 get R1		
72	Task1 release R1		
72	Preemption	task(1)(1)	task(2)(2)
73	Task2 get R2		
75	Task2 release R2		
77	Completion	task(2)(2)	task(1)(1)
79	Completion	task(1)(1)	task(63)
80	Preemption	task(63)	task(3)(4)
86	Completion	task(3)(4)	task(63)
98	Preemption	task(63)	task(2)(3)
99	Task2 get R2		

```
task( 1)( 0)
6
7
12
12
13
15
17
        Completion
                          task( 3)( 0)
        Taskl get R1
        Taski release Ri
        Preemption
                          task( 1)( 0)
                                                task( 2)( 0)
        Task2 get R2
Task2 release R2
Completion tas
                          task( 2)( 0)
task( 1)( 0)
                                                task(1)(0)
19
        Completion
                                                task(63)
                                                task(3)(1)
task(63)
20
        Preemption
                          task(63)
                          task(3)(1)
task(63)
26
        Completion
38
                                                task( 2)( 1)
        Preemption
        Task2 get R2
Task2 release R2
39
41
                          task( 2)( 1)
task( 3)( 2)
task( 2)( 1)
41
        Preemption
                                                task( 3)( 2)
task( 2)( 1)
47
        Completion
                                                task(63)
task(3)(3)
task(1)(1)
49
        Completion
60
        Preemption
                          task(63)
                          task( 3)( 3)
66
        Completion
67
        Taskl get R1
72
72
73
75
77
        Task1 release R1
        Preemption
                          task( 1)( 1)
                                                task(2)(2)
        Task2 get R2
Task2 release R2
                          task(2)(2)
task(1)(1)
        Completion
                                                task( 1)( 1)
        Completion
                                                task(63)
                                                task(3)(4)
task(63)
task(2)(3)
80
                          task(63)
task(3)(4)
        Preemption
86
        Completion
98
                          task(63)
        Preemption
99
        Task2 get R2
```

EX 2:

Tick	Event	CurrentTask ID	NextTask ID
1	Task2 get R2		
5	Task2 get R1		
8	Task2 release R	1	
9	Task2 release R	2	
9	Preemption	task(2)(0)	task(1)(0)
11	Task1 get R1		
13	Task1 get R2		
15	Task1 release R	2	
16	Task1 release R		
17	Completion	task(1)(0)	task(2)(0)
19	Completion	task(2)(0)	task(63)
22	Preemption	task(63)	task(1)(1)
24	Task1 get R1		
26	Task1 get R2		
28	Task1 release R	2	
29	Task1 release R	1	
30	Completion		task(63)
40	Preemption	task(63)	task(2)(1)
41	Task2 get R2		
45	Task2 get R1		
48	Task2 release R		
49	Task2 release R		
49	Preemption	task(2)(1)	task(1)(2)
51	Task1 get R1		
53	Task1 get R2		
55	Task1 release R		
56	Task1 release R		
57	Completion	task(1)(2)	task(2)(1)
59	Completion	task(2)(1)	task(63)
62	Preemption	task(63)	task(1)(3)
64	Task1 get R1		
66	Task1 get R2		
68	Task1 release R		
69	Task1 release R		1 ((40)
70	Completion	task(1)(3)	
80	Preemption	task(63)	task(2)(2)
81	Task2 get R2		
85	Task2 get R1	1	
88	Task2 release R		
89	Task2 release R		+==!((1)(4)
89	Preemption	task(2)(2)	task(1)(4)
91	Task1 get R1		
93 95	Task1 get R2	2	
95 96	Task1 release R Task1 release R		
96	Completion	1 task(1)(4)	task(2)(2)
99			task(2)(2) task(63)
99	Completion	task(2)(2)	Cask(63)

1 5 8 9 9	Task2 get R2 Task2 get R1 Task2 release Task2 release	R2			
13 15	Preemption Task1 get R1 Task1 get R2 Task1 release	R2	(0)	task(1)(0)
16 17	Taskl release Completion	кі task(1)	(0)	task(2)(0)
19 22	Completion Completion Preemption Task1 get R1	task(2) task(63)	(0)	task(63) task(-1)(1)
24 26 28 29	Taski get K2 Taski release Taski release	R2 R1			
30	Completion Preemption	task(1)	(1)	task(63)	1)
40 41 45 48	Task2 get R2 Task2 get R1 Task2 release	R1		task(2)(1)
49 49	Task2 release Preemption	R2 task(2)	(1)	task(1)(2)
51 53 55 56 57	Taski get R1 Taski get R2 Taski release Taski release	R2	. 17	040A(1)(2,
57	Completion	task(1)	(2)	task(_2)(1)
59 62	Completion Completion Preemption	task(2) task(63)	(1)	task(63) task(1)(3)
64 66 68 69	Taskl get Rl Taskl get R2 Taskl release	R2			
70 80	Completion	task(1)	(3)	task(63)	2)
81 85 88	Completion Preemption Task2 get R2 Task2 get R1 Task2 release	R1		task(2)(2)
89 89 91 93	Task2 release Preemption Task1 get R1 Task1 get R2	task(2)	(2)	task(1)(4)
95 96	Taski release Taski release	R1			
97 99	Completion Completion	task(1) task(2)	(4) (2)	task(2)(task(63)	2)
	•	, -,		, ,	

A report that describes your implementation (please attach the screenshot of the code and **MARK** the modified part).

最一開始先新建兩個 event 去個別代表資源 R1 及資源 R2·而宣告位子必須在 main.c 中為global variable 的型態·

而初始化若是太早可能造成程式有問題,因此要在 TCB、 task 等都初始並創建完後再初始化 event R1 及 R2 才行,且 R1 和 R2 是不同資源,因此不能採用 R=OSSemCreate(2)去設定:

```
⊡int main (void)
##if OS TASK NAME EN > Ou
  CPU_INTO8U os_err;
     CPU_IntInit();
     Mem_Init();
    CPU_IntDis();
     CPU_Init();
     OSInit();
     OutFileInit();
     InputFile();
     Task_STK = malloc(TASK_NUMBER * sizeof(int*));
     for (n = 0; n < TASK_NUMBER; n++) {
         Task_STK[n] = malloc(TASK_STACKSIZE * sizeof(int));
         OSTaskCreateExt(task2,
            &TaskParameter[n],
             &Task_STK[n][TASK_STACKSIZE - 1],
             TaskParameter[n].TaskPriority,
             TaskParameter[n].TaskID,
             &Task_STK[n][0],
             TASK_STACKSIZE,
             &TaskParameter[n],
             (OS_TASK_OPT_STK_CHK | OS_TASK_OPT_STK_CLR));
      //AddedCodePA3
     R1 = OSSemCreate(1);
     R2 = OSSemCreate(1);
```

因為是想透過Semaphore去達成搶到資源時,不讓其他task搶走,因此需要修改main.c下的task,使其不單單只有做delay的功能:

```
| Second | S
```

此外,在 ucos_ii.h 中宣告所需要用到的讀檔參數以及 R1、R2 的 Lock 與 Unlock 相對時間,以便後續進行是否執行 semaphore 的判斷:

其中下圖中的OSTCBUsingResource表示該 task 是否正在使用資源,0為否1為是:

```
OSTCBCyclesExecution; /* Setting about Execution Time */
                OSTCBCyclesCount;
INT32U
                OSTCBCyclesArrive;
                OSTCBCvclesEnd:
                OSTCBJobNumber;
                OSTCBCyclesPeriod;
                OSTCBCyclesSwitchStart; /* To know #swich the cycle start */
                OSTCBMyTaskCtxTimes;
                OSTCBR1Lock;
                OSTCBR1Unlock;
                OSTCBR2Lock;
                OSTCBR2Unlock;
                OSTCBUs ingResource;
                OSTCBOriPrio;
                OSTCBDeadline:
```

而在參數宣告完之後,需在OS_TCBInit 中進行初始化,也必須在此新增讀檔內容並存取資源 R1 及資源 R2 的 Unlock 和 Lock 時間:

```
int TaskInfo[INFO], k, 1 = 0;
          TASK_NUMBER = 0;
          while (!feof(fp) && prio != 63)
              fgets(str, sizeof(str) 1, fp);
ptr = strtok_s(str, " ", &pTmp);
while (ptr != NULL)
                   TaskInfo[k] = atoi(ptr);
ptr = strtok_s(NULL, " ", &pTmp);
/*printf("Info: %d\n", task_inf[i]);*/
                   if (k = 0) {
TASK_NUMBER++;
                        TaskParameter[1].TaskID = TASK_NUMBER;
                       TaskParameter[1].TaskArriveTime = TaskInfo[k];
                        TaskParameter[1].TaskExecutionTime = TaskInfo[k];
                   else if (k = 3) {
    TaskParameter[1].TaskPeriodic = TaskInfo[k];
    TaskParameter[1].TaskPriority = TaskInfo[k]; //Initial Priority=Period
                       TaskParameter[1].TaskRlLock = TaskInfo[k];
                       TaskParameter[1].TaskRlUnlock = TaskInfo[k];
                       TaskParameter[1].TaskR2Lock = TaskInfo[k];
                   TaskParameter[1].TaskR2Unlock = TaskInfo[k];
            if (prio != 63) {
               unsigned int delay = TaskParameter[id - 1].TaskArriveTime;
                unsigned int exetime = TaskParameter[id - 1].TaskExecutionTime;
               ptcb->OSTCBCyclesExecution = exetime;
               ptcb->OSTCBCyclesCount = Ou;
               ptcb->OSTCBJobNumber = Ou;
               ptcb->OSTCBCyclesEnd = Ou;
                ptcb->OSTCBCyclesSwitchStart = Ou;
               ptcb->OSTCBMyTaskCtxTimes = Ou;
               ptcb->OSTCBCyclesPeriod = TaskParameter[id - 1].TaskPeriodic;
                ptcb->OSTCBCyclesArrive = TaskParameter[id - 1].TaskArriveTime
П
                ptcb->OSTCBR1Lock = TaskParameter[id - 1].TaskR1Lock;
                ptcb->OSTCBR1Unlock = TaskParameter[id - 1].TaskR1Unlock;
                ptcb->OSTCBR2Lock = TaskParameter[id - 1].TaskR2Lock;
                ptcb->OSTCBR2Unlock = TaskParameter[id - 1].TaskR2Unlock;
                ptcb->OSTCBUsingResource = 0;
                ptcb->OSTCBDeadline = ptcb->OSTCBCyclesArrive + TaskParameter[id - 1].TaskPeriodic;
                while (ptcb->OSTCBDly != delay) {
                     ptcb->OSTCBD1y++;
```

接下來必須在 os_sem.c檔案 中去修改 OSSemPend 及 OSSemPost 以符合所要的打印格式。在 OSSemPend 中,因為 while 迴圈會在同一 tick 多次執行,因此會一直進入 OSSemPend 程式中,為了避免瘋狂打印的情形發生,要注意的是有一數值為 OSEventCnt 在第一次進入時為1之後變成0,可以用這個數值判斷在第一次進來時打印即可,後續再進來就不用繼續執行這些動作直接 return,除此之外,在取得資源時就將 OSTCBUsingResource 進行+1,代表這個任務使用了一個資源,因為有兩個資源的關係,因此最多OSTCBUsingResource 會加到2:

```
##if OS_CRITICAL_METHOD == 3u
 #endif
##ifdef OS_SAFETY_CRITICAL
  if (perr = (INT8U *)0) {
   OS_SAFETY_CRITICAL_EXCEPTION();
=#if OS_ARG_CHK_EN > Ou
    if (pevent == (OS_EVENT *)0) {
             *perr = OS_ERR_PEVENT_NULL;
     if (pevent->OSEventCnt == 0)//AddedCodePA3
               *perr = OS_ERR_EVENT_TYPE;
            OS_TRACE_SEM_PEND_EXIT(*perr);
               *perr = OS_ERR_PEND_ISR;
             OS_TRACE_SEM_PEND_EXIT(*perr);
     if (OSLockNesting > Ou) {
    *perr = OS ERR PEND LOCKED;
         OS_TRACE_SEM_PEND_EXIT(*perr);
        *perr = OS_ERR_NONE;
OS_TRACE_SEM_PEND_EXIT(*perr);
             AGRECUGEPAS

((Output_err = fopen_s(&Output_fp, "./Output.txt", "a")) = 0) {

if ((OSTimeGet() = (OSTCBCur->OSTCBCyclesStart + OSTCBCur->OSTCBZLock - 1)) && OSTCBCur->OSTCBZLock != 0) {

printf("%2d\tTask%d get R2\n", OSTimeGet(), OSTCBCur->OSTCBId);

fprintf(Output_fp, "%2d\tTask%d get R2\n", OSTimeGet(), OSTCBCur->OSTCBId);

OSTCBCur->OSTCBLisingResource ++;

//printf("%d\n", OSTCBCur->OSTCBUsingResource);
```

在 OSSemPost 中設計概念與OSSemPend 類似,為了避免 task 下的 while 迴圈在同一tick 下多次進入,也會撰寫 if 判斷式去檢查是否為第一次進入,並在確定 Pend 時將 OSTCBUsingResource 進行 -1 的動作代表已釋放一個資源:

```
##if OS_CRITICAL_METHOD == 3u
 ____TEAL_METHOD == 3u
| OS_CPU_SR cpu_sr = 0u;
#endif
#if OS_ARG_CHK_EN > Ou

☐ if (pever
   if (pevent = (OS_EVENT *)0) {
           return (OS_ERR_PEVENT_NULL);
     OS_TRACE_SEM_POST_ENTER(pevent);
     if (pevent->OSEventCnt = 1)//AddedCodePA3
    return;
      if (pevent->OSEventType != OS_EVENT_TYPE_SEM) {    /* Validate event block type
    OS_TRACE_SEM_POST_EXIT(OS_ERR_EVENT_TYPE);
            return (OS_ERR_EVENT_TYPE);
       OS_ENTER_CRITICAL();
      if (pevent->OSEventGrp != Ou) {
            (void)OS_EventTaskRdy(pevent, (void *)0, OS_STAT_SEM, OS_STAT_PEND_OK);
           OS_Sched();
OS_TRACE_SEM_POST_EXIT(OS_ERR_NONE);
           return (OS_ERR_NONE);
       if (pevent->OSEventCnt < 65535u) {
            pevent->OSEventCnt++;
           OS_TRACE_SEM_POST_EXIT(OS_ERR_NONE);
            if ((Output_err = fopen_s(&Output_fp, "./Output.txt", "a")) = 0) {
   if ((OSTimeGet() = (OSTCBCur->OSTCBCyclesStart + OSTCBCur->OSTCBRUUnlock - 2)) && OSTCBCur->OSTCBRUUnlock != 0) {
                       printf("%2d\tTask%d release Rl\n", OSTimeGet() + 1, OSTCBCur->OSTCBId);
fprintf(Output_fp, "%2d\tTask%d release Rl\n", OSTimeGet() + 1, OSTCBCur->OSTCBId);
OSTCBCur->OSTCBUsingResource --;
                  else if ((OSTimeGet() = (OSTCBCur->OSTCBCyclesStart + OSTCBCur->OSTCBR2Unlock - 2)) && OSTCBCur->OSTCBR2Unlock != 0) {
                    printf("%2d\tTask%d release R2\n", OSTimeGet() + 1, OSTCBCur->OSTCBId);
fprintf(Output_fp, "%2d\tTask%d release R2\n", OSTimeGet() + 1, OSTCBCur->OSTCBId);
                       OSTCBCur->OSTCBUsingResource --:
                  }fclose(Output_fp);
            return (OS_ERR_NONE);
        OS EXIT CRITICAL();
        OS_TRACE_SEM_POST_EXIT(OS_ERR_SEM_OVF);
        return (OS_ERR_SEM_OVF);
```

因為在 Non-preemptive critical section, NPCS 中,哪個任務搶到資源則就要等該任務將資源釋放以後才能換其他任務去使用,因此在 OSIntExit 中需要新增一判斷,當 OSTCBCur->OSTCBUsingResource != 0 時代表正在使用資源,直接跳出 OSIntExit,避免正在使用資源的 task 被其他 task 打斷執行:

```
⊏void OSIntExit (void)
⊨#if OS_CRITICAL_METHOD == 3u
                     OS_CPU_SR cpu_sr = Ou;
                       if(OSTimeGet() == 100)
                                        OSTimeGet();
                   if (OSRunning == OS_TRUE) {
                       OS_ENIER_CRITICES(),
if (OSIntNesting > Ou) {
                                if (OSIntNesting — Ou && OSTCBCur->OSTCBCyclesTot != OSTCBCur->OSTCBCyclesExecution && OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCur->OSTCBCCur->OSTCBCCur->OSTCBCCur->OSTCBCCur->OSTCBCCur->OSTCBCCur->OSTCBCCur->OSTCBCCur->OSTCBCCur->OSTCBCCur->OSTCBCCur->OSTCBCCur->OSTCBCCur->OSTCBCCur->OSTCBCCur->OSTCBCCur->OSTCBCCCur->
                                                 if (OSLockNesting == Ou) {
                                                                          OS_SchedNew();
                                                                          OSTCBHighRdy = OSTCBPrioTb1[OSPrioHighRdy];
if (OSPrioHi
□#if OS_TASK_PROFILE_EN > Ou
                                                                            if (OSPrioHighRdy != OSPrioCur) {
                                                                                             OSTCBHighRdy->OSTCBCtxSwCtr++;
                                                                                             OSCtxSwCtr++;
                                                                                             OSTCBCur->OSTCBMyTaskCtxTimes++;
                                                                                              OSTCBHighRdy->OSTCBMyTaskCtxTimes++
```

[PART II] CPP Implementation

Objective:

Implement the ceiling-priority protocol (CPP) based on the RM scheduler in uC/OS-II.

Problem Definition:

uC/OS-II uses a variation of the priority inheritance protocol to deal with priority inversions. In this assignment, you are going to implement the CPP based on the RM scheduler in uC/OS-II.

Consider the two examples and observe how the task suffers the scheduler delay.

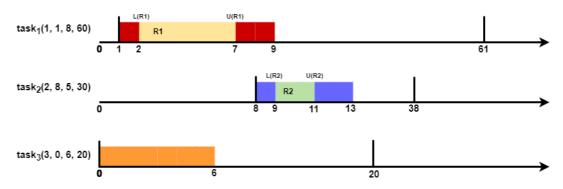
Periodic Task Set = { $task_{ID}$ (ID, arrival time, execution time, period, R1 lock, R1 unlock, R2 lock, R2 unlock) }

EXECUTE: X L(R#): Lock resource #, U(R#): Unlock resource #

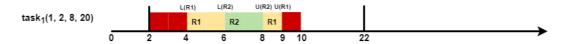
Example Task Set $1 = \{ task_1(1, 1, 8, 60, 1, 6, 0, 0), \}$

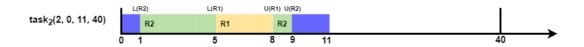
task₂ (2, 8, 5, 30, 0, 0, 1, 3),

task₃ (3, 0, 6, 20, 0, 0, 0, 0) }



Example Task Set 2 = { $task_1 (1, 2, 8, 20, 2, 7, 4, 6), task_2 (2, 0, 11, 40, 5, 8, 1, 9)}$





The correctness of schedule results of examples. Note the testing task set might not be the same as the given example task set.

EX 1:

Tick	Event	CurrentTask ID	NextTask ID	
6	Completion	task(3)(0)	task(1)(0)	
7	task1 get R1			12 to 11
8	Preemption	task(1)(0)	task(2)(0)	
9	task2 get R2			8 to 7
11	task2 release F	₹2		7 to 8
13	Completion	task(2)(0)	task(1)(0)	
17	task1 release F	₹1		11 to 12
19	Completion	task(1)(0)	task(63)	
20	Preemption	task(63)	task(3)(1)	
26	Completion	task(3)(1)	task(63)	
38	Preemption	task(63)	task(2)(1)	
39	task2 get R2			8 to 7
40	Preemption	task(2)(1)	task(3)(2)	
46	Completion	task(3)(2)	task(2)(1)	
47	task2 release F	R2		7 to 8
49	Completion	task(2)(1)	task(63)	
60	Preemption	task(63)	task(3)(3)	
66	Completion	task(3)(3)	task(1)(1)	
67	task1 get R1			12 to 11
68	Preemption	task(1)(1)	task(2)(2)	
69	task2 get R2			8 to 7
71	task2 release F	₹2		7 to 8
73	Completion	task(2)(2)	task(1)(1)	
77	task1 release F	R1		11 to 12
79	Completion	task(1)(1)	task(63)	
80	Preemption	task(63)	task(3)(4)	
86	Completion	task(3)(4)	task(63)	
98	Preemption	task(63)	task(2)(3)	
99	task2 get R2			8 to 7
100	Preemption	task(2)(3)	task(3)(5)	
				

6 7 8 9 11	Completion task(3)(0) task1 get R1	task(1)(0)	12 to 11
8	Preemption task(1)(0)	task(2)(0)	
9	task2 get R2		8 to 7
11	task2 release R2		7 to 8
13	Completion task(2)(0)	task(1)(0)	
17	taski release Ri		11 to 12
19	Completion task(1)(0)	task(63)	
20	Preemption task(63)	task(_3)(_1)	
26	Completion task(3)(1)	task(63)	
38	Preemption task(63)	task(2)(1)	A . B
39	task2 get R2	. 1/01/01	8 to 7
40	Preemption task(2)(1)	task(3)(2)	
46	Completion task(3)(2)	task(2)(1)	п
47	task2 release R2	1 ((2))	7 to 8
49	Completion task(2)(1)	task(63)	
60	Preemption task(63)	task(3)(3)	
66 67	Completion task(3)(3)	task(1)(1)	10 +- 11
67	taski get Ri	+==l-(2)(2)	12 to 11
68 60	Preemption task(1)(1)	task(2)(2)	0 + 0 7
69 71	task2 get R2 task2 release R2		8 to 7 7 to 8
73	Completion task(2)(2)	+oak(1)(1)	7 10 0
77	taski release Ri	task(1)(1)	11 to 12
79	Completion task(1)(1)	task(63)	11 to 12
80	Preemption task(63)	task(3)(4)	
86	Completion task(3)(4)	task(63)	
98	Preemption task(63)	task(2)(3)	
99	task2 get R2	(C)()	8 to 7
100	Preemption task(2)(3)	task(3)(5)	V VV 1
	1100mp 010n Oubh(B/(S/	· · · · · · · · · · · · · · · · · · ·	

EX 2:

```
NextTask ID
                                         CurrentTask ID
               task2 get R2
task2 get R1
task2 release R1
                                                                                                                     3 to 1
                                                                                                       1 to 3
               task2 release R2
             Preemption
task1 get R1
task1 get R2
task1 release R2
                                            task( 2)( 0)
                                                                         task( 1)( 0)
                                                                                                                     1 to 1
              task1 release R1
Completion
                                           task( 1)( 0)
task( 2)( 0)
task(63)
                                                                         task( 2)( 0)
                                                                         task(63)
task(1)(1)
              Completion
             Preemption
task1 get R1
task1 get R2
task1 release R2
                                                                                                                     4 to 1
1 to 1
                                                                                                       1 to 1
1 to 4
              task1 release R1
                                            task( 1)( 1)
task(63)
                                                                         task(63)
task( 2)( 1)
              Completion
Preemption
              task2 get R2
task2 get R1
task2 release R1
                                                                                                                     8 to 3
3 to 1
             task2 release R2
Preemption
task1 get R1
task1 get R2
task1 release R2
                                                                                                        3 to 8
                                            task( 2)( 1)
                                                                         task( 1)( 2)
                                                                                                                    1 to 1
                                                                                                       1 to 1
1 to 4
              task1 release R1
Completion
                                            task( 1)( 2)
task( 2)( 1)
task(63)
                                                                         task( 2)( 1)
task(63)
task( 1)( 3)
             Completion
Preemption
task1 get R1
task1 get R2
task1 release R2
                                                                                                                    1 to 1
              task1 release R1
Completion
                                                                                                        1 to 4
                                            task( 1)( 3)
task(63)
                                                                         task(63)
             Preemption
Preemption
task2 get R2
task2 get R1
task2 release R1
task2 release R2
                                                                         task( 2)( 2)
                                                                                                                     8 to 3
                                                                                                       1 to 3
                                                                                                        3 to 8
              Preemption
task1 get R1
task1 get R2
                                            task( 2)( 2)
                                                                         task( 1)( 4)
                                                                                                                    4 to 1
1 to 1
              task1 release R2
task1 release R1
                                                                                                       1 to 1
                                                                                                       1 to 4
              Completion task( 1)( 4)
Completion task( 2)( 2)
                                                                         task( 2)( 2)
task(63)
```

```
task2 get R2
task2 get R1
task2 release R1
                                                                                        8 to 3
 1
 5
                                                                                        3 to 1
                                                                              1 to 3
 8
         task2 release R2
                                                                               3 to 8
 9
          Preemption task(2)(0)
                                                   task( 1)( 0)
         taskl get R1
taskl get R2
11
                                                                                        4 to 1
13
                                                                                      1 to 1
         taski release R2
taski release R1
15
16
17
19
22
24
26
28
29
30
                                                                               1 to 1
                                                                               1 to 4
        Completion task(1)(0)
Completion task(2)(0)
Preemption task(63)
                                                      task( 2)( 0)
task(63)
                                                       task( 1)( 1)
        task1 get R1
task1 get R2
task1 release R2
task1 release R1
                                                                                        4 to 1
                                                                                        1 to 1
                                                                               1 to 1
                                                                               1 to 4
        Completion task( 1)( 1)
Preemption task(63)
task2 get R2
task2 get R1
task2 release R1
task2 release R2
Proemption task(2)(1)
                                                       task(63)
40
                                                       task( 2)( 1)
41
45
                                                                               8 to 3
3 to 1
48
                                                                               1 to 3
                                                                               3 to 8
          Preemption task(2)(1)
                                                       task( 1)( 2)
49
51
53
55
56
57
59
        taskl get R1
taskl get R2
                                                                                        4 to 1
                                                                                      1 to 1
        task1 release R2
task1 release R1
                                                                               1 to 1
                                                                               1 to 4
        taski release ki
Completion task(1)(2)
Completion task(2)(1)
Preemption task(63)
taski get Ri
taski get R2
                                                      task(2)(1)
task(63)
62
64
66
                                                       task( 1)( 3)
                                                                                        4 to 1
                                                                                        1 to 1
                                                                               1 to 1
68
69
70
         taski release Ri
                                                                               1 to 4
          Completion task(1)(3)
Preemption task(63)
                                                       task(63)
                                                       task(2)(2)
80
        task2 get R2
task2 get R1
task2 release R1
task2 release R2
81
                                                                                        8 to 3
85
                                                                                        3 to 1
                                                                               1 to 3
88
89
                                                                               3 to 8
89
91
93
95
          Preemption task(2)(2)
                                                     task( 1)( 4)
         taskl get R1
                                                                                        4 to 1
         taskl get R2
                                                                                       1 to 1
                                                                               1 to 1
         task1 release R2
96
         task1 release R1
                                                                               1 to 4
      Completion task( 1)( 4) task( 2)( Completion task( 2)( 2) task(63)
97
                                                      task(2)(2)
```

A report that describes your implementation (please attach the screenshot of the code and MARK the modified part).

一開始會先在 ucos_ii.h 宣告新的參數去儲存數值,會宣告全域變數去儲存 R1_PRIO、R2_PRIO、除此之外,撰寫了 Find_R1_R2_PRIO 去尋找 R1 和 R2 的優先權,而同樣的由於 task 執行時在同一秒會一直多次進入OSMutexPend,因此宣告了一個變數

OSEventEnterMutexCnt 在OS_EVENT 之下,去紀錄是否為第一次進入 Mutex。

```
⊟#if OS_LOWEST_PRIO <= 63u
⊟#else
 typedef INT16U OS_PRIO;
#endif
□#if (OS_EVENT_EN) && (OS_MAX_EVENTS > Ou)
typedef struct os_event {
      INT8U OSEventType;
              *OSEventPtr;
      INT16U OSEventCnt;
                                                  /* Group corresponding to tasks waiting for event to occur */
/* List of tasks waiting for event to occur */
      OS_PRIO OSEventGrp;
      OS_PRIO OSEventTbl[OS_EVENT_TBL_SIZE];
      int OSEventEnterMutexCnt;
⊨#if OS_EVENT_NAME_EN > Ou
               *OSEventName;
  OS EVENT:
  #endif
```

在宣告完變數之後會在 os_task.c 下進行初始化,會先宣告 TaskNumber 去記錄創建的任務數量,方便作優先權的給予,為了避免發生優先權重複的情形發生,因此在創建任務時原本都是以優先權=週期,按照創建的 task 順序依序給予63、62、61、60...等等的優先權。

之後也需在 os_core.c 的 OS_TCBInit 中去初始化新宣告的參數,像是將 OSTCBPrio 改成等於新賦予的值而並非週期,基本上所做的事和前面 os_task.c 建立 task 時所做的事情類似,同樣的在讀檔時根據創建的任務數去從優先權最低去給予,並初始化 R1、R2 相關的Lock time與 Unlock time,同時由於讀檔時不包含 idle task 因此優先權最低是從 62 開始給予,而 idle task 是每次執行都最優先建立的任務。

事前的一些參數宣告即設定都完成之後,在os_core.c 中撰寫兩個新的副程式去協助任務優先權的排序以及優先權的重新給予,因為一開始在OS_TCBInit給予優先權只考慮避免重複,未按照週期大小給予,因此需要重新初始化。

在副程式 Sort_Prio 中,一開始在 FixedPrio 會先藉由迴圈初始化成放置規定的優先權,也就是 4 的倍數,並再下面那個迴圈進行 sort,每次都把週期最小的任務找出來並依序將優先權透過 OSTaskChangePrio 去做更改。

在副程式 Find_R1_R2_PRIO 中主要負責初始化 R1 和 R2 的優先權,因為他們的優先權必須要由有用到資源的最高任務去決定,因此在這個副程式中會去找出使用到該資源的最高優先全為何,並判斷是否兩個資源都被同一個優先權最高的任務使用,若為是則保持 R1 優先權要較高故等於 task 優先權-3, R2在這時則會等於該任務的優先權-1。

最後則是修改 os mutex.c 中的 OSMutexPend 以及 OSMutexPost:

在 OSMutexCreate 中,為了記錄是否為第一次進入 Mutex 因此要在建立Event時將 OSEventEnterMutexCnt 先初始化成 0。

而在 OSMutexPend 中,會先新增一判斷,當 OSEventEnterMutexCnt =1時代表並非第一次進入 OSMutexPend 中,會直接進行 return ,並新增打印相關的程式碼,使其打印符合格式,除此之外由於 OSMutexPend 原本只會在有 task 要進去搶資源時才會將搶到資源的task 優先權提升,因此要將優先權提升的相關程式碼更改位置上移至進來時就將搶到資源的task 的優先權先提升。

```
INT8U mprio;
BOOLEAN rdy;
OS_TCB *ptcb;
OS_EVENT *pevent2;
 ⊟#if OS_CRITICAL_METHOD == 3u
         OS_CPU_SR cpu_sr = Ou;
##ifdef OS SAFETY CRITICAL
       if (perr == (INT8U *)0) {
    OS_SAFETY_CRITICAL_EXCEPTION();
    if (pevent->OSEventEnterMutexCnt == 1) //AddedCodePA3part2
    return;
#if OS_ARG_CHK_EN > Ou
if (pevent == (OS_EVENT *)O) {
    *perr = OS_ERR_PEVENT_NULL
    return;
    OS TRACE MUTEX PEND ENTER(pevent, timeout):
    }
if (OSIntNesting > Ou) {
  *perr = OS_ERR_PEND_ISR;
  OS_TRACE_MUTEX_PEND_EXIT(*perr);
     if (OSLockNesting > Ou)
        *perr = OS_ERR_PEND_LOCKED;
OS_TRACE_MUTEX_PEND_EXIT(*perr);
    ] else if ((OSTimeGet() == (OSTCBOur->OSTCBCyclesStart + OSTCBOur->OSTCBRLOck - 1)) && OSTCBOur->OSTCBRLock != 0) {
    printf("KOAlthockEd get El \t\t\t\t\t\t\t\t\", OSTimeGet(), OSTCBOur->OSTCBId);
    fprintf(Output_fp, "KOAlthockEd get El \t\t\t\t\t\t\t\", OSTimeGet(), OSTCBOur->OSTCBId);
    OSTCBOur->OSTCBId; pagesource+;
          ; }
}fclose(Output_fp);
pevent->OSEventEnterMutexCnt++;
```

同意的在 OSMutexPost 也為了避免瘋狂打印會去判斷若 OSEventEnterMutexCnt = 0 時代表並非第一次進入這個副程式,就直接 return 跳出,至於其餘有新增的程式碼則是與打印相關的程式碼。

```
| Description | Control |
```

最後則是針對 main program 的撰寫,要注意的是在創建 R1、R2 的 OSMutexCreate 要放置在 task 創建完成之後,因為此時 R1、R2 的 priority 也初始化完成。

在 task 執行的 while 迴圈中,因為會出現 task 搶到2個資源的情形,因此在 semaphore 所撰寫的資源 release 的判斷式在 Mutex 不適用,需改成當該任務已執行的秒數=資源 unlock 的時間時才能釋放資源。

[PART III] Performance Analysis [10%]

Compare the scheduling behaviors between NPCS and CPP with PART I and PART II results.

NPCS 因為在搶到資源後就會禁止在執行資源時被其他 task 打斷,雖然可以完整執行完資源,但也因此可能導致 high priority task 在 ready 之後因為 low priority task 正在使用資源而無法遲遲執行,似 Example 1 的狀況,此時任務最多都只有使用到一個資源,而 CPP 在使用R1 資源時是可以被打斷去執行 high priority task (Example 1 中 的 T2)。

而由 Example 2 去做討論,CPP 會賦予 R1、R2 資源優先權 ,並該優先權是由使用到該資源且優先權最高的 task 去決定,因此若是 high priority task 使用到許多資源,那麼會盡快使 low priority task 將拿到的資源用完釋放,避免拉長 priority task 的完成時間。

Explain how NPCS and CPP avoid the deadlock problem.

NPCS 會使搶到資源的 task 優先執行並釋放 · 像是拿到 R1 資源就要等 R1 資源用完才有可能才會換其他 task 執行 · 因此若是在使用 R1 過程中穿插使用了其他資源也不會因此打斷該 task 的執行 · 所以能避免 deadlock 的發生 ·

在 CPP 中,會賦予資源優先權,並在 task 搶到資源時提升 task 的 priority ,且優先權是由使用到該資源且優先權最高的 task 去決定,可以讓搶到資源的 task 在尚未使用完該資源時都保持較高的優先權,因此能免 task 雙方拿著未使用完的資源,卻需要他人正在執行的資源狀況 (deadlock)的發生。

Credit:

[PART I] NPCS Implementation [40%]

- The correctness of schedule results of examples. Note the testing task set might not be the same as the given example task set. (20%)
- A report that describes your implementation (please attach the screenshot of the code and **MARK** the modified part). (20%)

[PART II] CPP Implementation [50%]

- The correctness of schedule results of examples. Note the testing task set might not be the same as the given example task set. (20%)
- A report that describes your implementation (please attach the screenshot of the code and **MARK** the modified part). (30%)

[PART III] Performance Analysis [10%]

- Compare the scheduling behaviors between NPCS and CPP with PART I and PART II results. (5%)
- Explain how NPCS and CPP avoid the deadlock problem. (5%)
- ***** You must modify the source code.
- **Standard input and output filenames in the project are necessary for the checker. Please check the file names before submitting.**

```
#define INPUT_FILE_NAME "./TaskSet.txt"
#define OUTPUT_FILE_NAME "./Output.txt"
```

***** Please set the parameter, INFO, as 10 to read more task information.

```
#define INFO 10
```

Please set the system end time as 100 seconds in this project.

```
#define SYSTEM_END_TIME 100
```

- **X** You must check your project can produce the correct output file.
- **We only use two share resources in this project.**
- **We will use different task sets to verify your code.**
- ***** You will submit two μC/OS-II projects for PART I and PART II, respectively.

Project submit:

Submit to Moodle

Submit deadline: <u>Dec. 18, 2022 (Sunday) 12:00</u> File name format: RTOS_Myyyddxxx_PA3.zip

RTOS_Myyyddxxx_PA3.zip includes:

- The report (RTOS_Myyyddxxx_PA3.pdf).
- Folder with the executable μC/OS-II project (**RTOS_Myyyddxxx_PA3_NPCS**).
- Folder with the executable μC/OS-II project (**RTOS_Myyyddxxx_PA3_CPP**).