

1- Using Analytical Methods:

- CPU Hyperperiod

$$\text{Hyperperiod} = \text{LCM}(50, 50, 100, 20, 10, 100) = 100$$

LCM: Least Common Multiplier (for all tasks periodicities in this case)

Task	Occurrence During Hyperperiod	Execution Time
Button 1 Monitor	2	29us
Button 2 Monitor	2	29us
Periodic Transmitter	1	93us
UART Transmitter	5	30us
Load 1 Simulation	10	5ms
Load 2 Simulation	1	12ms

- CPU Load

It is the total execution time during hyperperiod divided by hyperperiod

$$U = [(29\mu\text{s} \times 2) + (29\mu\text{s} \times 2) + (93\mu\text{s} \times 1) + (30\mu\text{s} \times 5) + (5\text{ms} \times 10) + (12\text{ms} \times 1)] / 100\text{ms}$$
$$= 0.624 * 100\% = 62\%$$

- System Schedulability

We say the system is schedulable if $U < U_{\text{rm}}$

$$U = 0.624 < U_{\text{rm}} = 0.735$$

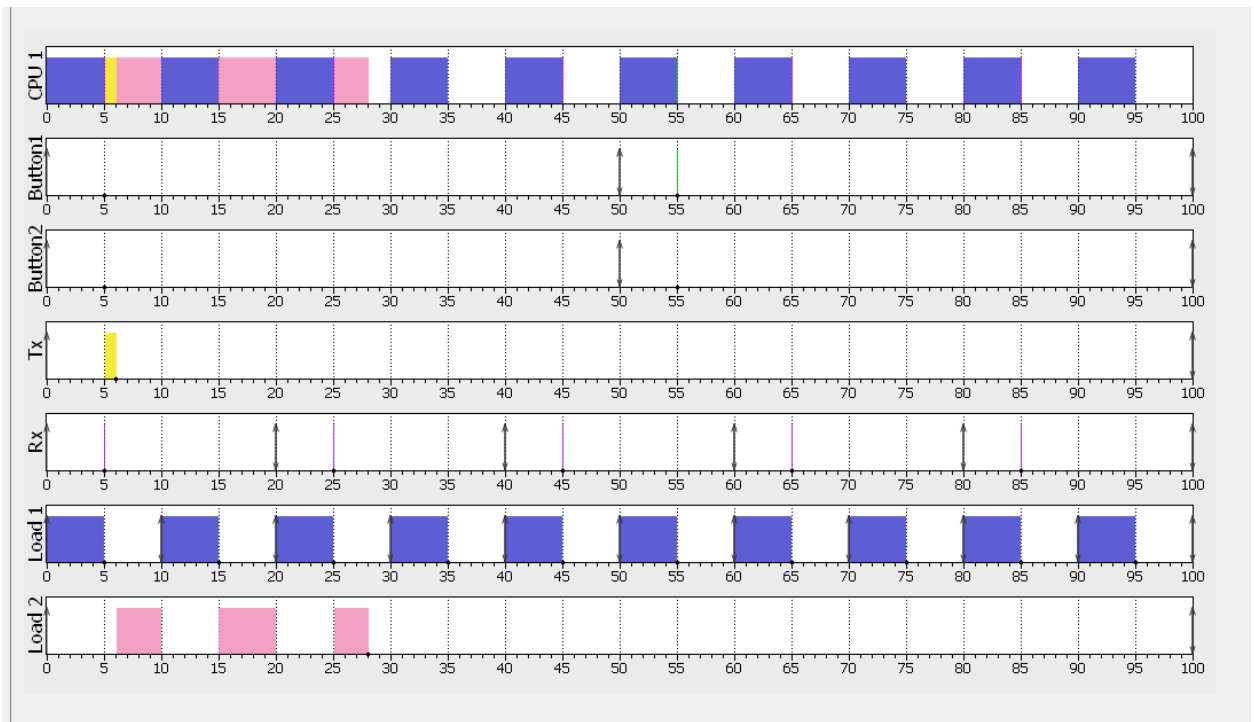
Hence, the system is schedulable

2- Using Simso Offline Simulator

- CPU Load

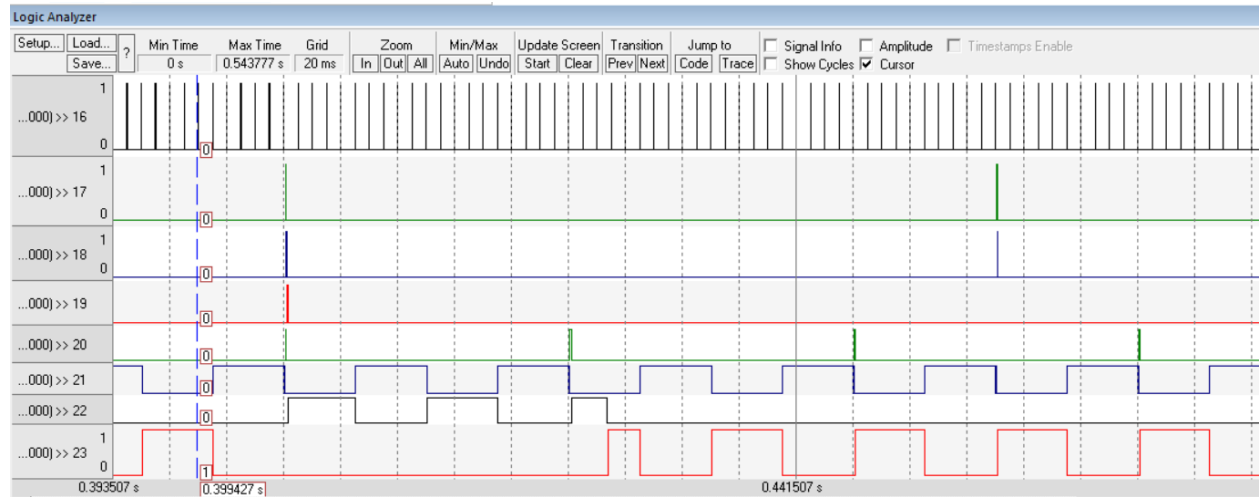
	Total load	Payload	System load
CPU 1	0.6320	0.6320	0.0000
Average	0.6320	0.6320	0.0000

- Gantt Chart



3- Keil Simulator

- Logic Analyzer



- CPU Load

Name	Value	Type
total_exeTime	0x00072699	uint
cpu_load	0x0000003F	uint
<Enter expression>		

4- Using time-demand analysis

First of all, We must define critical instant where all the tasks are schedulable at the same time.

critical instant = 100

Task	Periodicity	Execution Time	Time Provided
Button 1 Monitor	50ms	29us	50ms
Button 2 Monitor	50ms	29us	50ms
Periodic Transmitter	100ms	93us	100ms
UART Transmitter	20ms	30us	20ms
Load 1 Simulation	10ms	5ms	10ms
Load 2 Simulation	100ms	12ms	100ms

Then, we calculate worst response time for each task to check the schedulability.

If time demand is less than or equal to task periodicity then the task is schedulable.

- Button 1 Monitor:
 - $W(50) = 29\mu s + (50/10) * 5ms + (50/20) * 30\mu s = 25.059ms$
 - $25.059ms < 50ms$
 - Task is schedulable

- Button 2 Monitor:
 - $W(50) = 29\mu s + (50/10) * 5ms + (50/20) * 30\mu s + (50/50) * 29\mu s = 25.087ms$
 - $25.059ms < 50ms$
 - Task is schedulable

- Periodic Transmitter:
 - $W(100) = 93\mu s + (100/10) * 5ms + (100/20) * 30\mu s + (100/50) * 29\mu s + (100/50) * 29\mu s = 50.36ms$
 - $50.36ms < 100ms$
 - Task is schedulable

- UART Receiver:
 - $W(20) = 30\mu s + (20/10) * 5ms = 10.03ms$
 - $10.03ms < 20ms$
 - Task is schedulable

- Load 1 Simulation:
 - $W(10) = 5ms + 0 = 5ms$
 - $5ms < 10ms$
 - Task is schedulable

- Load 2 Simulation:
 - $W(100) = 12ms + (100/10) * 5ms + (100/20) * 30\mu s + (100/50) * 29\mu s + (100/50) * 29\mu s + (100/100) * 93\mu s = 62.45ms$
 - $62.45ms < 100ms$
 - Task is schedulable

All tasks of the system are schedulable and don't miss their deadline, so the system overall is **schedulable**.