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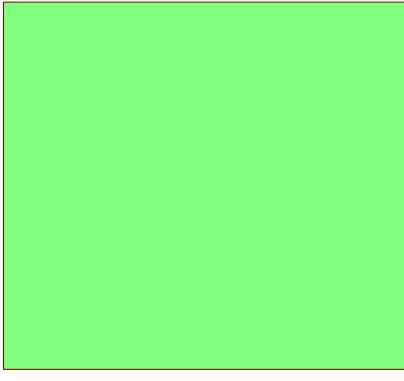
A

B

C

D

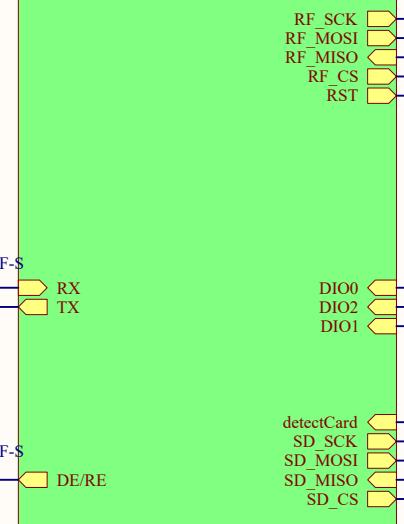
Designator
Power.SchDoc



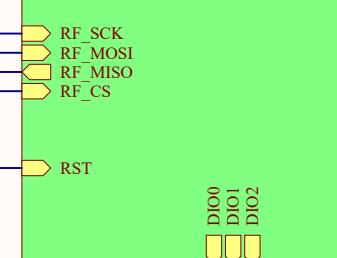
Rs485
MaxModule.SchDoc



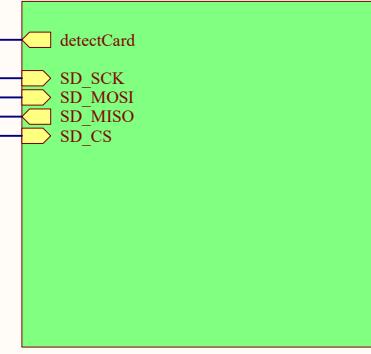
MCU
McuESP32.SchDoc



Lora
LoraRFM.SchDoc



Storage
SdCard.SchDoc



J1

RS_TX

RS_RX

TSW102-07-F-\$

RX

TX

J2

TSW-102-07-F-\$

DE/RE

RF_SCK
RF MOSI
RF MISO
RF CS
RST

DIO0

DIO2

DIO1

detectCard
SD_SCK
SD_MOSI
SD_MISO
SD_CS

Title

Size

A4

Number

Revision

Date: 7/28/2025

Sheet of

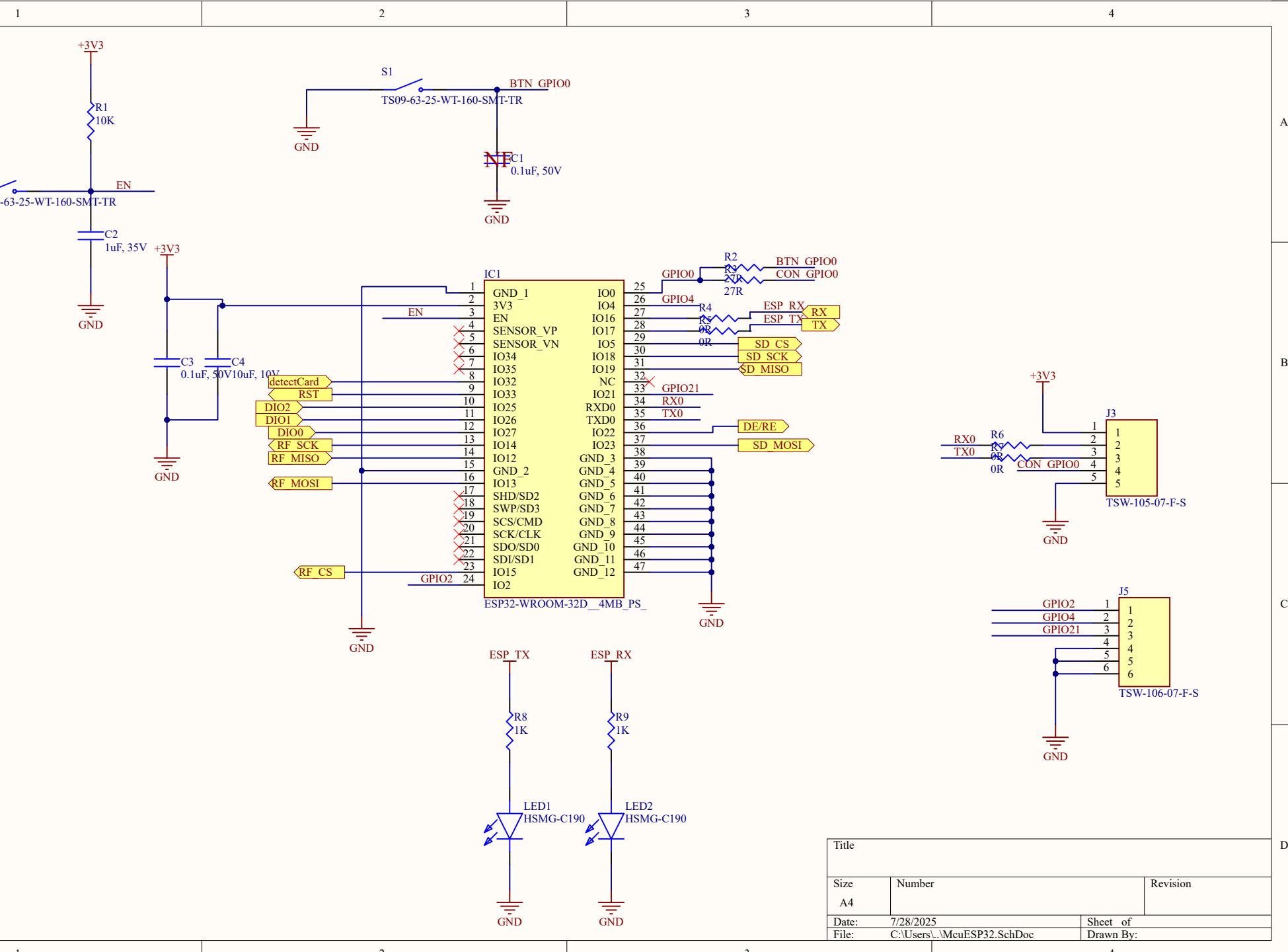
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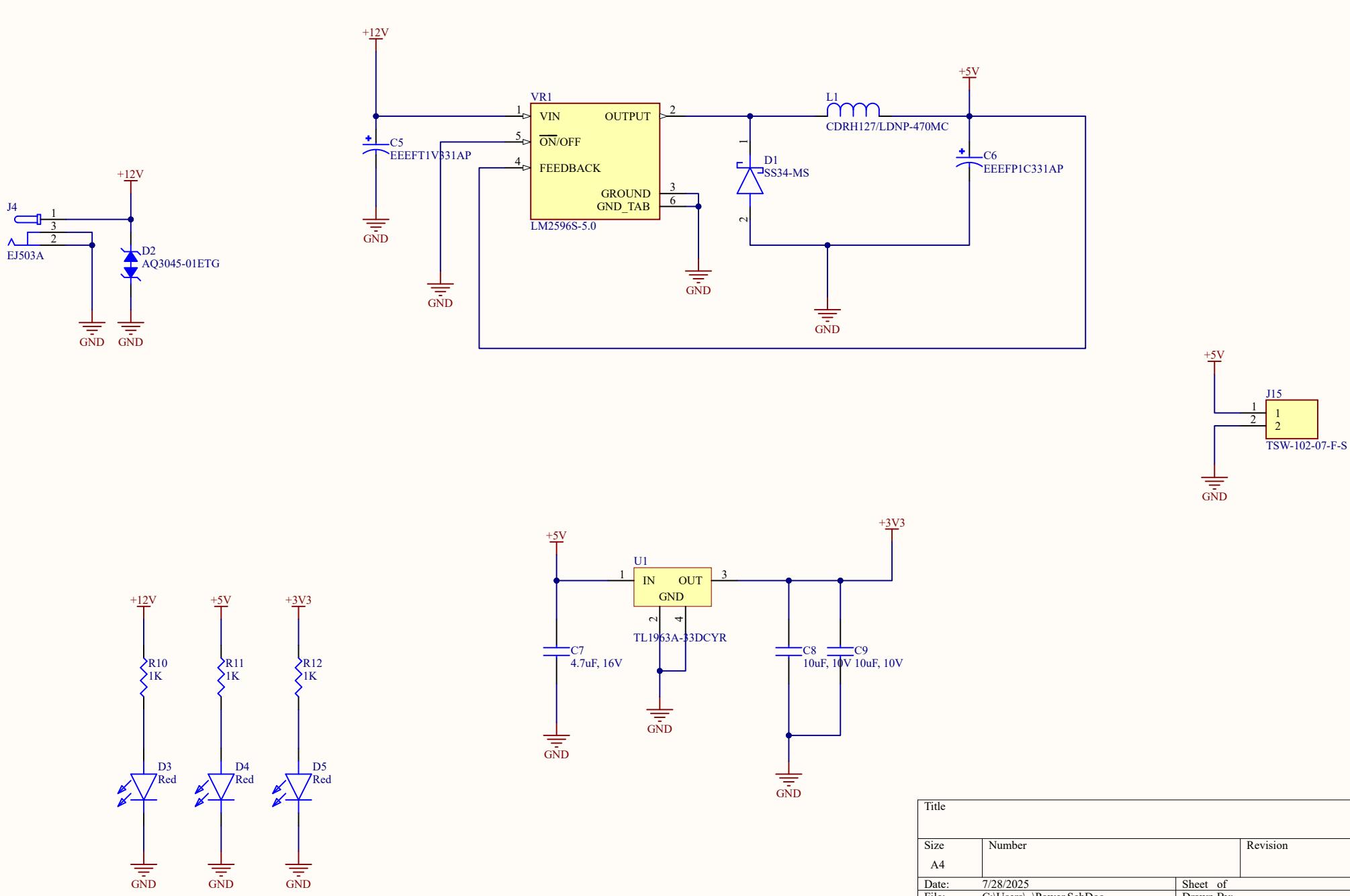
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Title		
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Date: 7/28/2025		Sheet of
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1

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A

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D

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A

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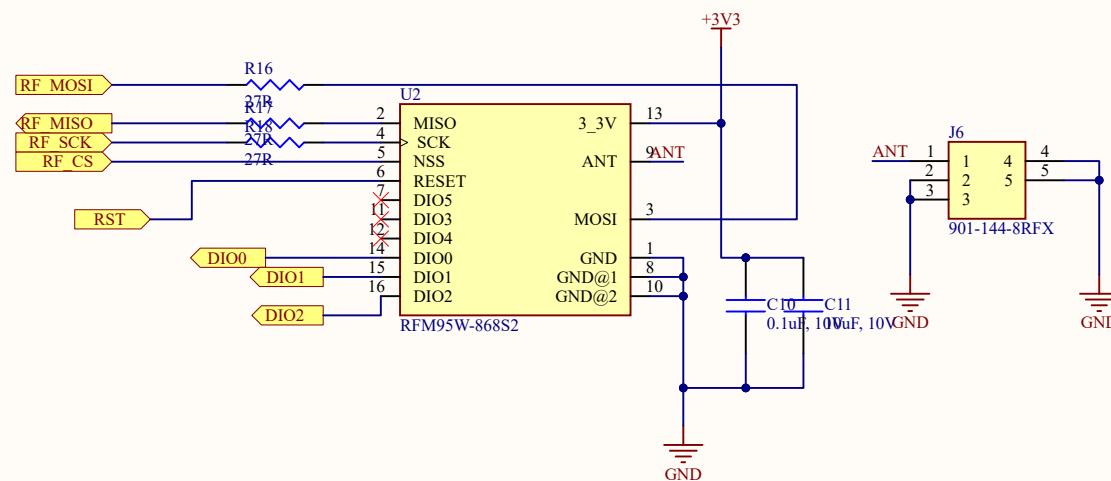
B

C

C

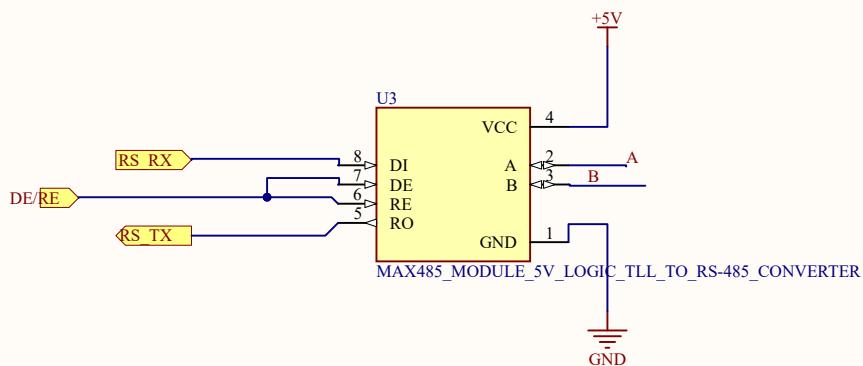
D

D

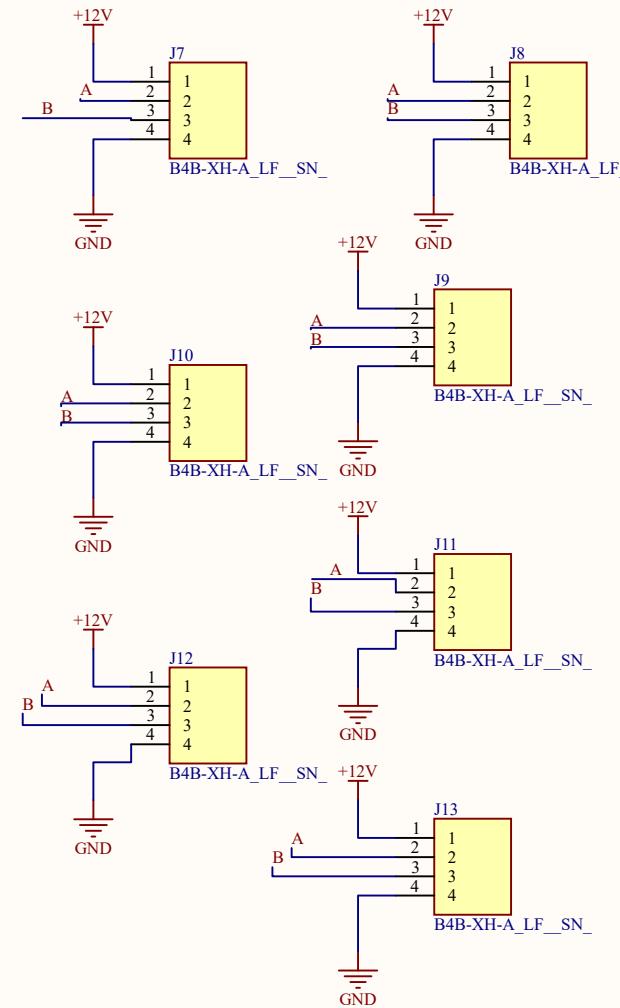


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Date: 7/28/2025		Sheet of
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A

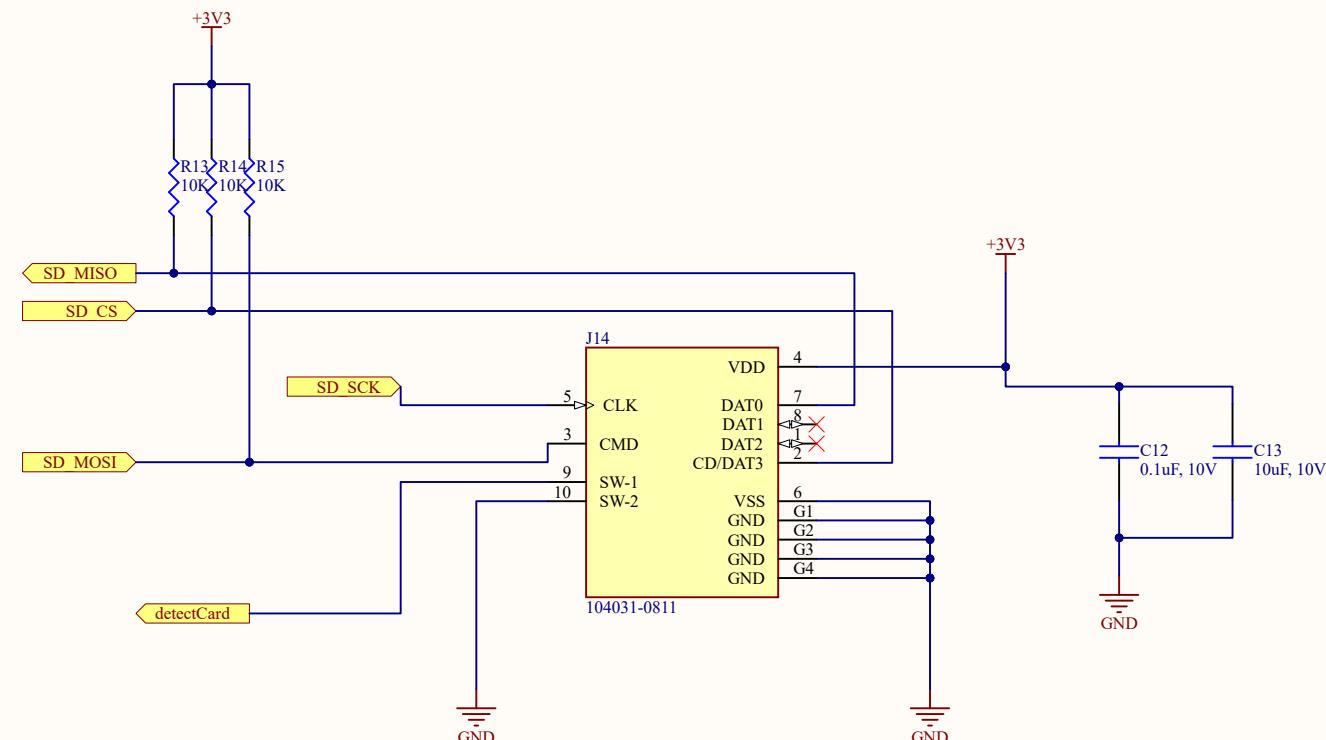


B



C

Title		
Size	Number	Revision
A4		
Date: 7/28/2025	Sheet of	
File: C:\Users\...\MaxModule.SchDoc		Drawn By:



Title		
Size A4	Number	Revision
Date: 7/28/2025		Sheet of
File: C:\Users\...\SdCard.SchDoc		Drawn By: