

MISR UNIVERSITY FOR SCIENCE AND TECHNOLOGY
COLLEGE OF ENGINEERING
MECHATRONICS DEPARTMENT



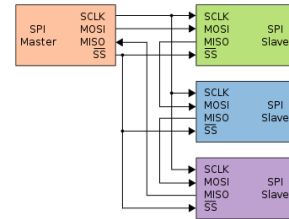
MTE 405 SENSORS AND MEASUREMENTS

Lab 7 – SPRING 2019

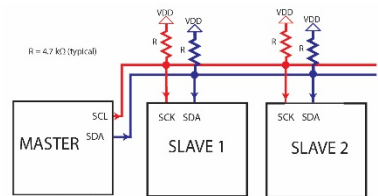
Lab 7

Goals Of The Lab

Advanced Measurements System



Serial Peripheral Interface (SPI)

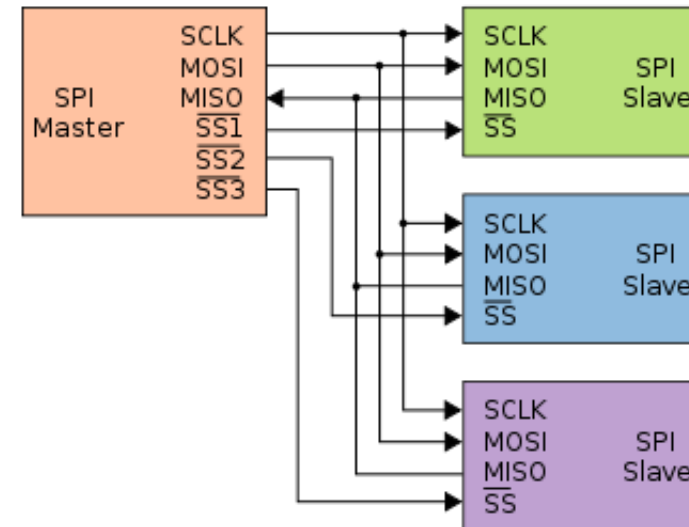


Inter-Integrated Circuit (I²C)

Lab 7

SPI

Serial Peripheral Interface



Lab 7

SPI



Sending data between **Microcontroller** and **peripherals**



Dedicated **Clock bus** (*CLK or SCK*)



Dedicated **Data bus** (*MISO and MOSI*)



Chip Select (CS) for selecting peripheral





Sending data between **Microcontroller** and **peripherals**



Dedicated **Clock bus** (*CLK or SCK*)



Dedicated **Data bus** (*MISO and MOSI*)



Chip Select (CS) for selecting peripheral



Common peripherals

- Accelerometers
- SD cards
- IO port expander

Lab 7 SPI

Clock speed is higher than I2C and UART



Sending data between **Microcontroller** and **peripherals**

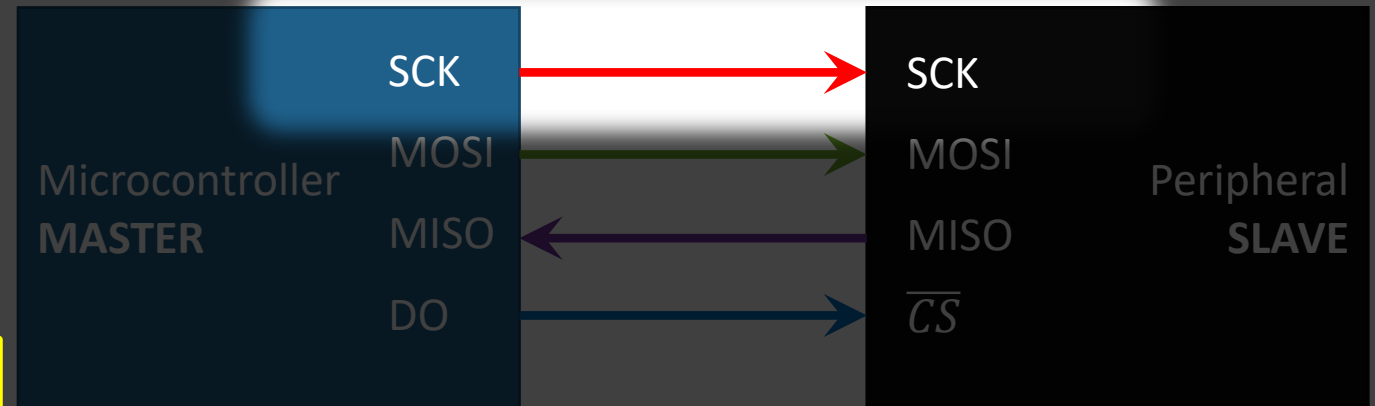


Dedicated **Clock bus** (CLK or SCK)



Dedicated **Data bus** (MISO and MOSI)

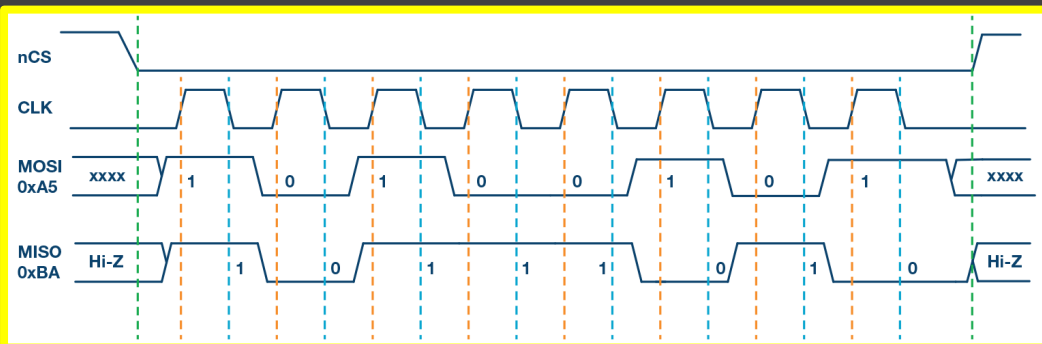
Clock is used for synchronization



CPOL : Clock polarity (when IDLE)

CPHA : Clock phase (when to shift data out)

Clock has 4 modes (**assignment**)



Lab 7

SPI



Sending data between **Microcontroller** and **peripherals**



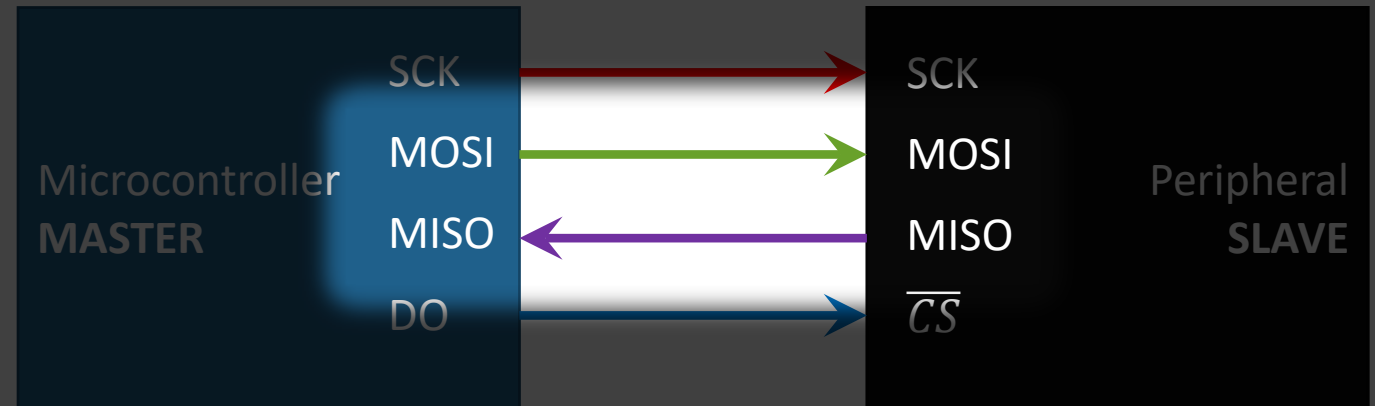
Dedicated **Clock bus** (*CLK or SCK*)



Dedicated **Data bus** (*MISO and MOSI*)



Chip Select (CS) for selecting peripheral



MOSI : Master Out (**Micro**) → Slave In (**Peripheral**)

MISO : Master In (**Micro**) ← Slave Out (**Peripheral**)

Lab 7

SPI



Sending data between **Microcontroller** and **peripherals**



Dedicated **Clock bus** (*CLK or SCK*)



Dedicated **Data bus** (*MISO and MOSI*)

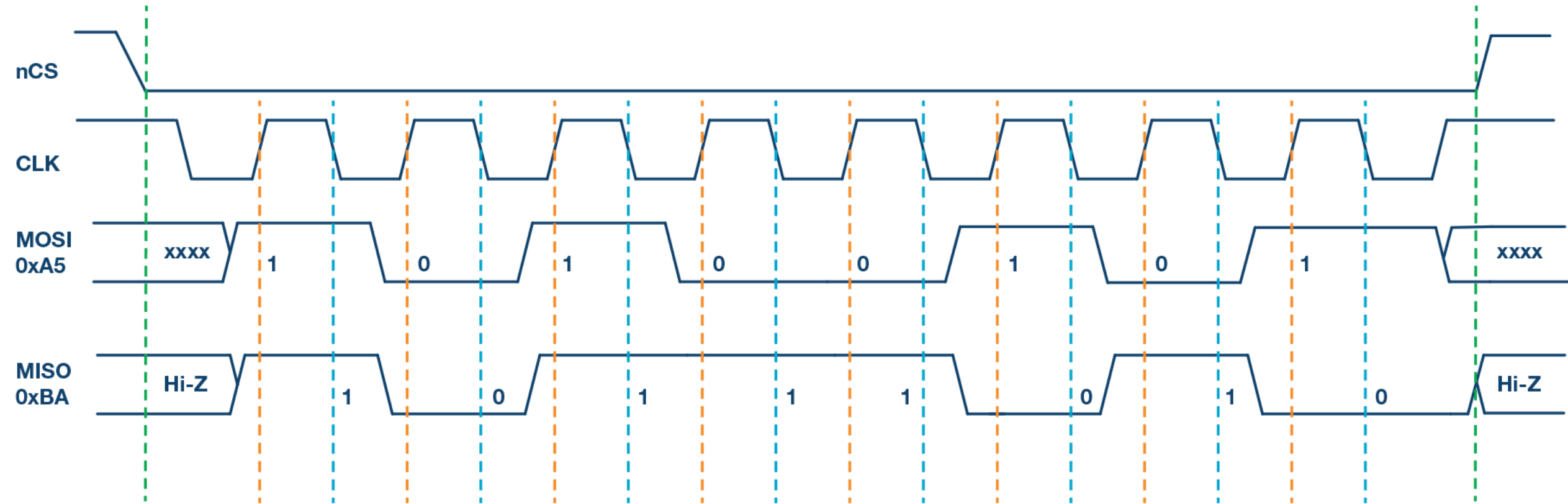


Chip Select (CS) for selecting peripheral

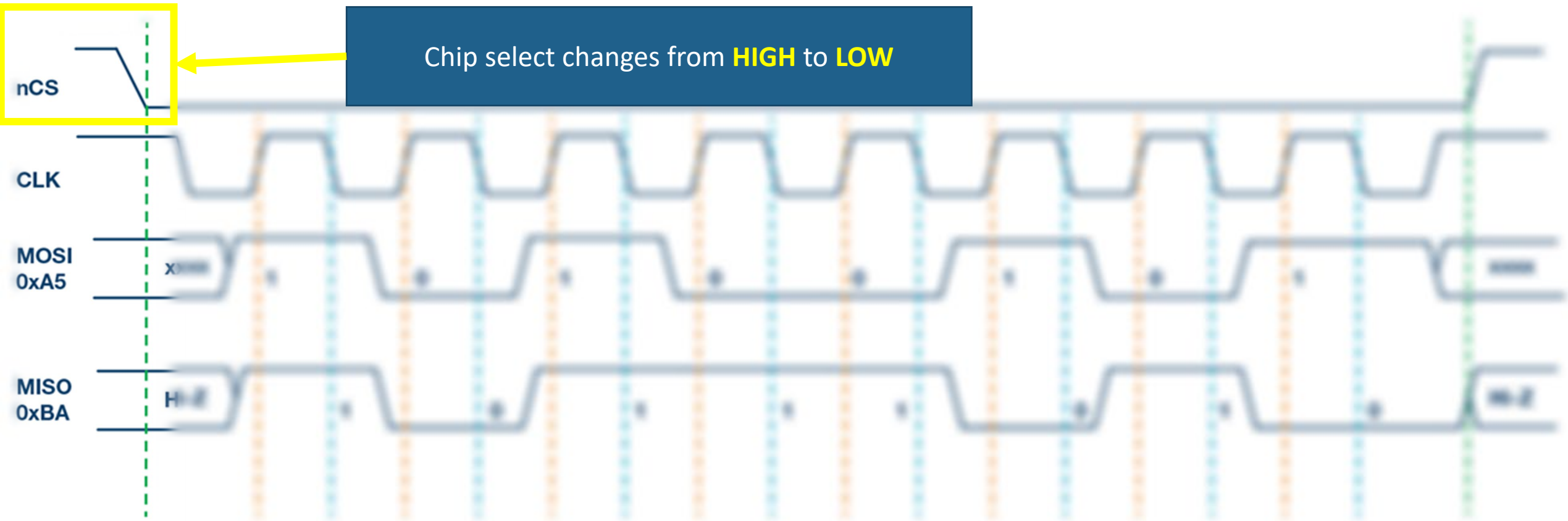


\overline{CS} : Chip Select (Active Low)

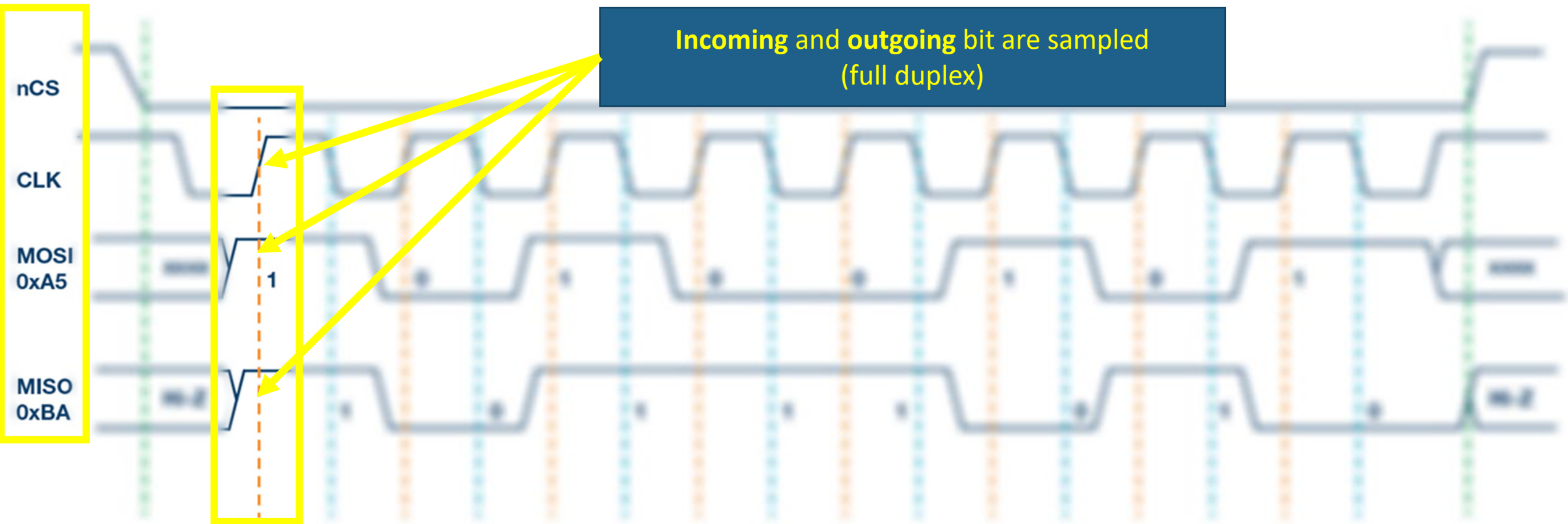
SPI Communication



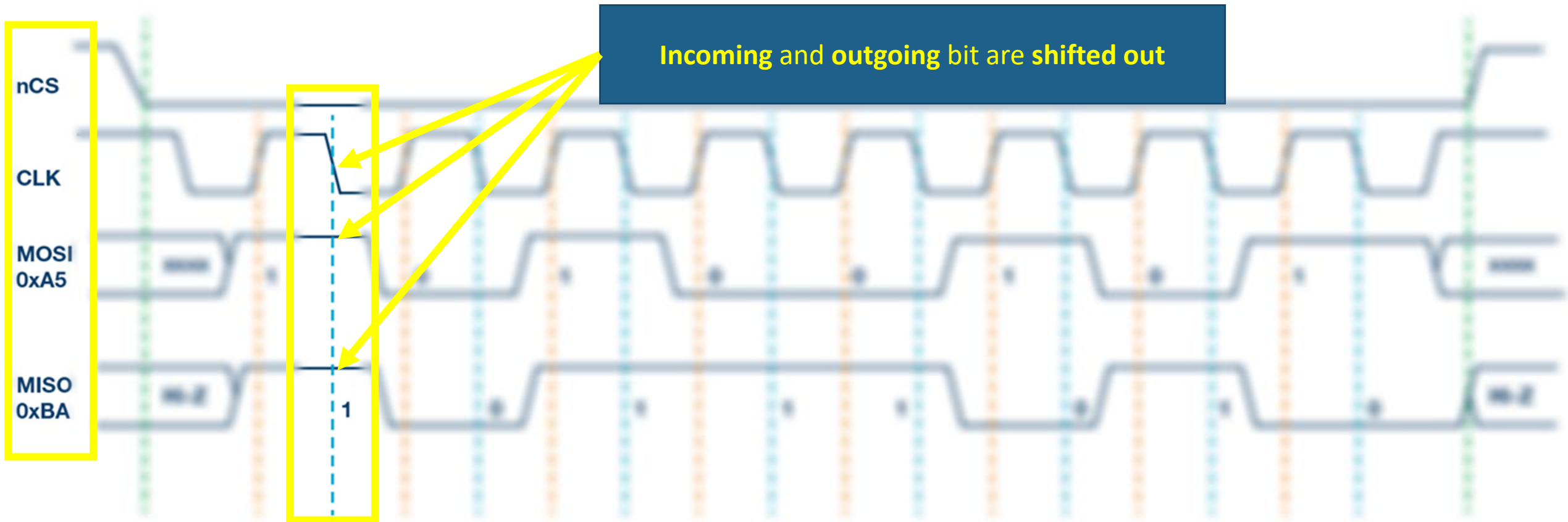
SPI Communication



SPI Communication

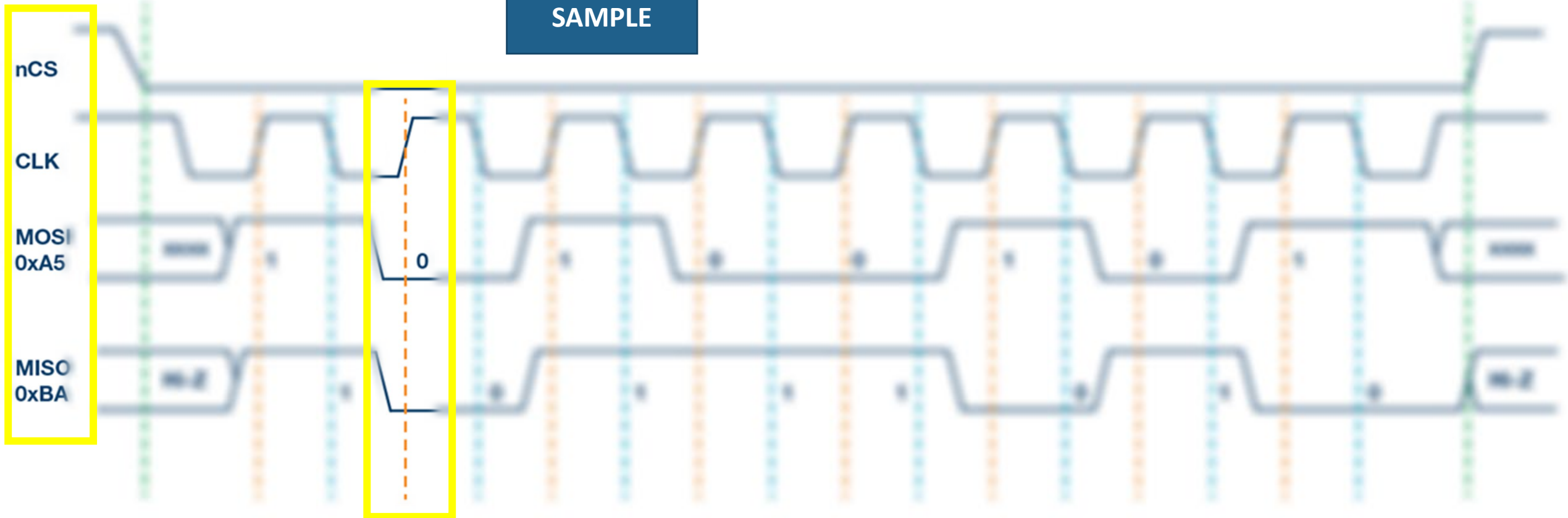


SPI Communication



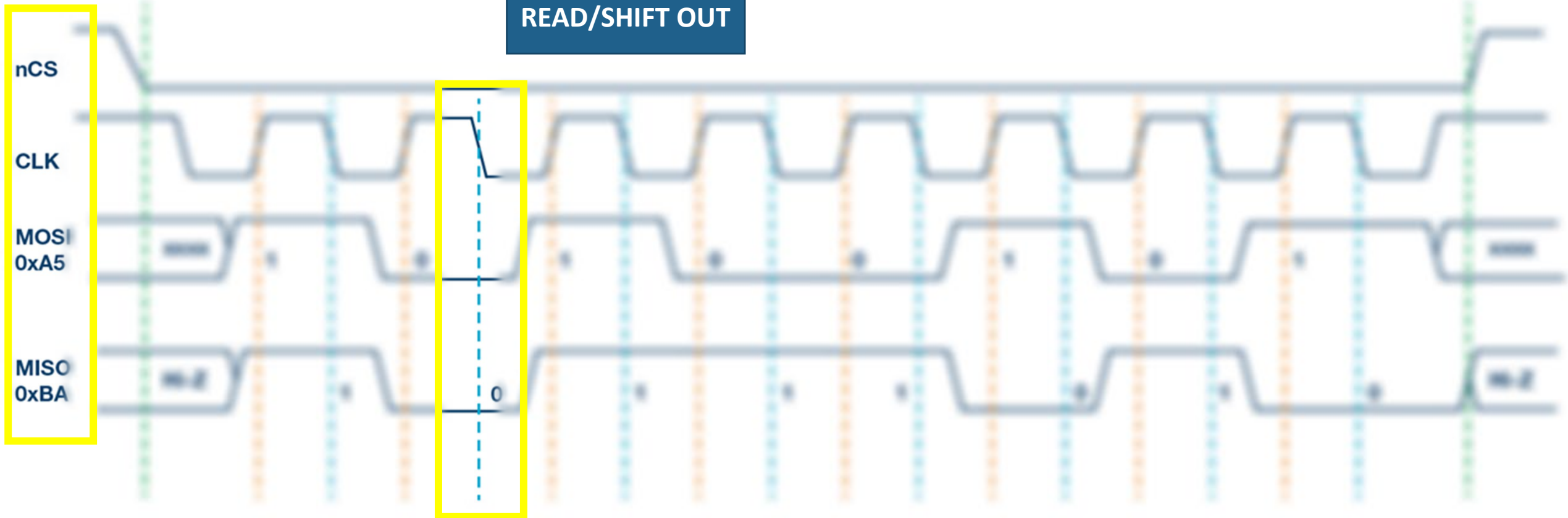
Lab 7
SPI Communication

SAMPLE

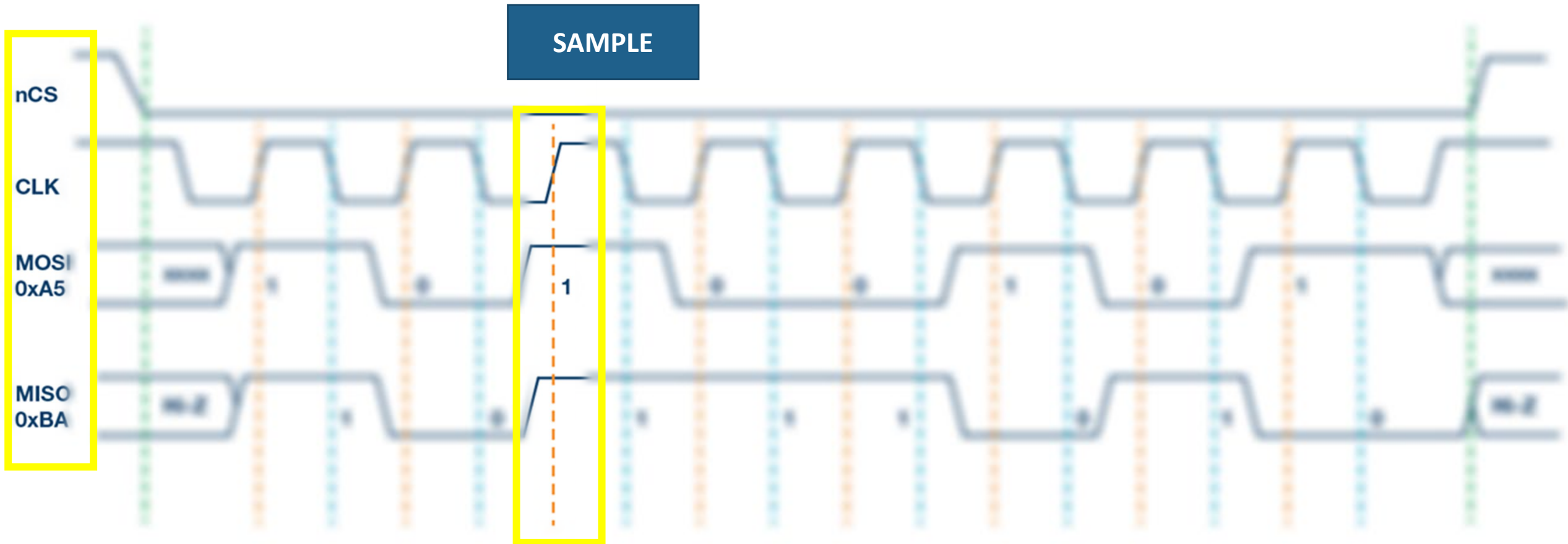


SPI Communication

READ/SHIFT OUT

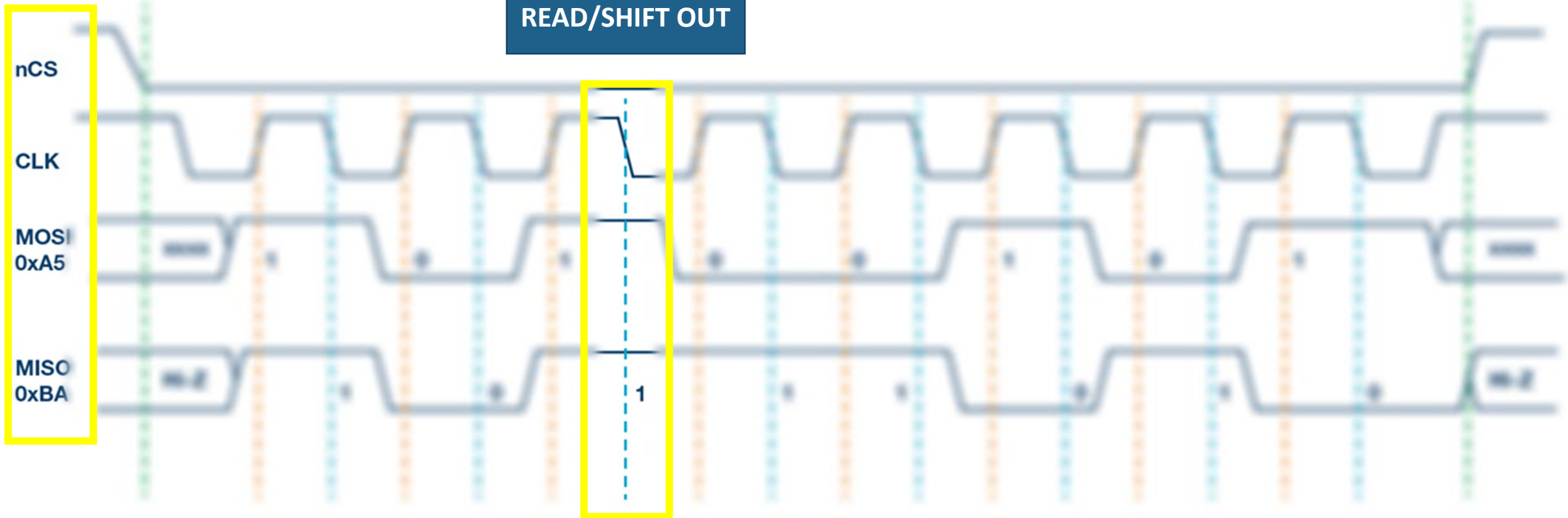


SPI Communication

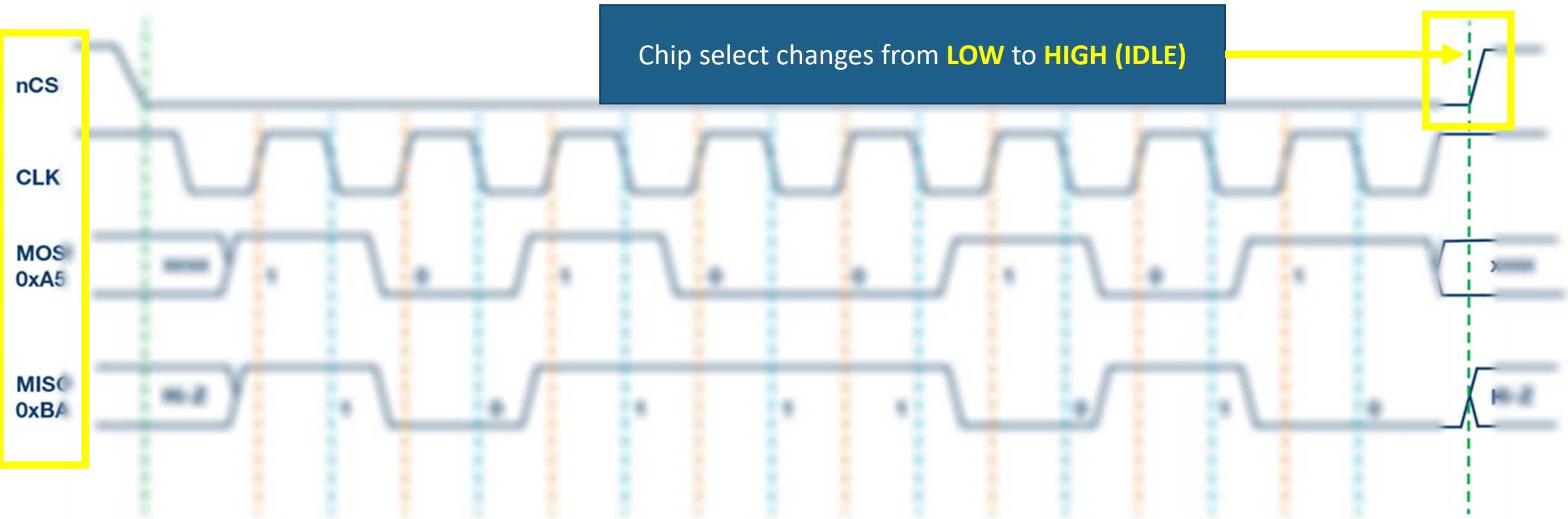


SPI Communication

READ/SHIFT OUT



SPI Communication



SPI Multi-Slave Configuration



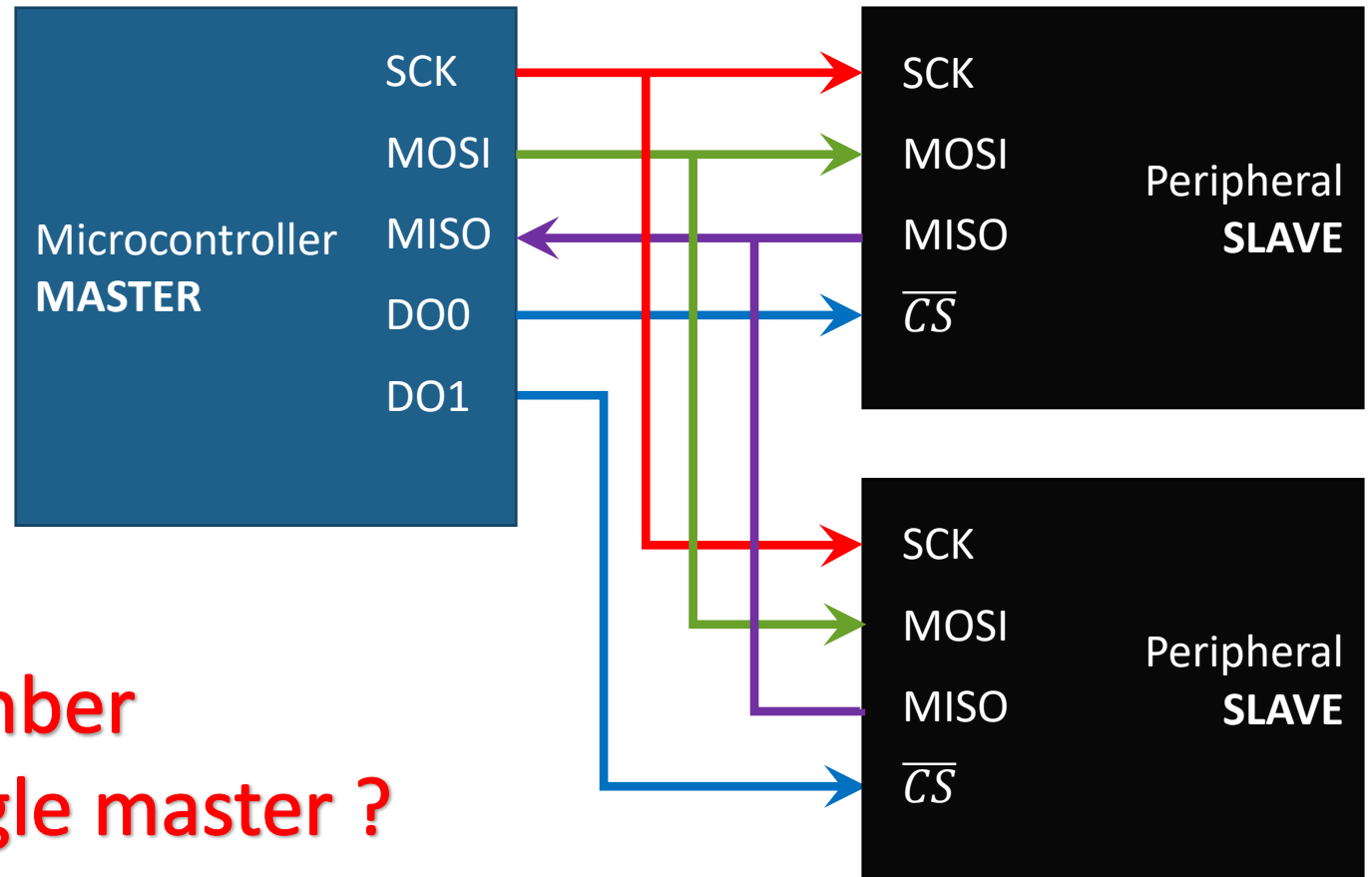
Slave is selected using **Chip Select**



Each data bus is tied together



Dedicated **Data bus** (*MISO and MOSI*)



**What is the max number
of slaves connected to a single master ?**

Lab 7

Exercise 1

SPI Digital Potentiometer



Libraries for exercise



Built-in SPI library

```
#include <SPI.h>
```

```
SPI.begin();
```

```
digitalWrite(CSPin, LOW);
```

```
SPI.transfer(byte);
```

```
digitalWrite(CSPin, HIGH);
```

```
// SPI library
```

```
// Initialize SPI
```

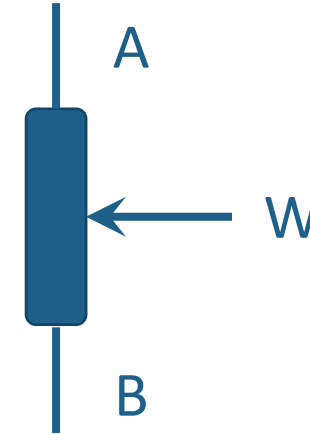
```
// Select peripheral
```

```
// Send Byte
```

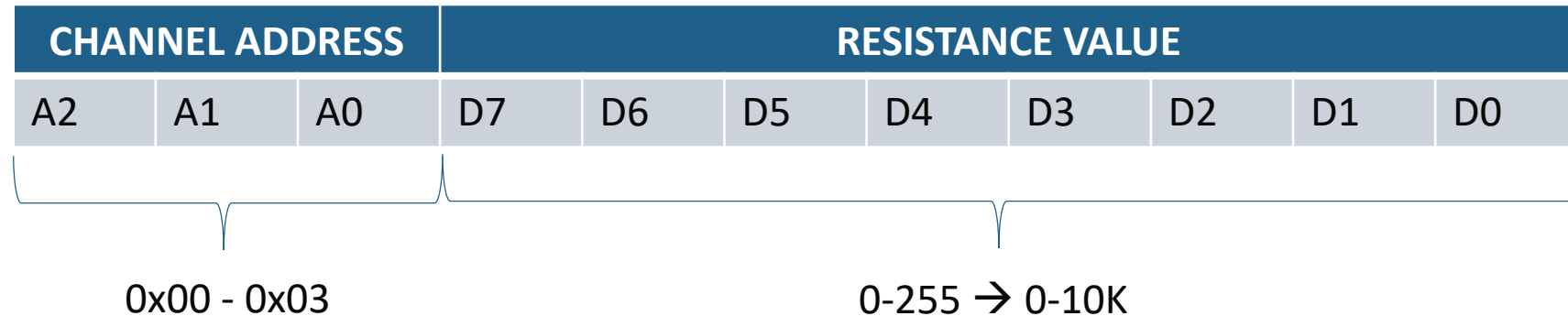
```
// Deselect peripheral
```

AD5204 Digital Potentiometer

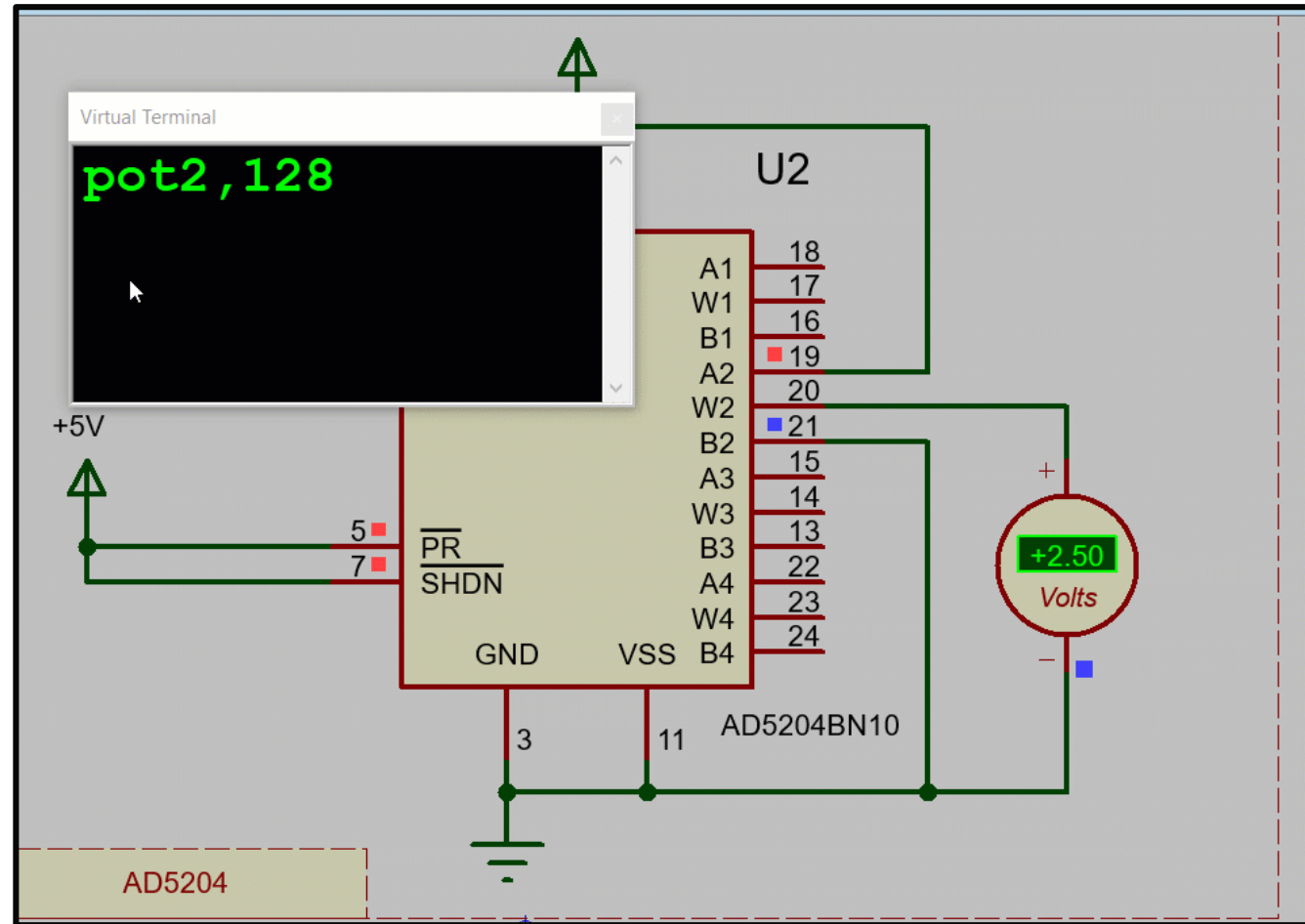
- **256** positions
- **4** channels
- 10k, 50k and 100k variants

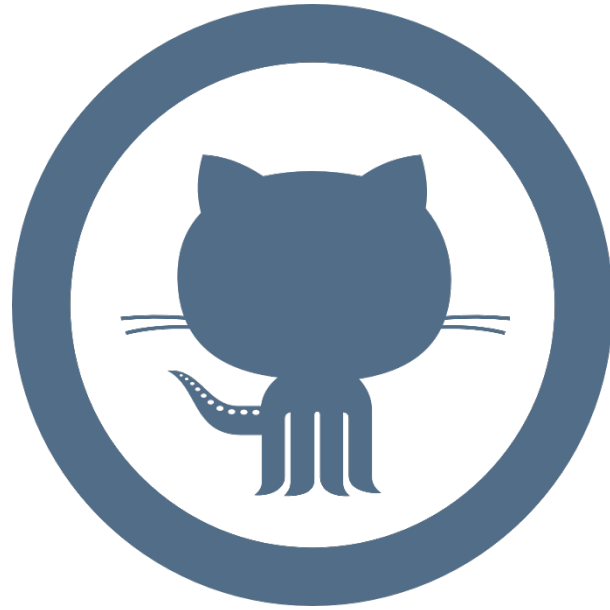


AD5204BNXX



AD5204 Digital Potentiometer





Don't forget to pull the lab update from.

<http://github.com/wbadry/mte405>

END OF Lab 7