



Cairo University



Two Semester System
Computer Department
First Year
Logic Project

Submitted to
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#Input/Output

Inputs:

12 of Input Pins

Outputs:

3 of LEDs

3 of 7-Segment Display

#Number of gates used:

NOT Gate	113
AND Gate	293
OR Gate	185
NOR Gate	8
XOR Gate	88
Build-in Multiplexer	1
TOTAL without previous MUX	687
TOTAL	688