

Verilog Lab 1 Requirement

Carry Select Adder (CSA)

- Student 1:-

Name: عبدالرحمن حمدي احمد مخيمر

Section: 1

BN: 36

- Student 2:

Name: عبدالرحمن نعمان لقمان ابراهيم

Section: 2

BN: 1