-> Page 1 Analog Integrated Circuits. Chapter 6 : Frequency Responce. 1 Poles and Teros. - Any System Transfor function Has = NOS) - Teros : Poots of numerator. Poles, roots of denominator - Freq Response 1 S -> JW H(Jw) = Vont(Jw) = 1H(Jw)1et \* Mag of a+Jb= = = Ja2+b2 \* Phase of (a+Jb) = & = tant b - Example  $\frac{1}{V(s)} = \frac{V_{out}(s)}{V_{in}(s)}$   $= \frac{11sc}{R + 11sc} = \frac{1}{1 + 8Rc} = \frac{1}{1 + 87c}$ -> H(Jw) = 1/Jwc = + JwRc = + 200 RC = 1+ 200 Lx Z= RC = time Constant LAWC = = = t Cut-off/ Corner freq. LA Poles & Sp = - 1/2 = - Wc, no teros. 17+ (M/W) = - 1

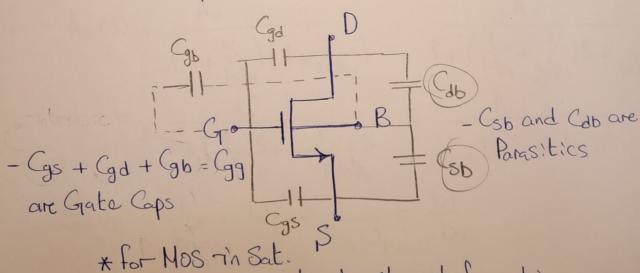
PCH(JW)) = stant Wo

(2) MOSFET Capacitors and where to find them - Coupling and Bypass Capacitors & External Caps.

\* Not Common in CMOS technology.

\* Acts as a HPF

- Internal Capacitors



Par Ltt Calid ( COCC) I I all the Channel formation Cg Cgd; due to Channel Pinched off at La Csb > (db) Nost of Channel goes to S

-> Page 3 (3) SCTC and OCTC techniques. \* SCTC 1 for (LFR) i not Common Tin Analog IC Ly only Consider one Cap Dative > other SC L> WL,3dB = WLI + WL2 + - - (LIN dominants)

# Highest Pole dominants (LIN dominants)

I for High frequery range - more Common. \* OCTC 1 > only Consider one Cap 2 a time -> other 8.C L> WH, 3dB = WHI 11 WHZ 11 ---# Lowest Pole dominants (HIN dominants) \* Both gives good aprox if only one pole dominants and Poles are real.

4) Dominant pole Approx.

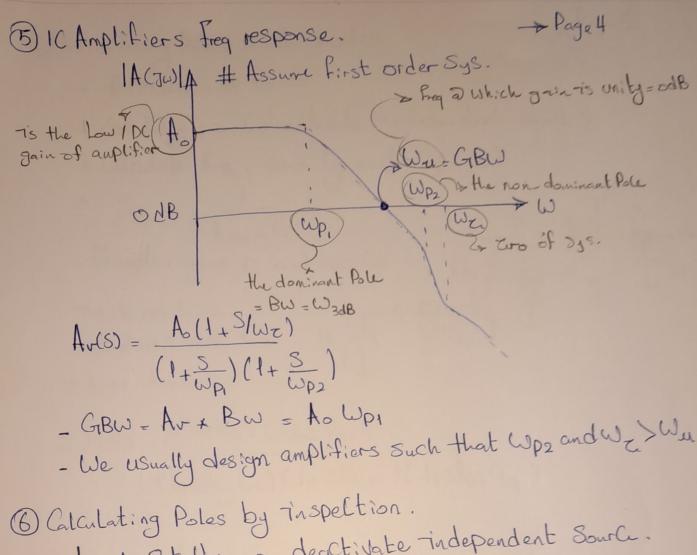
Dominant pole Appron.

- Assume: Poles are real and Wp. K. Wp.

Ao  $Av(S) = \frac{A_0}{(1 + \frac{S}{Wp_1})(1 + \frac{S}{Wp_2})} = \frac{A_0}{1 + (\frac{1}{Wp_1} + \frac{1}{Wp_2})S + \frac{S^2}{Wp_1Wp_2}}$  $\approx \frac{A_0}{1 + (\frac{1}{W_P})S + \frac{1}{W_P W_{P2}}} = \frac{A_0}{1 + b_1 S + b_2 S^2}$ 

05 WP = 1 , WP2 = 1 = b1 b2

- gives good aprox for both dominant and non-dominant.



6) Calculating Poles by Trispeltion.

List Usig = 0, deactivate independent Source.

2: Calculate the Venin resistance. Soen by each Cap

3: Spii = - T

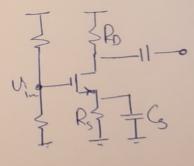
Ryhi Ci

T To

Example

DG: Rth = Rs 11 gm

Sp = - + CRs 11 1 G



(F) Calculating Toros by Tinspection

Ly 1: Find S that makes Vont =0 for each Cap

Example 1: Vant =0 & Circuit above

Vont = 0 & Cis of FR

Vont = 0 & Zs is of = 00

 $\frac{1}{8C} = 00$   $\frac{1}{8C} = 0$   $\frac{1}{8C} = 0$   $\frac{1}{8C} = 0$ 

~ Page 5 (8) Associating Poles With nodes - # nodes = # Poles ! Set Usig = 0 (deactivate and ependent Source) 2 Calculate Rm, i Seen by each Cap (Gi) - Example: Ignore is and Mos Cap « each mode 15 Associated with Pole Using

.. HIN dominates

: H(S) = (3m1Rox) (3m2 Roz) (1+ SRsig (in) (1+ 3Ro(1)) (1+ 3Ro2 92) 9 Miller Effect

Ly Miller Hebrenn

if we have a floating impedance Zi
between Ewo Point X, y

it can be Converted to G and Z2 X of Till ZII

not floating Components

Where.

Zi = Z

$$\frac{Z_1 - Z_2}{1 - Av}$$

$$\frac{Z_2 - Z_2}{1 - Av} = \frac{Vy}{Vx}$$

Proof, if the two Graits equivalent + IZ = IZ

$$\frac{V_{x}-V_{y}}{Z}=\frac{V_{x}}{Z_{1}} \rightarrow Z_{1}=\frac{Z_{1}V_{x}}{V_{x}-V_{y}}=\frac{Z_{1}}{1-\frac{V_{x}}{V_{x}}}$$

- this decomposition of a "floating" impedance To The two "grounded" impedances proves useful in analysis and design

- if the impedance to form the only Path between X and y, then the Convertion is often invalid

- Miller's theorem proves useful in Cases where the Tinpedance To appears to parallel with the main Signal

