Analog integrated Circuits Lec 16, Frequency Compansiation.

Page +

1 Freq Compansiation

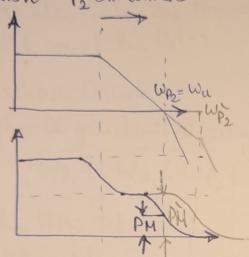
- 75 the modification of the System, to achive a specific PM, to Control the Peaking and ringing.

- We Want

Gx < Px -> Wu < WP2

We Can achive this by

III Push WP2 Ontwards



achised by Lowering resistance or Caps a Pamode

L> not always feas: ble for free

PHI PM

Lower GBW 2. Lower Speed

2 Single Stage OTA

I 5T OTA

- We always want iWp, << Wu < Wp2!

- The HIN sots Open loop Bw = WPont of THI

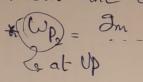
The First mon-dominant Pole
[mirror mode] Sets Ultimate GBW,
ultimate Closed loop Bw [Buffer]

WP2 = CH > CH > CABI + GB2 + HCGOH) + GS4 + GS3 + CABI

Wz = 2Wp2 -> reduce the effect of P2

- Tif Fully diff. ?

* Out and Out Contribute & Pole Ho



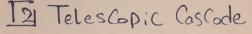
Pole -

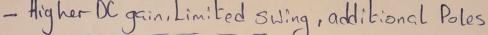
* if there is a mismatch

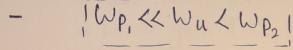
- out and out Contribute Ewo Poles P. P. 2 & which are Close to each other

- T't also Create one Tero at Wz = 2[Wp, 11Wp]

CanCil one of the Poles



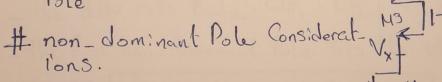




- the HIN Sets BWOL

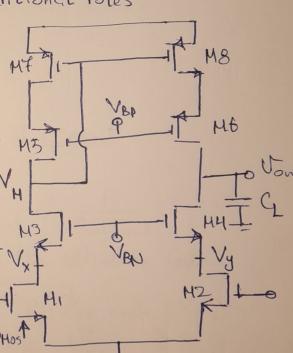
WP, = WPont = Pont Cont

- Vx and Vy Contribute a Single VH
Pole



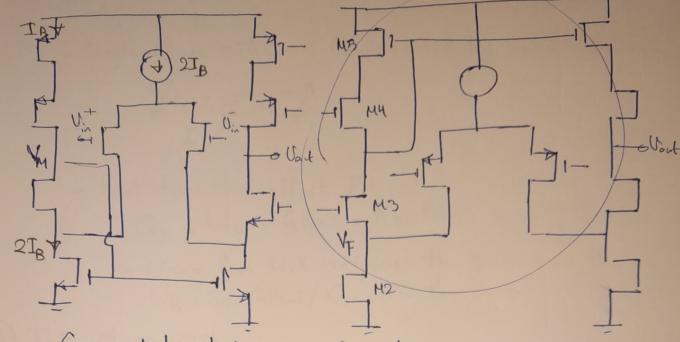
I PMOS Contribute Larger HI

Caps - For Same ID, WAMOS, CAMOST



Cancel each other

- Two Possible Timplementation for SE output



- Compared to telescopic Cascode

- * More Power 2x
- * Lower gain [Poill Too]
- * More node / Poles
- * More Complex

input and output range de Carpled

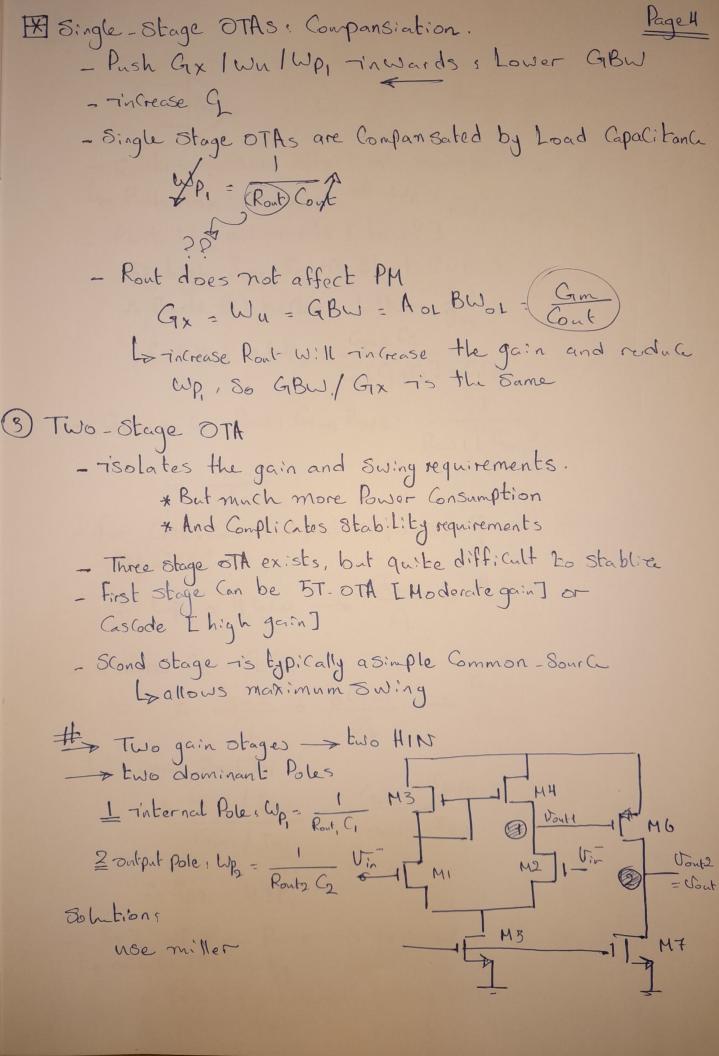
- WPIKKWUK WP2

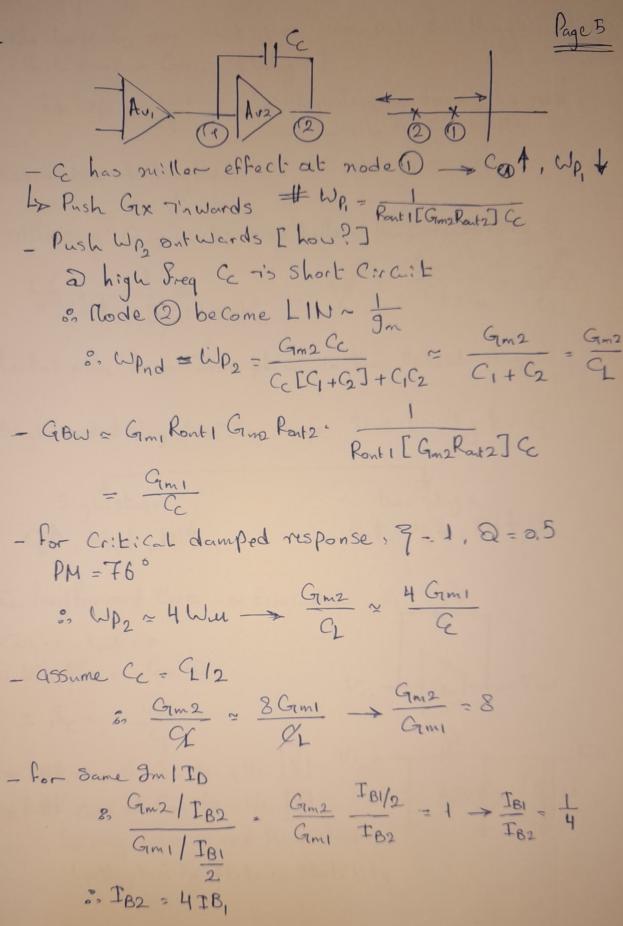
- the HIN sets BWDL, WP, = WBut = Rout Gut

- the first non-dominant Pole Sits the ultimate GIBW

- Consideration

- M2 has large Capacitance (double the Carrent)





- 80% of the Power is Consumed in the Second Stage to achive stability

& 867. of the Current not Contribute to GBW & miller OTA is very energy inefficient

- Too Large Cc does not give more Pole Splitting Page 6 : Just Smaller Gibw [Why?]
Ly WP2 ~ Granz at high freq Cc does rits Job and Converte out 2 to LIN
_ Reasonable starting Point Cc = 9,8:0,5 G
- Tin Creasing Gamz Works even better than increasing Ce The Font I [Gm Rout 2] Ce The Court of Court Court o
La But more Power Consumption in the Second Stage
Single andré stage Vs Two stage Sins: Livity Lo Q
Singlistage Ewo stage QA, WP, I, PHA QA, WP, I, PHA QA, WP, I, PHA
The feedforward tero -> Reminder CF
Vont = 0, Lont = 0 Stin SCT = - Gun Vin Gan Vant Gan
2 PZ = - Games + Ve VI Vino + HT Tolling
Ly Very bad Sor FB Loop Stability
Lavery bad Sor FB Loop Stability

- RAP Tero 73 bad for both magnitude and Phase LyPushes Gre outwards and Pushus Parinwards

Paget

L> Tracreasing Ce may hurt stability Wz = 2nb = Gens Cc

- Handling the RHP Tero

Ly Add a resistance to Control the Value of the Taro

Vontil = Ing Vout

RZ+ I

Sz (c

Sz = [I - Rz]

Sz = [I - Rz]

- Place the Tero.

3 Some designers try to move the ters to the LHP to Canal the first non-dominant Pole and for ruprove Ay

* Practically never achieved due to Variation

* actually does not Lead to faster response (Why?) Critical damped [Second order system]

faster than over damped [Ist order System]

* Pole-tero double to Pushing the LHP cero to Lower Fred Ly Very Poor Settling Line
Ly noise amplification

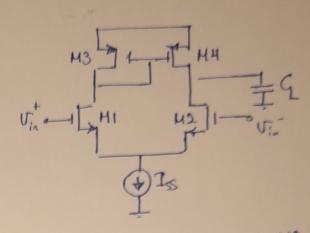
(5) Slew Rate (SR)

- is the maximum rate at which the support of an opamp changes when a Large differential input signal is present

III Single 8 tage Slew Rate

IDDHIA, IDDM24

4 MI (Complibly ON M2 (Complibly off



off SR = Iss /CL and Vice Versa if Vint, Vint?

121 Ewo Stage OTA Slew Rate
TH miller Compansiated

$$\frac{2}{V^*} = \frac{9^n}{T_0} \rightarrow \frac{2T_0}{9^n} = V^* \longrightarrow 8R = V^* Wa$$

