- For the circuit shown in Fig. Derive an expression for the output impedance

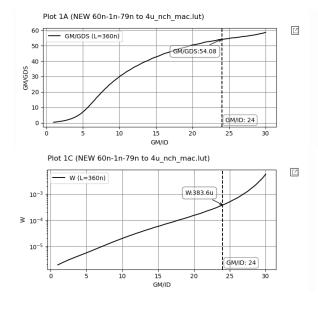
$$R_{out} = R_1 || r_{o2} . [1 + (g_{m2} + g_{mb2}) r_{o1}] \approx R_1 || r_{o2} . g_{m2} r_{o1}$$

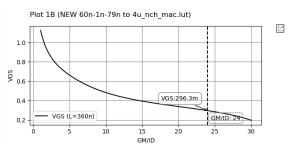
- Design the circuit to get

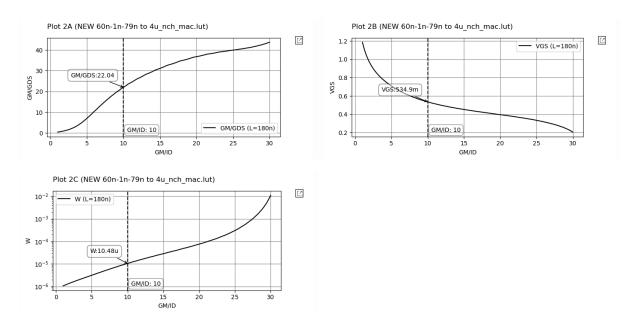
Spec.		T VDD
DC Gain	≥ 23 dB	$R_1 \stackrel{>}{\rightleftharpoons} \qquad \qquad$
BW	≥ 50 MHz	$V_x \rightarrow M_2 \qquad \qquad$
Power Consumption	≤ 0.8 mW	$V_{in} \rightarrow V_{in} \rightarrow V_{in}$
Cap Load	1.25 pF	÷

$$\begin{split} P_{cons} &= V_{DD} \ I_D \leq 0.8 \ mW \rightarrow \because I_{Dmax} \leq 666 \ uA \\ GBW &= A_V \cdot BW = \frac{g_{m1}}{2\pi C_L} \geq 710 \ MHz \rightarrow g_{m1} \geq 5.576 \ mS \rightarrow g_{m1} = 8 \ mS \\ A_V &= g_{m1} R_{out} \geq 14.2 \rightarrow R_{out} = 1775 \rightarrow Choose \ R_1 = 1800 \ \Omega \\ @ \ V_{OUT} &= \frac{V_{DD}}{2} \ for \ maximum \ output \ swing \rightarrow I_D = \frac{0.6}{1800} = 333.33 \ uA \rightarrow \left(\frac{g_m}{I_D}\right)_1 = 24 \\ Assume \ M_2 \ operate \ in \ SI \ for \ large \ r_{o2} \ and \ Assume \ r_{o1} = r_{o2} = r_o \\ &\therefore \ set \left(\frac{g_m}{I_D}\right)_2 = 10 \rightarrow g_{m2} = 3.33 \ mS \rightarrow R_{out} = R_1 \ || \ r_o^2 \cdot g_{m2} \rightarrow r_o = 6195 \ \Omega \\ &\therefore \ \left(\frac{g_m}{g_{ds}}\right)_1 = 49.56 \rightarrow \left(\frac{g_m}{g_{ds}}\right)_2 = 20.63 \end{split}$$

For M1



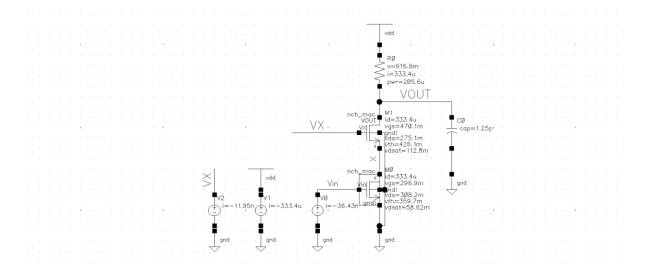




After Simulations, I need to increase the R1 to 1900

Simulations Results

- DC OP



- Vout AC Analysis

