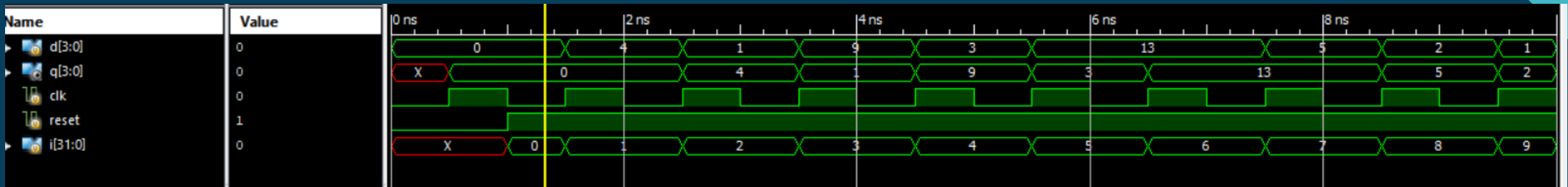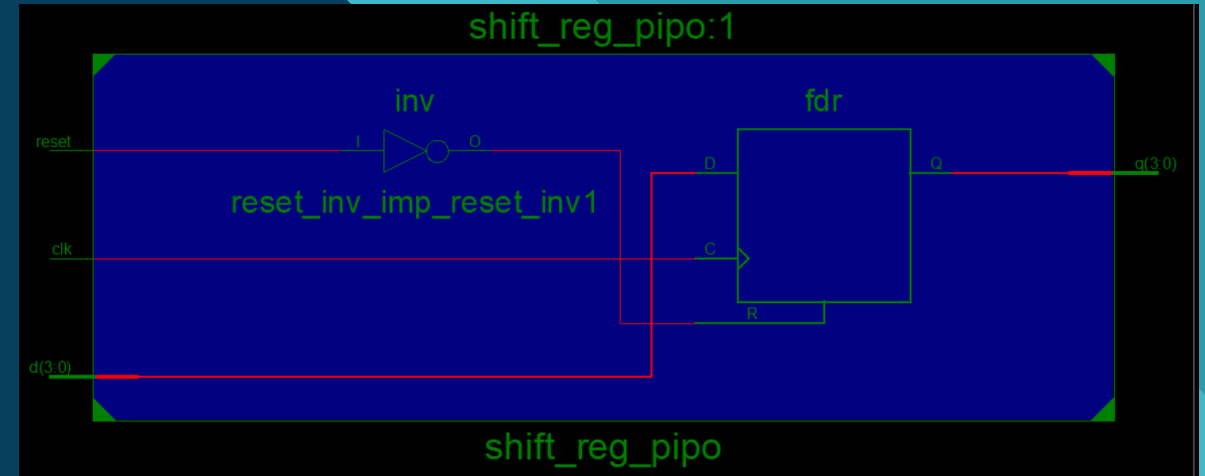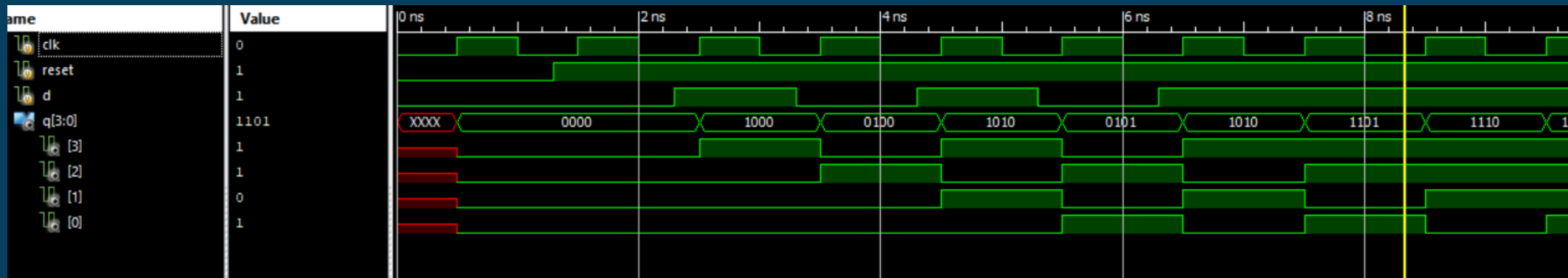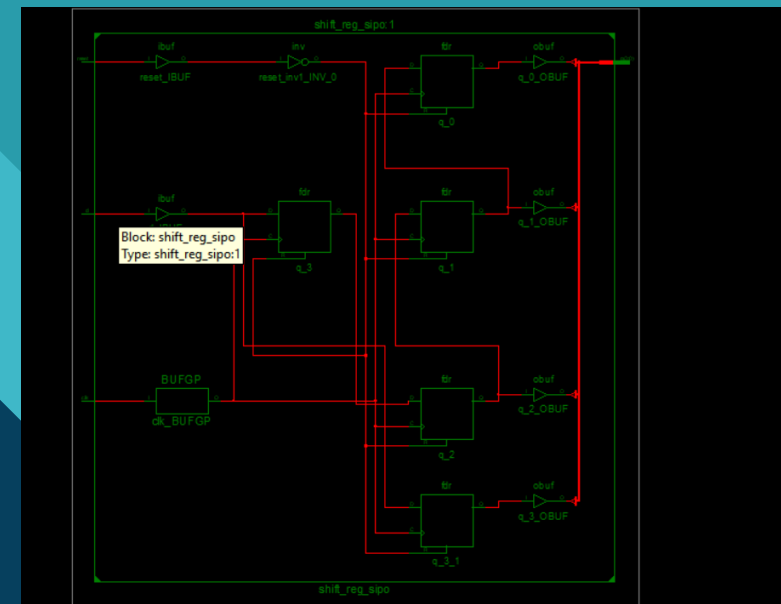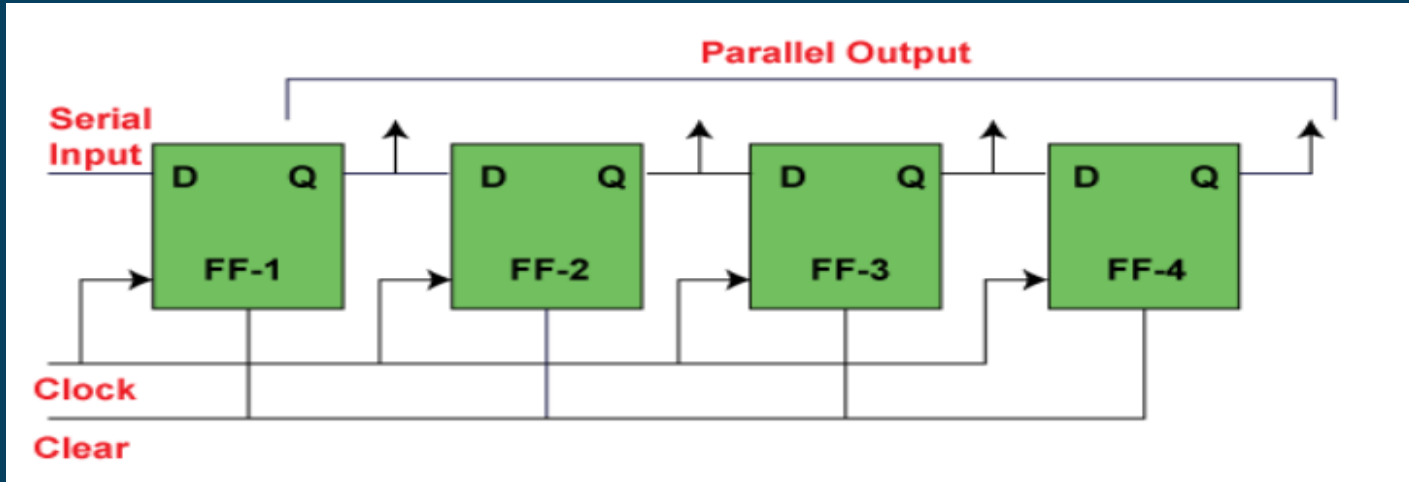# Type of shift register

Abdelraman Khaled elsayed

Parallel in parallel out

- A parallel-in, parallel-out (PIPO) shift register is a digital circuit that allows parallel data to be simultaneously loaded into the shift register and retrieved from it in parallel.
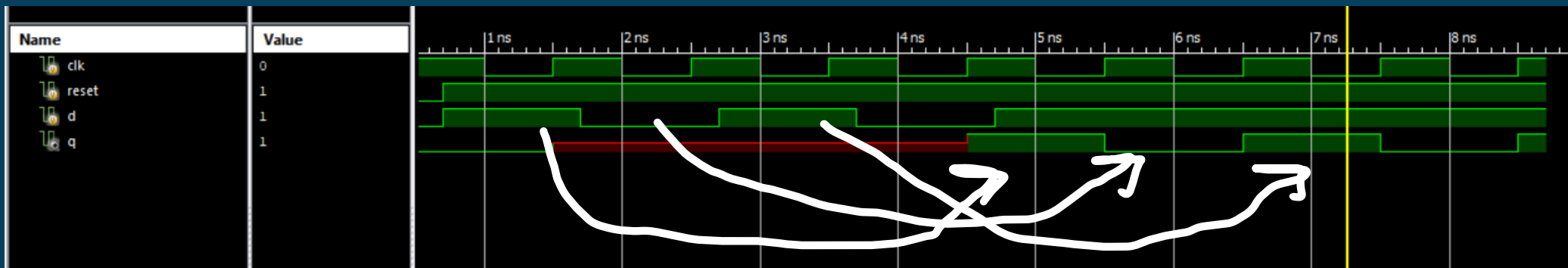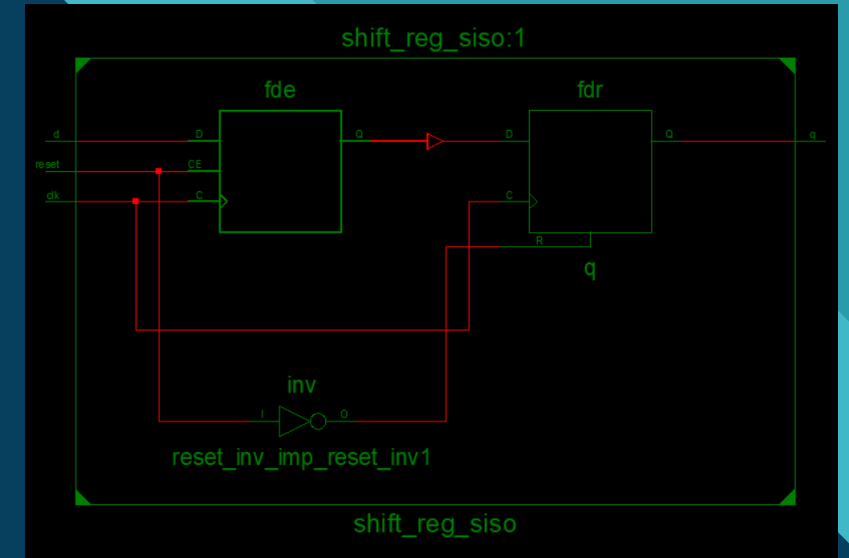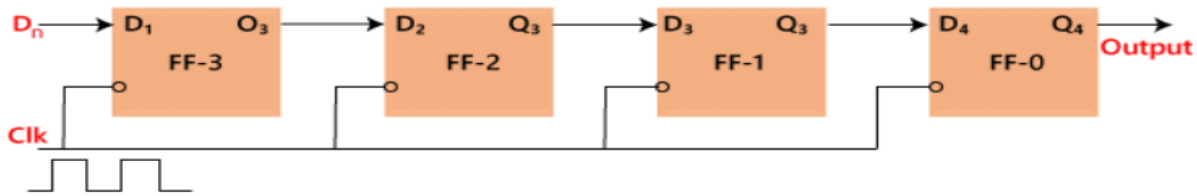
Serial in parallel out

- If four data bits are shifted in by four clock pulses via a single wire at data-in, below, the data becomes available simultaneously on the four Outputs .
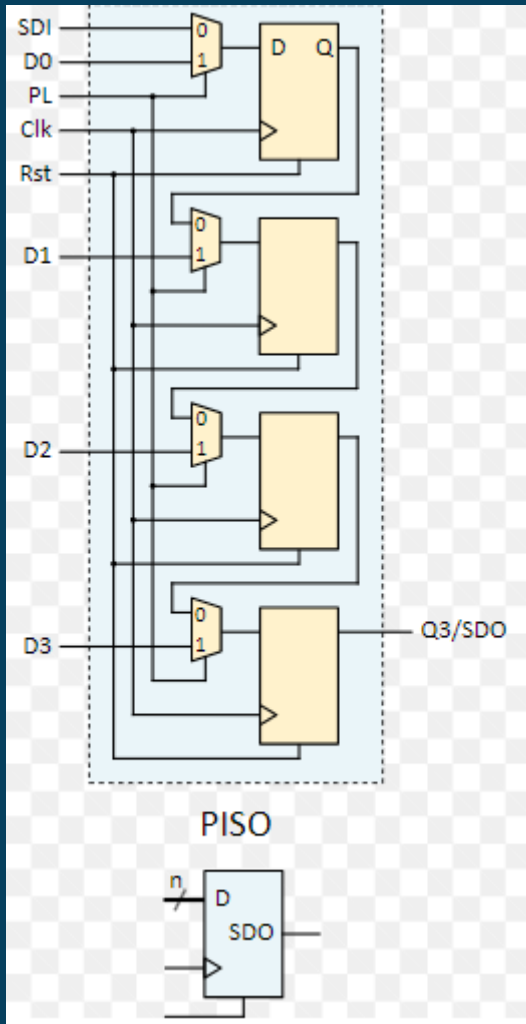
Serial in serial out

- In "Serial Input Serial Output", the data is shifted "IN" or "OUT" serially. In SISO, a single bit is shifted at a time in either right or left direction under clock control.

Block Diagram:

Parallel in serial out

- In this type of shift register, the input data enters a parallel way and comes out serially. So the i/p of the second FF is the o/p of the first flip flop.

Thank You 1