

Electronic Devices

Lecture 7 **P-N Junction**

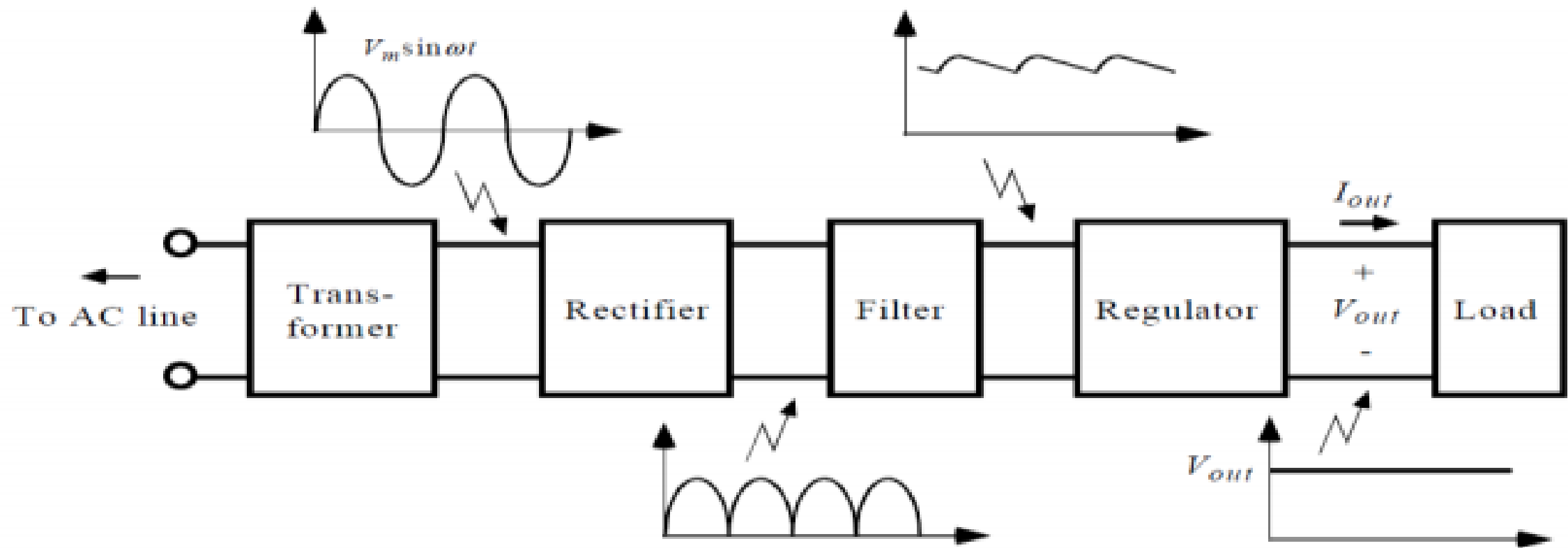
Dr. Roaa Mubarak

Diode Applications

- **Rectifying**
- Clipping
- Clamping

Rectifiers

- Rectifier circuit rectifies the AC signal into DC signal, the most known example is the power supply.



Components of a typical linear power supply

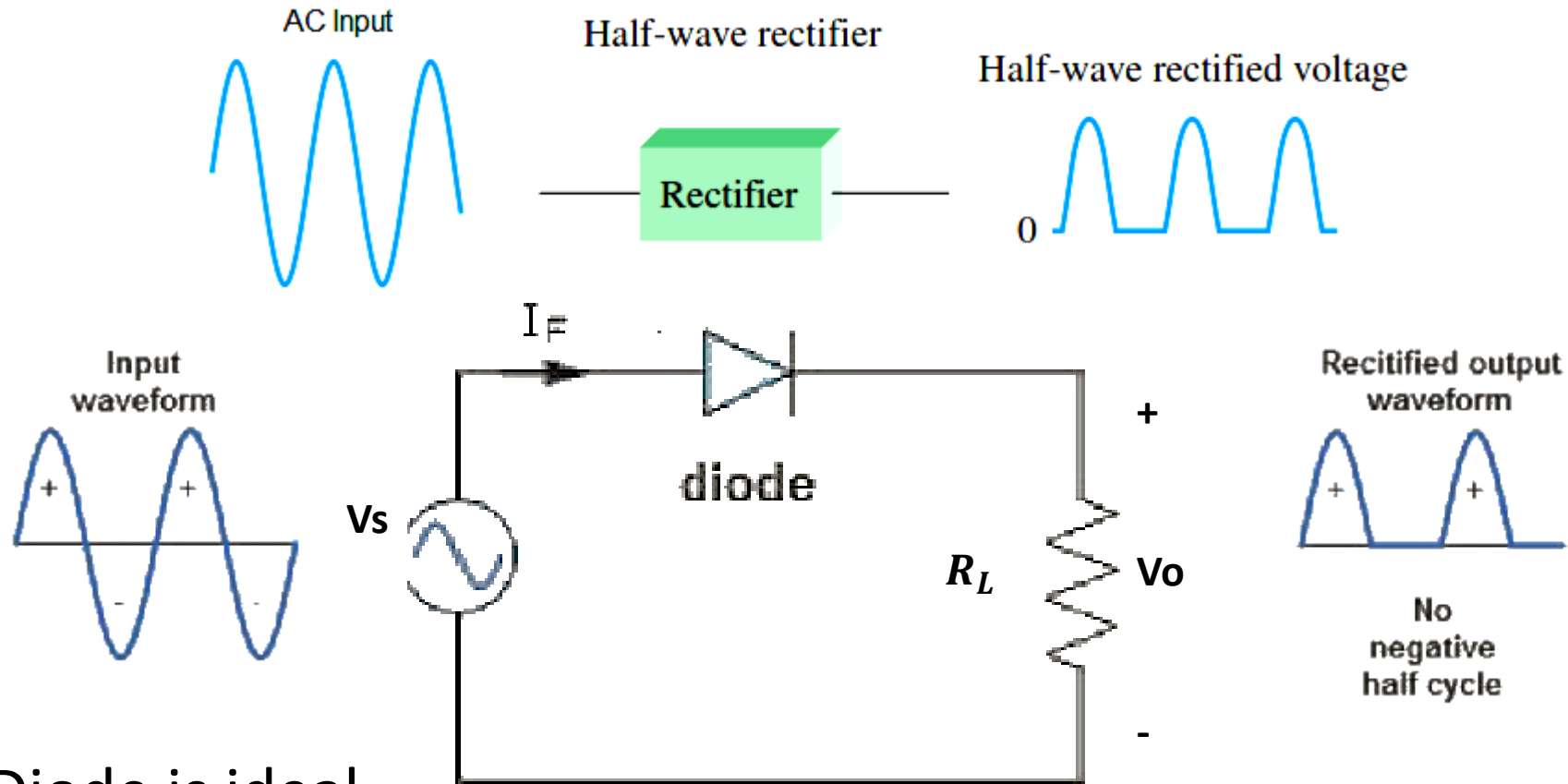
Rectifier

- A basic rectifier converts an AC voltage to a pulsating DC voltage.
- A filter then eliminates AC components of the wave form to produce a nearly constant DC voltage output.
- Rectifier circuits are used in virtually all electronic devices to convert the 220V 50Hz AC power line source to the DC voltages required for operation of electronic devices.
- In rectifier circuits, the diode state changes with time and a given piecewise line model is valid only a certain time interval.

Rectifier Circuits

- Half wave rectifier
- Full wave rectifier
- Bridge rectifier.

Halfwave rectifier



Assuming Diode is ideal

$V_s > 0 \rightarrow D \text{ is ON (short circuit)} \rightarrow V_o = V_s$

$V_s < 0 \rightarrow D \text{ is Off (open circuit)} \rightarrow V_o = 0$

Halfwave rectifier

Assume diode is real one “ Practical”, the piecewise linear model is used.

$V_s < V_\gamma \rightarrow D$ is OFF (open circuit)

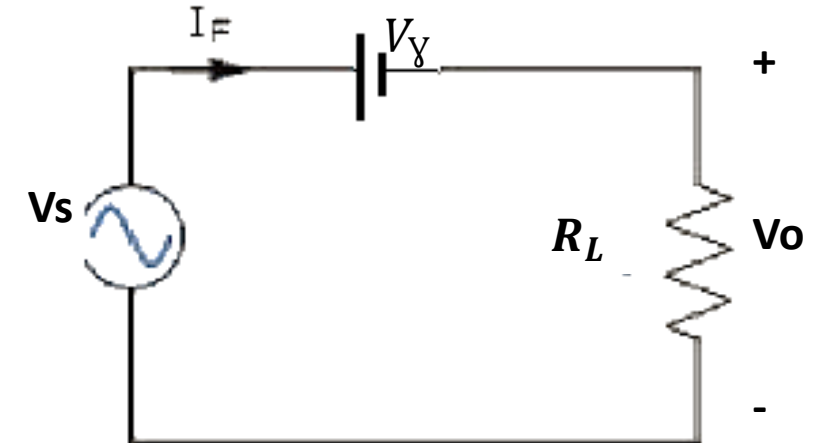
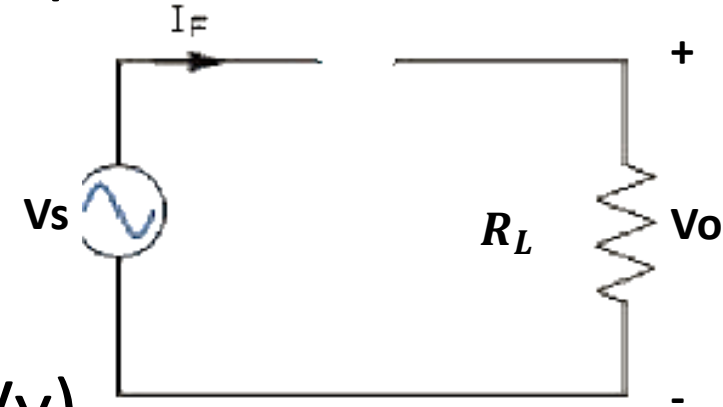
$V_o = 0$

$V_s > V_\gamma \rightarrow D$ is ON (replace diode with V_γ)

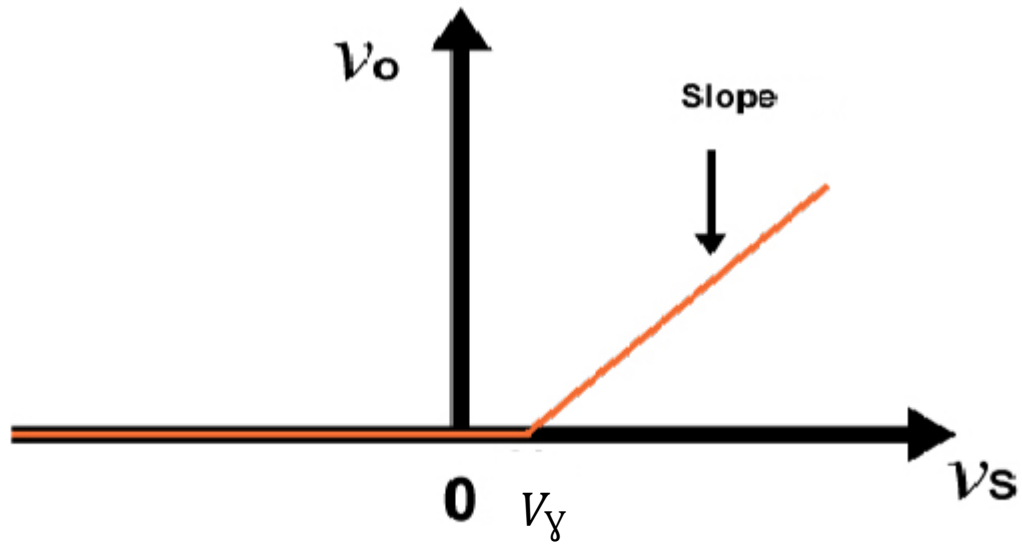
By KVL

$$-V_s + V_\gamma + V_o = 0$$

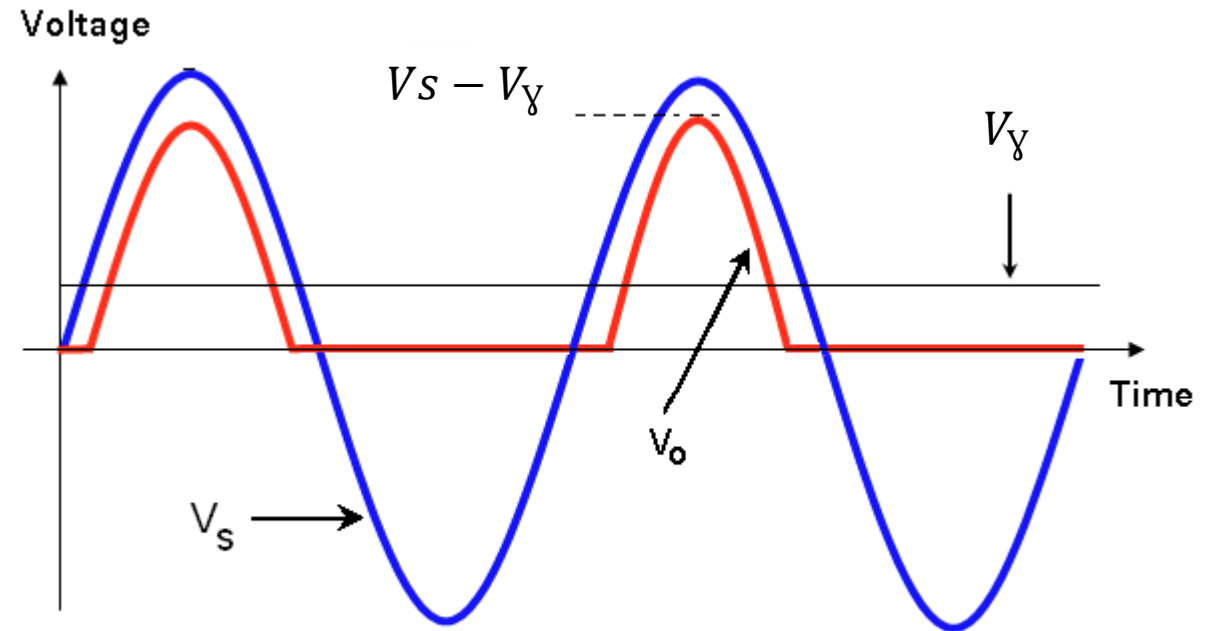
$$V_o = V_s - V_\gamma$$



Halfwave rectifier

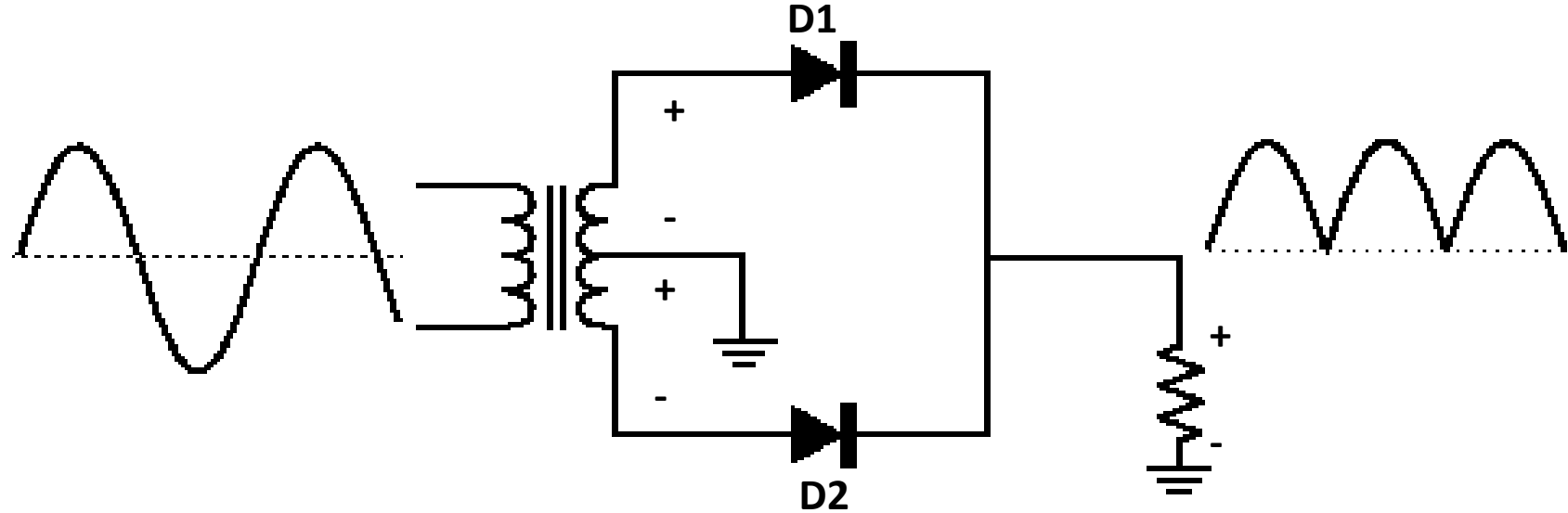


Transfer characteristics



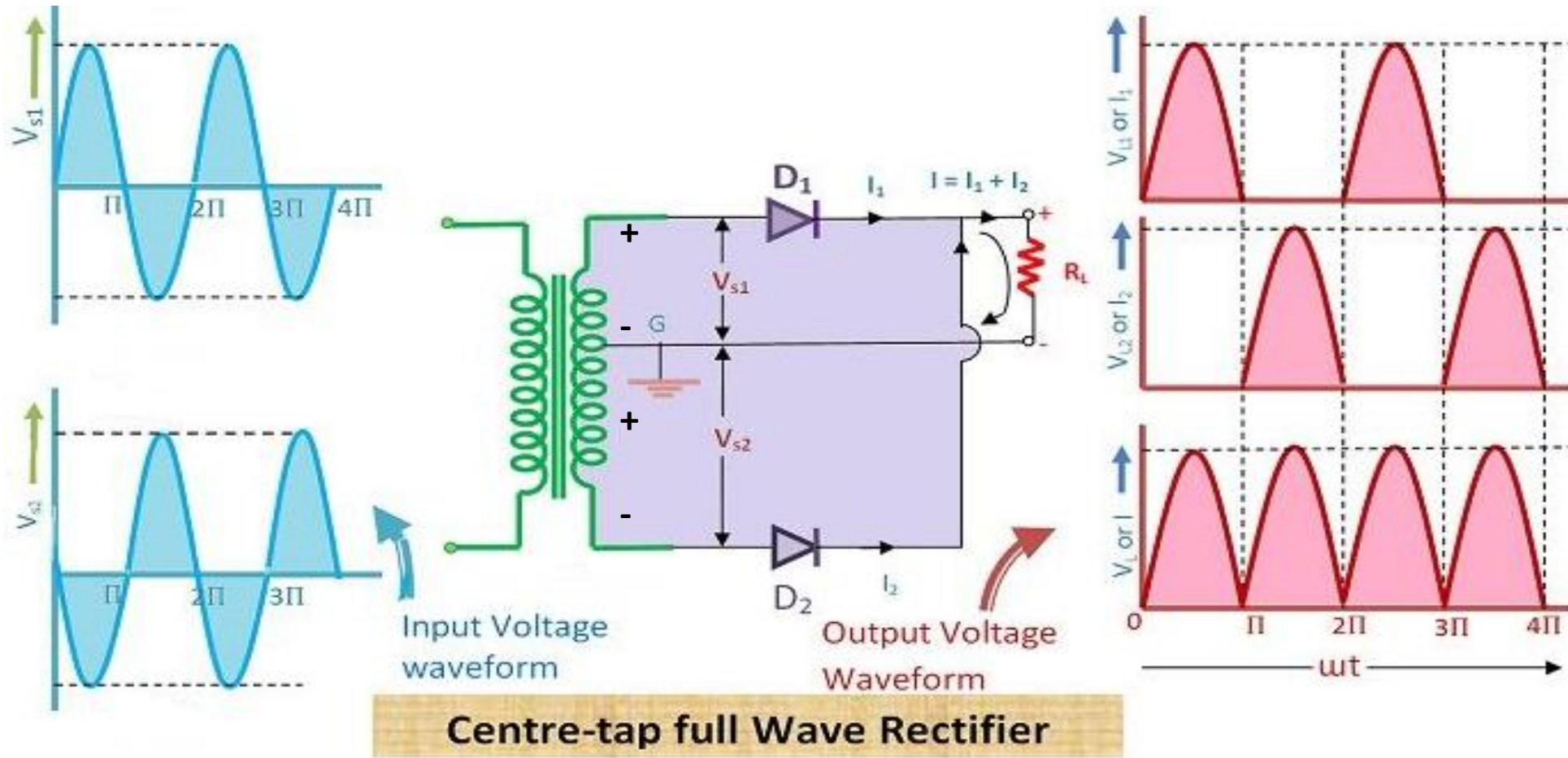
Input & output waveform

Full wave rectifier “Center tap”



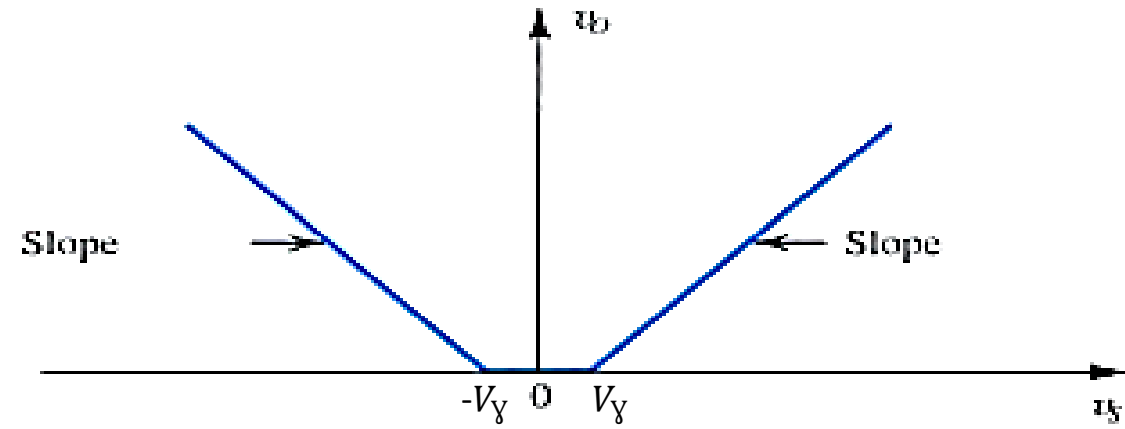
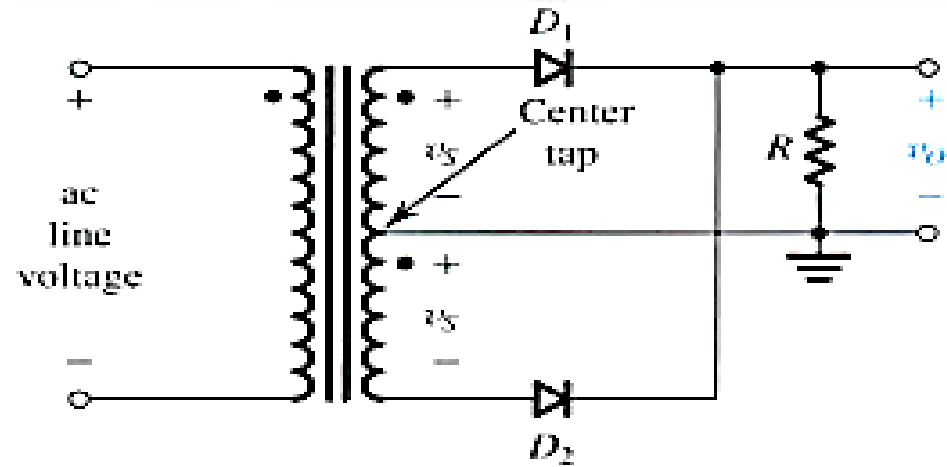
- Two half wave circuits make a Full wave rectifier circuit.
- D1 and D2 are not working together.

Full wave rectifier “Center tap”

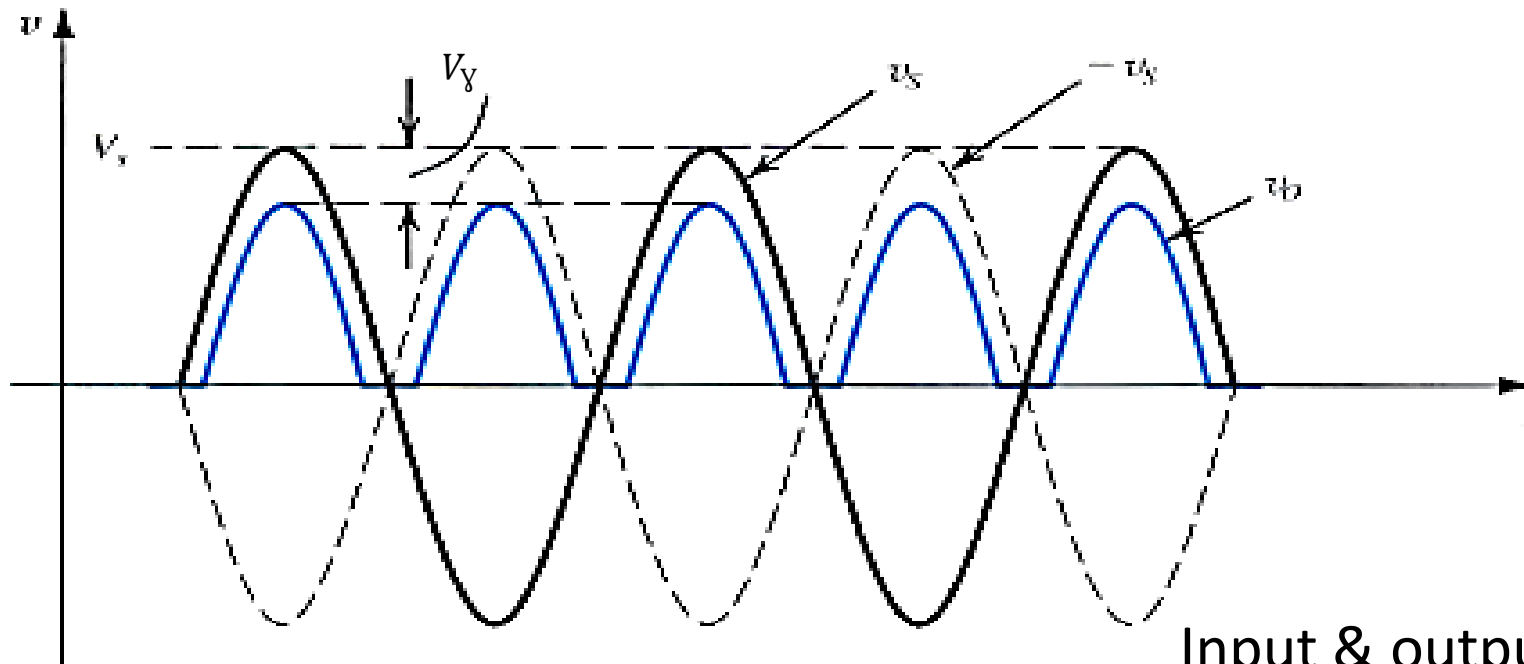


- if $V_s > V_\gamma \rightarrow D_1$ is ON & $V_o = V_s - V_\gamma$
- If $V_s < -V_\gamma \rightarrow D_2$ is ON & $V_o = -(V_s - V_\gamma)$
- If $-V_\gamma < V_s < V_\gamma \rightarrow D_1$ & D_2 are OFF & $V_o = 0$

Full wave rectifier “Center tap”



Transfer characteristics



Input & output waveform

Bridge Rectifier

- For positive half cycle ($V_s > 2V_\gamma$)

D1 & D2 are ON D3 & D4 are OFF

$$V_o = V_s - 2V_\gamma$$

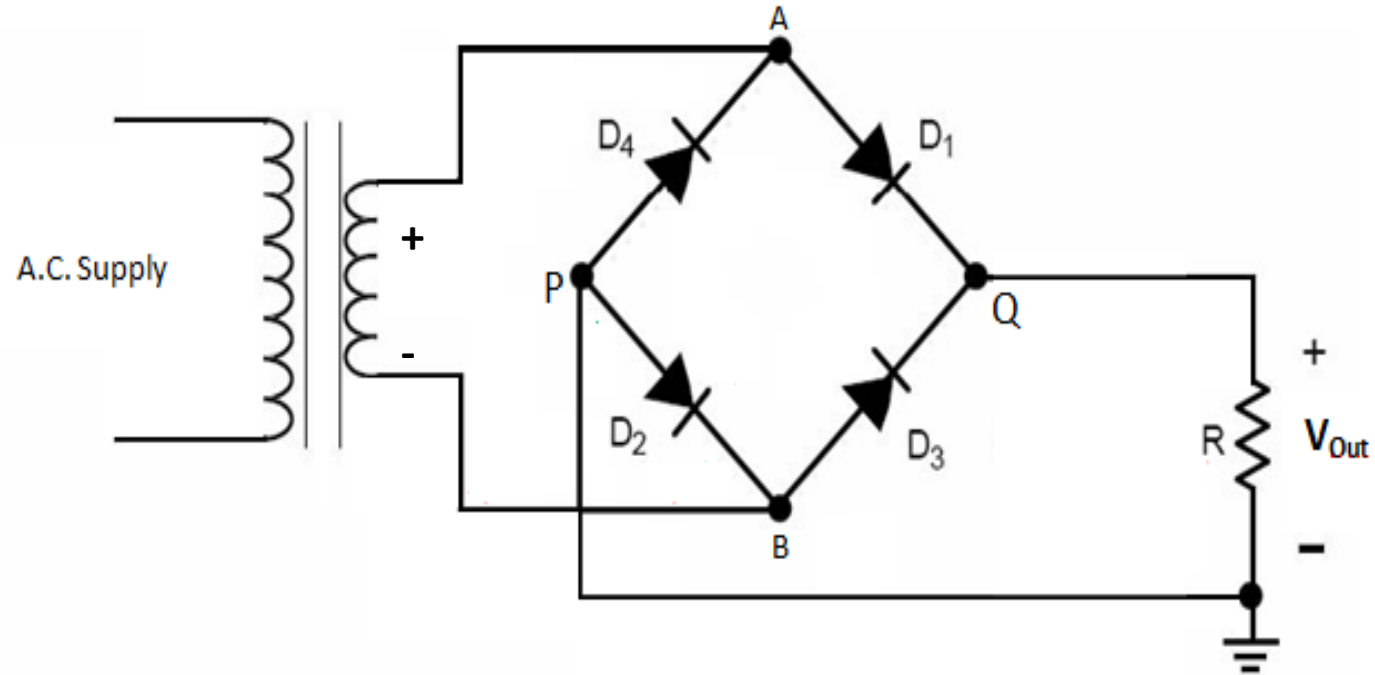
- For negative half cycle ($V_s < -2V_\gamma$)

D1 & D2 are OFF D3 & D4 are ON

$$V_o = -(V_s - 2V_\gamma)$$

- $-2V_\gamma < V_s < 2V_\gamma$ all D are off

$$V_o = 0$$



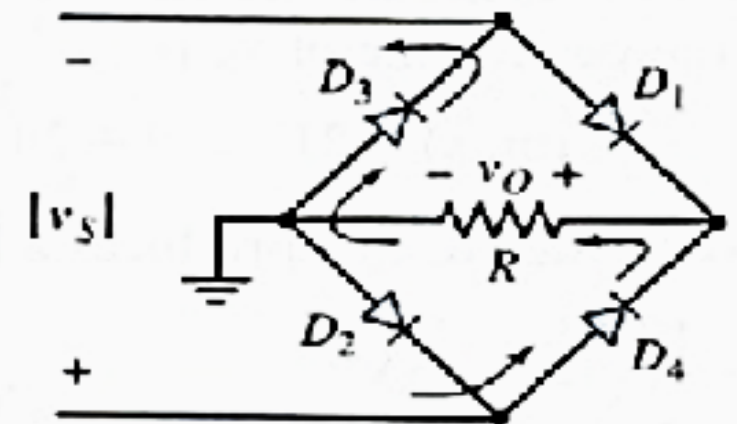
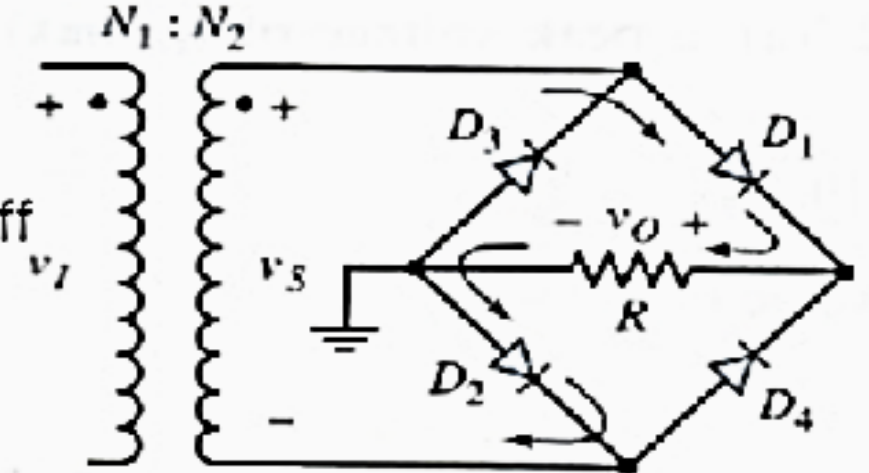
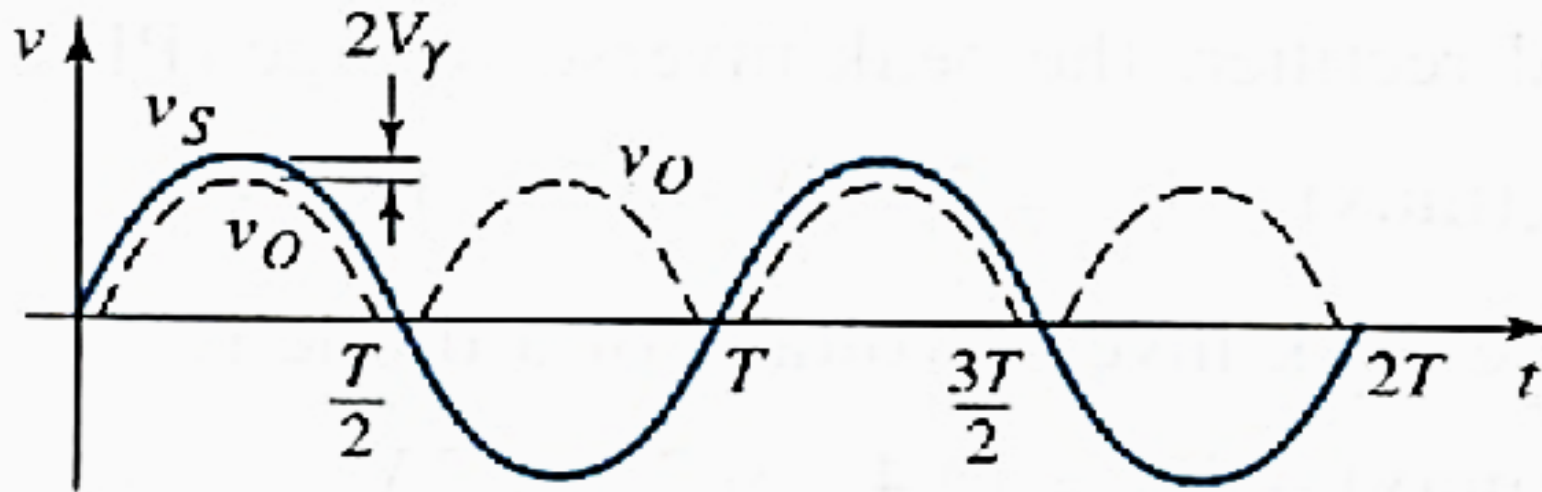
Bridge Rectifier

➤ $V_s > 2V_y$, D_1 and D_2 conducts, D_3 and D_4 off;

$$V_o = V_s - 2V_y$$

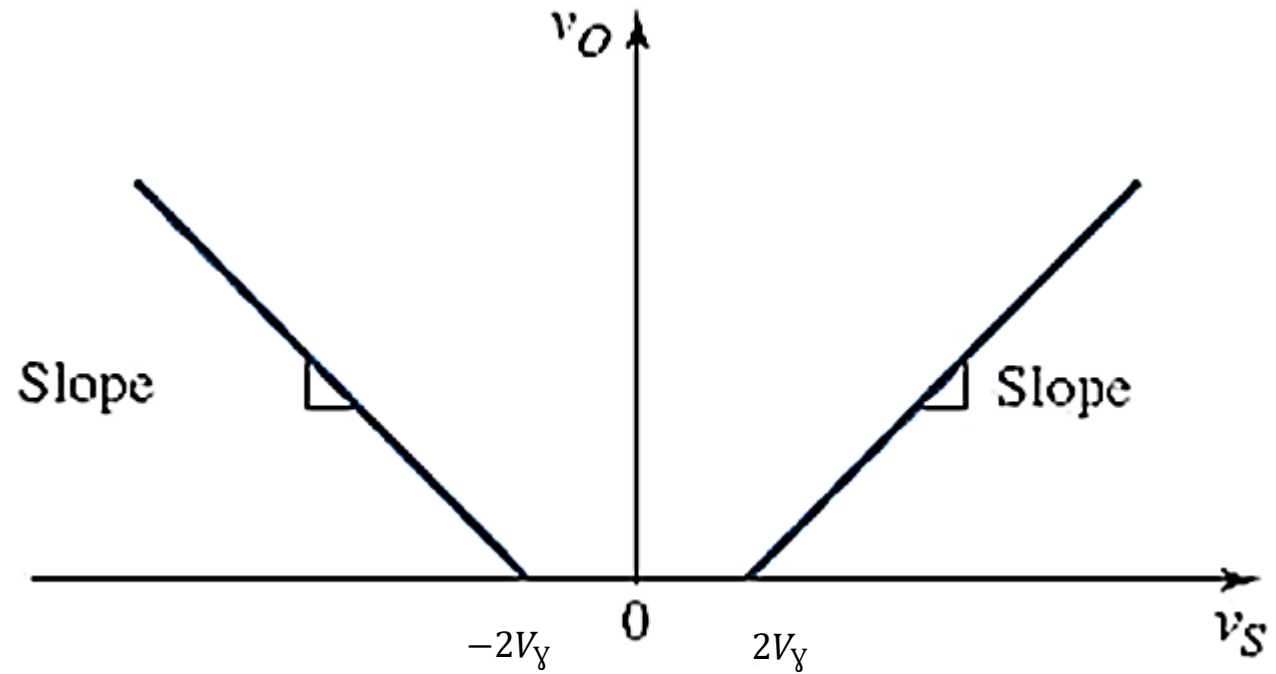
➤ $V_s < -2V_y$, D_3 and D_4 conducts, D_1 and D_2 off

$$V_o = -(V_s - 2V_y)$$



Input & output waveform

Bridge Rectifier



Transfer characteristics

Rectifier with filter

