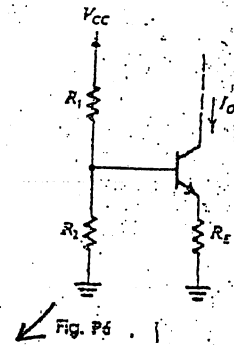


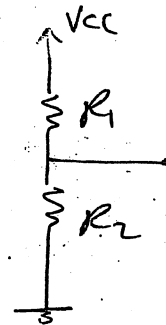
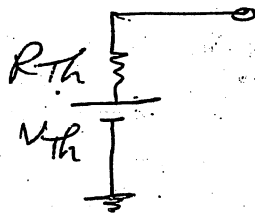
## Problems

- 1-The circuit in Fig. P1 provides a constant current  $I_o$  as long as the circuit to which the collector is which is maintained the BJT in active mode. Show that

$$I_o = \alpha \frac{V_{CC}[R_2/(R_1 + R_2)] - V_{BE}}{R_E + (R_1/R_2)(\beta + 1)}$$



using Thevenin's Theorem

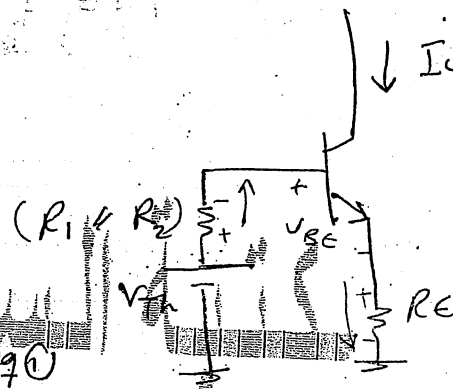


$$R_{Th} = R_1 \parallel R_2$$

$$V_{Th} = \frac{R_2 V_{CC}}{R_1 + R_2}$$

$$V_{Th} = (R_1 \parallel R_2) I_B +$$

$$V_{BE} + R_E I_E \rightarrow \text{eqn}$$



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(2)

∴ BJT in Active Mode

$$I_E = (\beta + 1) I_B$$

$$I_C \cong I_O = \beta I_B$$

$$\frac{\beta}{\beta + 1} = \alpha$$

$$\therefore I_C = I_O = \alpha I_E$$

$$\therefore I_E = \frac{I_C}{\alpha} = \frac{I_O}{\alpha}$$

from eq (1)

$$\frac{R_2}{R_1 + R_2} V_{CC} = (R_1 \parallel R_2) \frac{I_E}{1 + \beta} + V_{BE} + R_E \frac{I_O}{\alpha}$$

$$= (R_1 \parallel R_2) \frac{I_O}{(1 + \beta)\alpha} + V_{BE} + R_E \frac{I_O}{\alpha}$$

$$\left( \frac{R_2}{R_1 + R_2} \right) V_{CC} - V_{BE} = \frac{I_O}{\alpha} \left[ \frac{(R_1 \parallel R_2)}{1 + \beta} + R_E \right]$$

$$\therefore I_O = \frac{\alpha \left( \frac{R_2}{R_1 + R_2} V_{CC} - V_{BE} \right)}{\left( \frac{R_1 \parallel R_2}{1 + \beta} \right) + R_E}$$

(2)

(3)

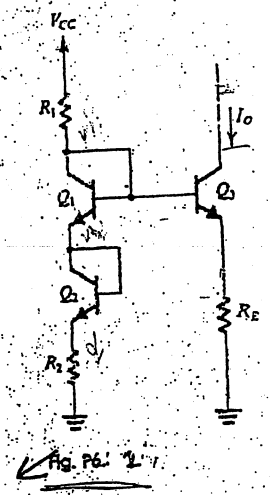
2-For the circuit in Fig. P2, assuming all transistors to be identical with  $\beta$  infinite, Drive an expression for the output current  $I_o$ , and show that by selecting

$$R_1 = R_2$$

And keeping the current in each junction the same, the current  $I_o$  will be

$$I_o = \alpha V_{CC} / 2R_E$$

Which is independent of  $V_{BE}$ . What the relationship of  $R_E$  to  $R_1$  and  $R_2$  be? For  $V_{CC} = 15V$ , and assuming  $\alpha \gg 1$  and  $V_{BE} = 0.7V$ , design the circuit to obtain an output current of 1 mA. What is lowest voltage that can be applied to the collector of  $Q_3$ ?



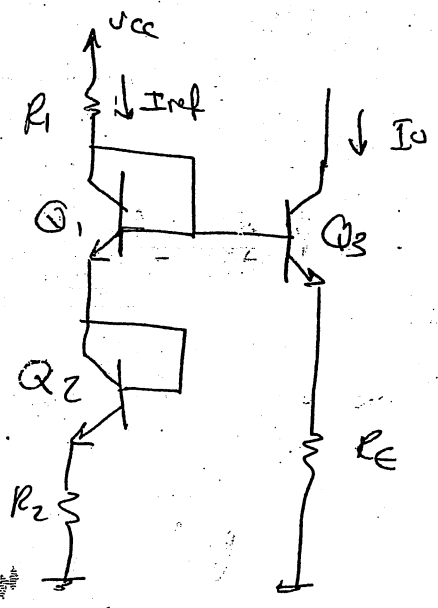
Assume all transistors identical

From KVL

$$V_{BE1} + V_{BE2} + R_2 I_{E2} = V_{BE3} + R_E I_{E3}$$

Neglecting the two Base Current ( $Q_1, Q_2$ )

$$I_{ref} \approx I_{C1} \approx I_{C2} \approx I_{E2}$$



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$$0.7 + 0.7 + R_2 I_{ref} + R_E \frac{I_o}{\alpha}$$

(4)

$$0.7 + R_2 I_{ref} = R_E \frac{I_O}{\alpha}$$

$$\therefore I_O = \frac{\alpha (0.7 + R_2 I_{ref})}{R_E}$$

$\therefore$  we must find  $I_{ref}$ .

$$I_{ref} = \frac{V_{CC} - 1.4}{R_1 + R_2}$$

for  $R_1 = R_2$

$$\therefore \boxed{I_{ref} = \frac{V_{CC} - 1.4}{2R}} = \frac{V_{CC}}{2R} - \frac{0.7}{R}$$

$$\therefore I_O = \frac{\alpha \left( 0.7 + \frac{R_2 V_{CC}}{2R} - \frac{0.7 R_2}{R} \right)}{R_E}$$

$$\therefore \boxed{I_O = \frac{\alpha V_{CC}}{2 R_E}}$$

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To find The relation of  $R_E$  to  $R_1$  and  $R_2$

$\therefore$  Not Concern  $R_1 = R_2 = R$

from KVL

$$V_{BE1} + V_{BE2} + R_2 I_{ref} = V_{BE3} + R_E I_{E3}$$

$$0.7 + 0.7 + R_2 I_{ref} = 0.7 + R_E \frac{I_0}{\alpha}$$

$$\therefore R_E = \frac{\alpha}{I_0} [R_2 I_{ref} + 0.7]$$

$$\text{but } I_{ref} = \frac{V_{CC} - 1.4}{R_1 + R_2}$$

$$\therefore R_E = \frac{\alpha}{I_0} \left[ \frac{R_2 (V_{CC} - 1.4)}{R_1 + R_2} + 0.7 \right]$$

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(6)

Design The circuit to obtain output current  
of  $1\text{mA}$ . (which mean find  $\underline{R_1}, \underline{R_2}, \underline{R_E}$ )

$$V_{CC} = 15\text{V} \quad \alpha \gg 1$$

$$\text{If } \alpha = 1, \quad I_0 = 1\text{mA}, \quad V_{CC} = 15\text{V}$$

$$\therefore R_E = \frac{\alpha}{I_0} \left( 0.7 + \frac{(V_{CC} - 1.4) R_2}{R_1 + R_2} \right)$$

$$R_E = \frac{1}{10^{-3}} \left( 0.7 + \frac{(15 - 1.4) R_2}{R_1 + R_2} \right)$$

For selecting  $R_1 = R_2$

$$\therefore R_E = \frac{1}{10^{-3}} \left( 0.7 + \frac{(15 - 1.4)}{2} \right)$$

$$\boxed{R_E = 7.5 \text{ k}\Omega}$$

$$\boxed{R_1 = R_2 = 1 \text{ k}\Omega} \quad \text{but any value for } R_1, R_2 \text{ in suitable range}$$

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The Lowest voltage on Collector of  $Q_3$

$Q_3$  must be in Active

So,  $V_{CE}$  at least  $= 0.2 \text{ V}$

$$V_{CE_{sat}} = 0.2 \text{ V}$$

$$\therefore V_{C_{min}} = V_{CE_{sat}} + \frac{I_0 R_E}{\alpha}$$

$$= 0.2 + 1 \text{ mA} \times 7.5 \text{ k}$$

$$= 0.2 + 7.5 = 7.7 \text{ volt}$$

$$\therefore \boxed{V_{C_{min}} = 7.7 \text{ volt}}$$

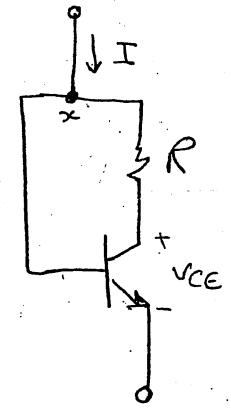
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ex:

Q For The Circuit Shown in figure  
Show that by selecting  $R = \frac{1}{g_m}$   
 $V_{CE}$  is kept Constant for small change  
in Current.



Sol:

at point x :

$$V_{BE} = R I_C + V_{CE}$$

$$\therefore V_{CE} = 0.7 - I_C R$$

if  $I_C$  change by  $\Delta I_C$

$$\therefore V_{CE} = 0.7 - (I_C + \Delta I_C) R$$

for select  $\left( R = \frac{1}{g_m} \right)$

$$\therefore V_{CE} = 0.7 - (I_C + \Delta I_C) \frac{1}{g_m}$$

$$g_m = \frac{I_C}{V_T} = \frac{I_C}{0.025}$$



(10)

$$\therefore V_{CE} = 0.7 - (I_C + \Delta I_C) \frac{V_T}{I_C}$$

For small change in  $I_C$   $\Delta I_C \ll 0$

$$\therefore V_{CE} = 0.7 - \frac{I_C \cdot V_T}{I_C}$$

$$\therefore V_{CE} \approx 0.7 - V_T$$

where  $V_T = \frac{kT}{q} = 25 \text{ mV}$

$$\therefore V_{CE} = 0.7 - 0.025 = 0.675 \text{ V}$$

$$V_{CE} \approx 0.675 \text{ Volt}$$

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ex:

⊗ For the circuit in figure

- find the value of  $R$  that will result

in  $I_o = 1\text{mA}$ .

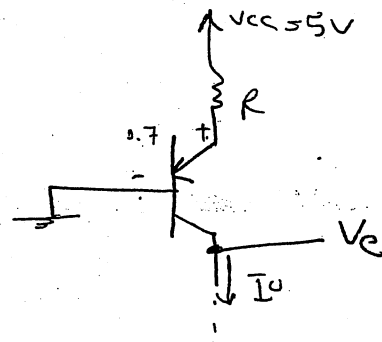
- what is the largest voltage that can

be applied to the collector? Assume  $|V_{BE}| = 0.7\text{V}$

Sol:

⊗  $V_{BE} = -0.7\text{V}$

$I_o = 1\text{mA}$



$$R = \frac{V_{CC} - 0.7}{1\text{mA}} = \frac{V_{CC} - V_{EB}}{I_o}$$

$$R = 4.8\text{ k}\Omega$$

⊗ For the transistor to be on edge of Saturation

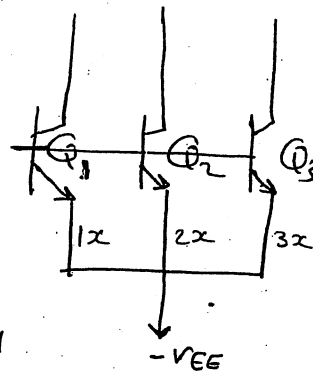
$$V_{C_{max}} = 5 - I_o R - V_{CE}$$

$$= 5 - (1\text{mA} \times 4.8\text{ k}\Omega) - 0.3\text{V}$$

$$V_{C_{max}} = 0.4\text{V}$$

ex:

- ⊗ The transistors  $Q_1$ ,  $Q_2$  and  $Q_3$  in circuit of figure (1) have emitter-base junction areas in the ratio of 1:2:3 respectively.



- a - If  $Q_1$  is diode-connected and fed with a 1mA current source, what ~~the~~ <sup>Current result</sup> are such that active mode operation is maintained.
- b - Repeat with  $Q_2$  diode connected and fed with a 1mA current source.
- c - Repeat with  $Q_3$  diode connected and fed with a 1mA current source using a 1mA current source.

Sol:

(a)  $Q_1$  is diode connected

$$I_1 = 1 \text{ mA}$$

$$I_2 = 2I_1 = 2 \text{ mA}$$

$$I_3 = 3I_1 = 3 \text{ mA}$$

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(b)  $Q_2$  is diode Connected

$$I_2 = 1 \text{ mA}$$

$$I_1 = \frac{1}{2} I_2 = 0.5 \text{ mA}$$

$$I_3 = 1.5 I_2 = 1.5 \text{ mA}$$

(c)  $Q_3$  is diode Connected

$$I_3 = 1 \text{ mA}$$

$$I_1 = \frac{1}{3} I_3 = 0.33 \text{ mA}$$

$$I_2 = \frac{2}{3} I_3 = 0.67 \text{ mA}$$

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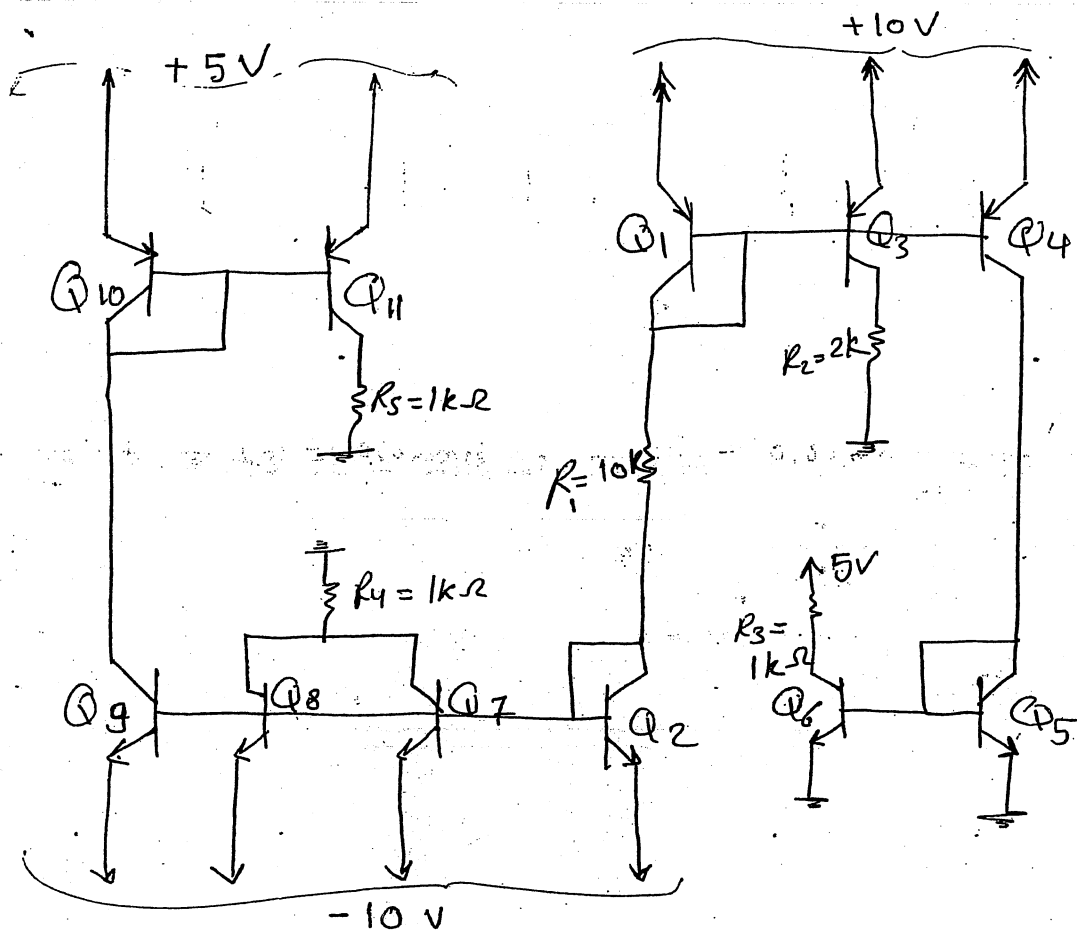
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ex:

- ⊗ Find The Voltages at all nodes and The Currents Through all branches in The Circuit.

Assume that  $V_{BE} = 0.7$  and  $\beta = \infty$



$$I_{C1} = I_{C2} = I_R$$

$$V_{B1} = 10 - 0.7 = 9.3V$$

$$V_{B2} = -10 + 0.7 = -9.3V$$

$$I_R = \frac{9.3 + 9.3}{10} = 1.86 \text{ mA}$$

$$I_R = I_{C1} = I_{C2} = I_{C3} = I_{C4} \\ = I_{C5} = I_{C6}$$

$$V_{C3} = 1.86 \times 2 = 3.72 \text{ V}, \quad V_{C5} = 0.7 \text{ V}$$

$$V_{C6} = 5 - 1.86 \times 1 = 3.14 \text{ V}$$

$$I_{C9} = I_{C8} = I_{C7} = I_{C2} = 1.86 \text{ mA}$$

$$I_{R4} = 2 \times 1.86 \text{ mA} = 3.72 \text{ mA}$$

$$V_{C7} = -3.72 \times 1 = -3.72 \text{ V}$$

$$I_{C10} = I_{C9} = 1.86 \text{ mA}$$

$$V_{C9} = V_{C10} = V_{B10} = 5 - 0.7 = 4.3 \text{ V}$$

$$I_{C11} = I_{C10} = 1.86 \text{ mA}$$

$$V_{C11} = 1.86 \times 1 = 1.86 \text{ V}$$

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