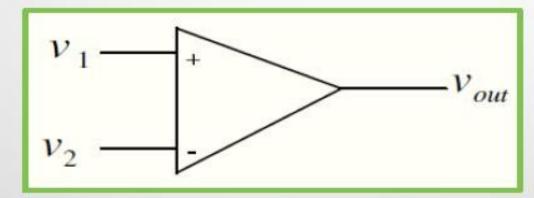
#### **Introduction to Differential**

#### An

- Differential Amplifiers are used extensively in IC
  - They are more immune to Noise and Interference Signals
  - No need for Bypass and coupling capacitors
- Example: the Voltage op-amp is a differential amplifier



$$v_{out} = A_V \left( v_1 - v_2 \right)$$





# to There are two reasons for using differential in preference single-ended amplifiers

First, differential circuits are much less sensitive to noise and interference than single-ended circuits

The second reason for preferring differential amplifiers is that the couple differential configuration enables us to bias the amplifier and to amplifier stages together without the need for bypass and coupling capacitors such as those utilized in the design of discrete-circuit amplifiers

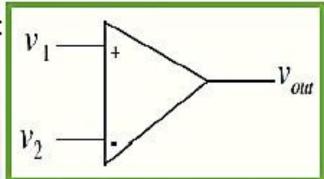




- The Differential Amplifier amplifies the difference between its input terminals and rejects their average (Common Mode)
  - Let's define the following Input Signals:

$$v_{I\!D} = v_1 - v_2$$

$$v_{CM} = \frac{v_1 + v_2}{2}$$



Consequently, the two input signals can be replaced with the following:

$$v_1 = \frac{v_{D}}{2} + v_{CM}$$

$$v_2 = -\frac{v_{ID}}{2} + v_{CM}$$



### Objective

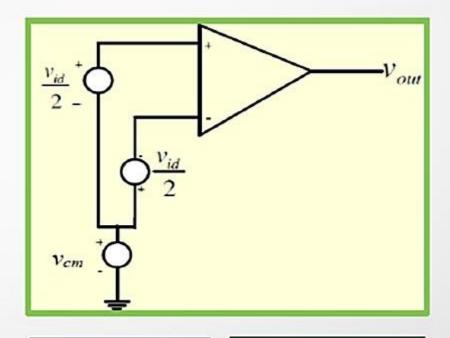
- Calculation of the Differential Mode Gain of the amplifier (ideally tends to infinity) (Signal Gain)
- Calculation of the Common Mode Gain of the Amplifier (ideally tends to ZERO)

(Noise Gain)

Calculation of the Common

Mode Rejection Ratio

(CMRR) (ideally tends to
infinity) (The ability of the amplifier to
reject a common signal at its inputs



$$A_d = \frac{v_{out}}{v_{id}}$$

$$A_{cm} = \frac{v_{out}}{v_{cm}}$$

$$CMRR = \left| \frac{A_d}{A_{cm}} \right|$$

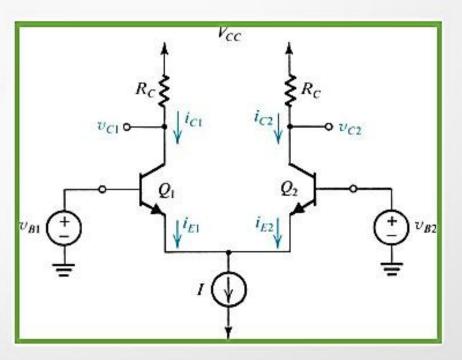




#### **BJT Differential Pair**

#### BJT Differential Amplifier/Pair

- Emitter Coupled Circuit:
  - Two Identical BJTs (Same β and I)
  - Their Emitters are connected together
  - The input signals are connecter to the Q's Bases
  - A Constant DC biasing Current 'I' is used to set the DC operating point.





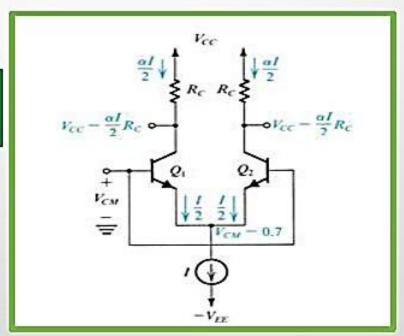


- Emitter Coupled Circuit Large Signal Analysis (Active Mode)
  - If Base 1 and Base 2 are connected together (Common mode signal), then the two emitter currents will be equal

$$v_{BE1} = v_{BE2} = v_{CM}$$

$$i_{E1} = i_{E2} = \frac{I}{2}$$
  $i_{C1} = i_{C2} = \alpha \frac{I}{2}$ 

$$v_{C1} = v_{C2} = V_{CC} - \alpha \frac{I}{2} R_C$$





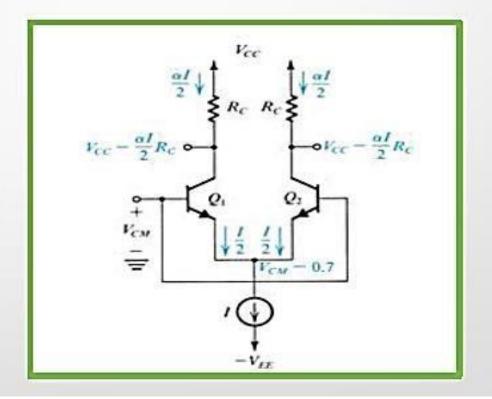


- Emitter Coupled Circuit Large Signal Analysis (Cont.):
  - Changing the value of the common mode signal will not change the transistors currents

$$v_{BE1} = v_{BE2}$$
= $v_{CM}$ 

$$v_{C1} = v_{C2} = V_{CC} - \alpha \frac{I}{2} R_C$$

The DA rejects the common mode Signal







- Emitter Coupled Circuit Large Signal Analysis:
  - If Base 1 and Base 2 are not connected together (Differential signal), then the two emitter currents won't be equal

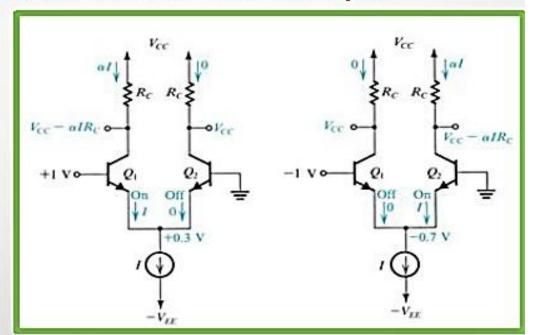
$$v_{BE1} - v_{BE2} = v_{ID}$$
  $i_{E1} =$ 

If  $v_{ID}$ >>0 such that Q1 is Sat and Q2 is Off

$$i_{E1} = I \& i_{E2} = 0$$

If  $v_{ID}$ <<0 such that Q1 is OFF and Q2 is Sat

$$i_{E1} = 0 \& i_{E2} = I$$



The DA responds to any change in the Differential mode Signal



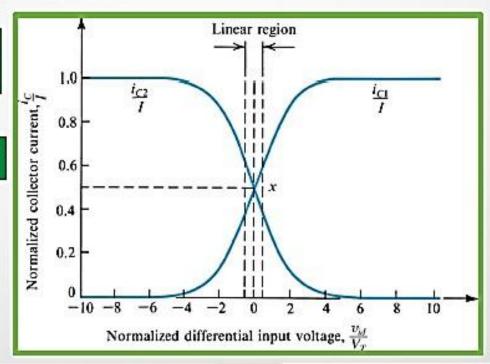
- Emitter Coupled Circuit Large Signal Analysis:
  - Q1 and Q2 are active and Identical

$$i_{E1} = rac{I_s}{lpha} \exp\left(rac{v_{BE1}}{V_T}
ight) \quad i_{E2} = rac{I_s}{lpha} \exp\left(rac{v_{BE2}}{V_T}
ight)$$

$$\frac{i_{E1}}{i_{E2}} = \exp\left(\frac{v_{ID}}{V_T}\right) \qquad i_{E1} + i_E$$

$$rac{i_{E1}}{i_{E1} + i_{E2}} = rac{1}{1 + \exp\left(rac{-v_{ID}}{V_T}
ight)}$$

$$rac{i_{E2}}{i_{E1} + i_{E2}} = rac{1}{1 + \exp\left(rac{v_{ID}}{V_T}
ight)}$$



$$i_{E1} = rac{I}{1 + \exp\left(rac{-
u_{ID}}{V_T}
ight)}$$

$$i_{E2} = rac{I}{1 + \exp\left(rac{
u_{ID}}{V_T}
ight)}$$

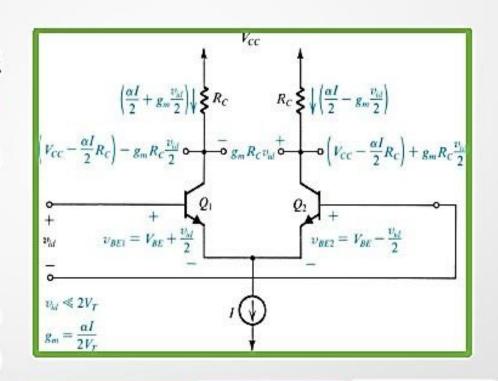




- Small Signal Analysis Differential Mode Gain
- DC Operating point is adjusted such that:

$$I_{c2}=I_{c1}=lpharac{I}{2}pproxrac{I}{2}$$

The circuit is like two
Common Emitters with
the output taken as a
difference between the
two Collectors



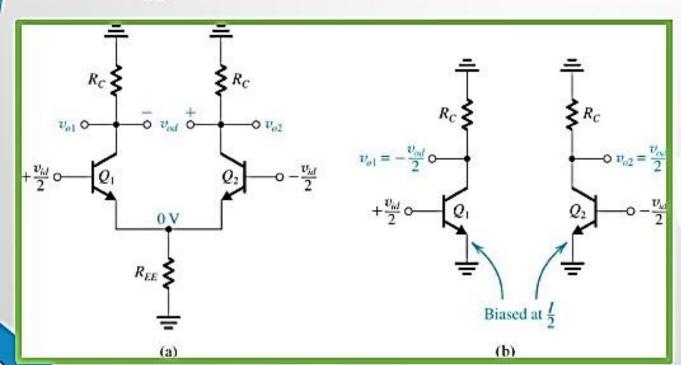
$$g_{m2} = g_{m1} r_{m1} = r_{m2} r_{o1} = r_{o2}$$

$$\frac{v_{c2} - v_{c1}}{v_{id}} = g_m R_C$$





- Small Signal Analysis Differential Mode Gain
- We can use half Circuit Concept to calculate the gain
- $\square$  R<sub>EE</sub> is the Current Source Resistance ( $v_e$  is Zero, why?)



$$\frac{v_{od}}{v_{id}} = \frac{v_{c2} - v_{c1}}{v_{id}} = g_m R_C$$

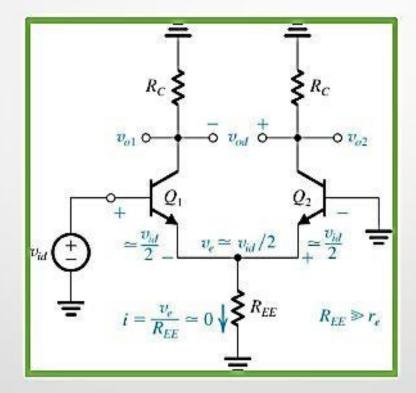




Small Signal Analysis Differential Mode Gain

Notes: What if the signal is not applied fully differential?

(Derive!)

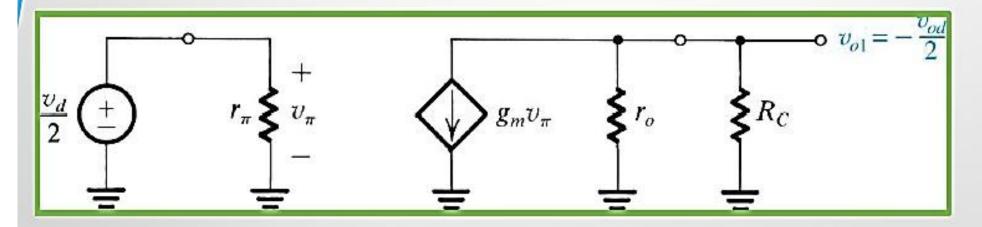






- Small Signal Analysis Differential Mode Gain
- Notes: if r<sub>o</sub> is taken into Consideration, then:

$$\frac{v_{od}}{v_{id}} = \frac{v_{c2} - v_{c1}}{v_{id}} = g_m(R_C // r_o)$$



$$R_{in}=2r_{\pi}$$

$$R_{out} = R_C // r_o$$

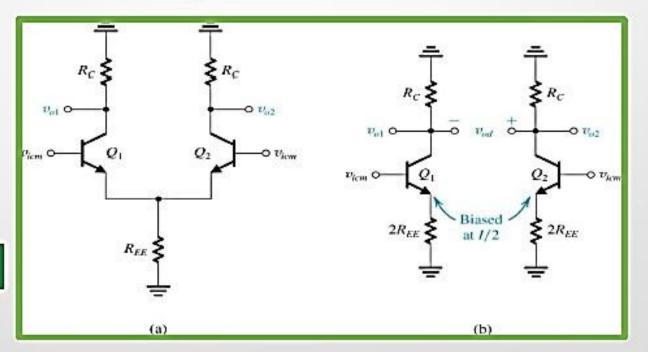




- Small Signal Analysis Common Mode Gain
- R<sub>EE</sub> is the Current Source Resistance ( $v_e$  is not Zero in the common mode Case, why?)

$$\frac{v_{ocm}}{v_{id}} = \frac{v_{c2}}{v_{id}} \cong \frac{-g_m R_C}{1 + 2g_m R_{EE}}$$

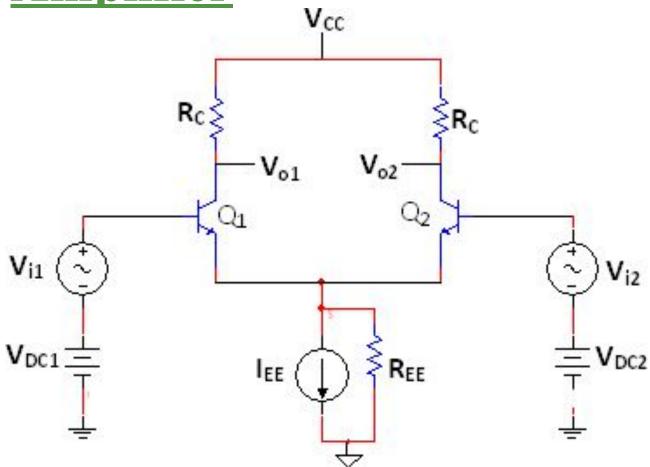
 $|CMRR| \cong 1 + 2g_m R_{EE}$ 





#### Summary- BJT Differential

**Amplifier** 



**REE** 

Current Source output resistance

**IEE** 

Current source value

Vi1 = Vic + (Vid/2)

Vi2 = Vic - (Vid/2)

Vic input common signal (Noise(

Vid differential input signal





$$D.c. Analysis (Vi1 \rightarrow short, Vi2 \rightarrow short)$$

$$X IE_1 = IE_2 = \frac{I}{2}$$

$$X IC = X IE$$

$$X IC_1 = IC_2 = \frac{XI}{2}$$

$$XICM = Vo2 = VCC - \frac{XI}{2}RC$$

$$RC = VICM =$$





12) A.c. Anolysis (VICM = o short) Vi1 = Vic + Vid Vice = o) Uiz = Vic - Vid Using half-Circuit Conapt [a] Differential-mode gain  $ADM = \frac{V.d/2}{Uid/2} = \frac{-\beta ib Rc}{ib rc}$ ,  $\beta = gm rc$ ADM = - B RC = -9m Rc differential





16 Common-mode gain = \frac{-\beta\ib \RC}{\ib\lambda\pi + (1+\beta)\ib 2\REE VC+B)16  $ACM = \frac{-\beta RC}{(\pi + 2(1+\beta)REE)}$ S2REE B = 2m/T 1+B & B

$$ACM \stackrel{\text{\tiny L}}{=} \frac{-g_{m}Y_{\pi} RC}{Y_{\pi} + 2(g_{m}Y_{\pi})REE}$$

$$ACM \stackrel{\text{\tiny L}}{=} \frac{-g_{m}RC}{1 + 2g_{m}REE}$$





#### Common Mode Rejection Ratio (CMRR)

$$CMRR = \frac{A_{DM}}{A_{CM}} \approx \frac{-gmRc}{-gmRc} (1 + 2gmREE)$$

$$CMRR = 1 + 2gmREE$$

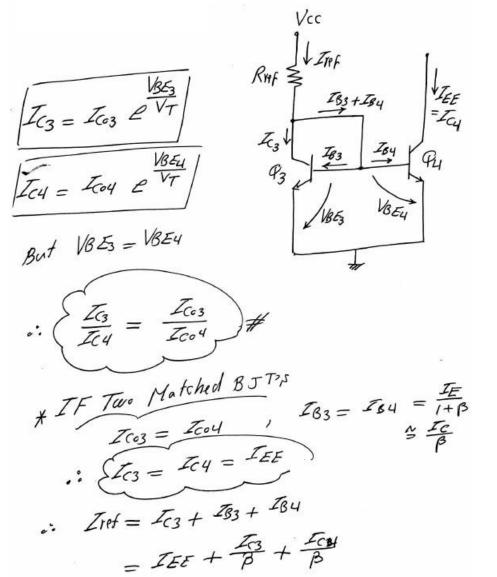
$$CMRR |_{dB} = 20 \log (1 + 2gmREE)$$

$$CMRR |_{dB} = 20 \log (1 + 2gmREE)$$





#### **Current Mirror Current Source**



in 
$$I_{ef} = I_{EE} + 2 \frac{I_{C3}}{\beta} = I_{EE} + 2 \frac{I_{EF}}{\beta}$$

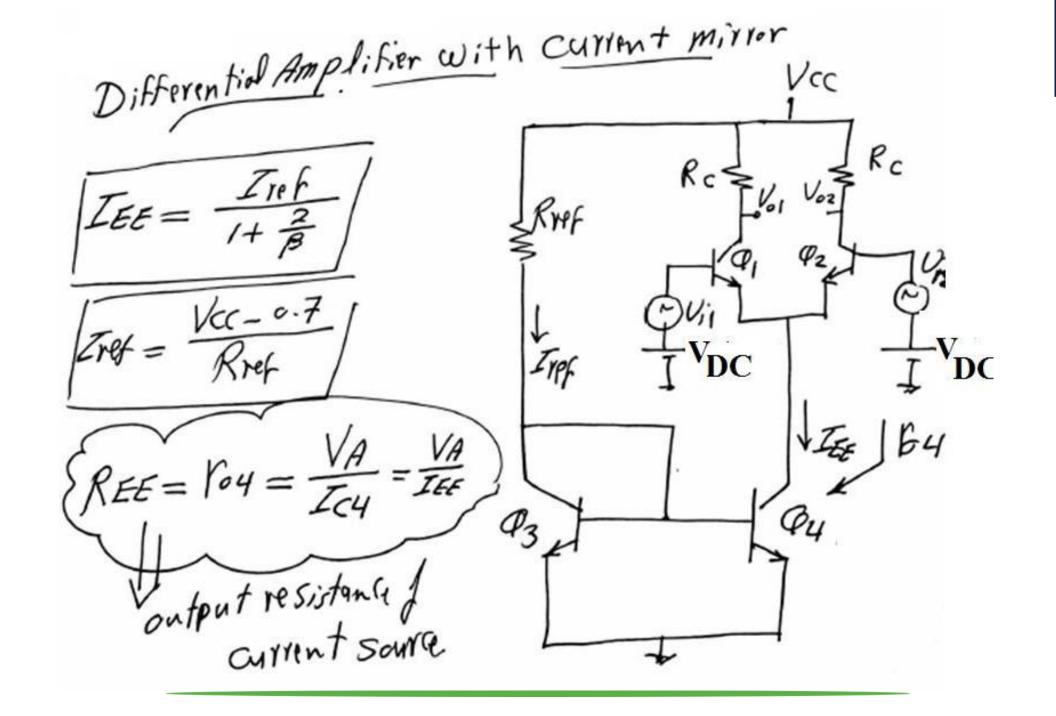
$$I_{ef} = I_{EE} \left[ 1 + \frac{2}{\beta} \right]$$

$$I_{ef} = \frac{I_{ef}}{1 + \frac{2}{\beta}}$$

$$I_{ef} = \frac{V_{cc} - V_{gE_3}}{R_{ef}} = \frac{V_{cc} - \frac{1}{\gamma}}{R_{ref}}$$









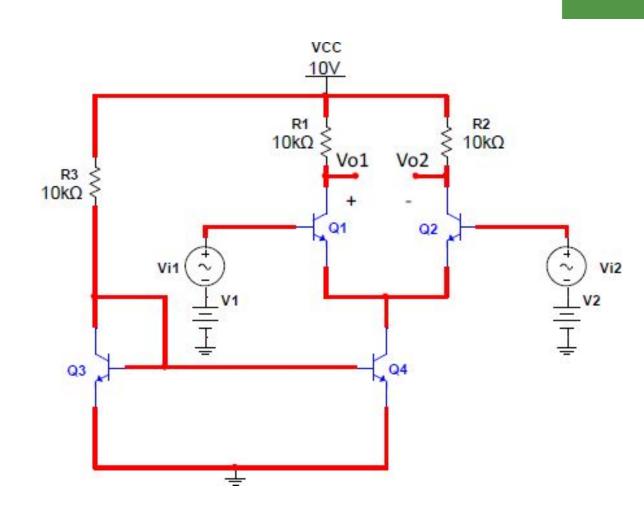
#### **Example**

#### <u>1:</u>

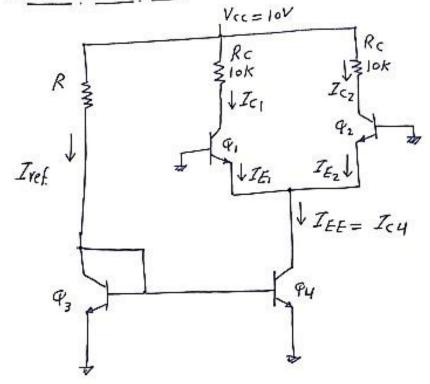
#### For the BJT differential pair shown in Figure, Calculate:

- (a) All DC collector currents.
- (b) The differential mode gain (Adm).
- (c) The common mode gain (Acm).
- (d) The CMRR in dB.

$$B = 100, VT = 0.025 V, VA4 = 100 V.$$



#### **Solution:**

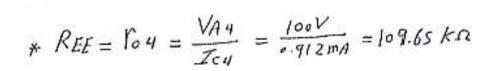


\* Iref = 
$$\frac{V_{CC} - V_B E_3}{R} = \frac{10 - 0.7}{10} = 0.93 \text{ mA}$$

\* 
$$I_{C4} = I_{EE} = \frac{I_{RF}}{1 + \frac{2}{\beta}} = \frac{0.93}{1 + \frac{2}{100}} \approx 0.912 \,\text{mA}$$

$$X IE_1 = IE_2 = \frac{IEE}{2} = 0.456 \text{ mA}$$

$$* I_{c_1} = I_{c_2} = \frac{\beta}{1+\beta} I_{E_1} = 0.451 \text{ mA}$$



\* 
$$\int_{\mathcal{I}_1} = \int_{\mathcal{I}_2} = \beta \frac{V_T}{I_{c_1}} = 100 \times \frac{0.025}{0.451} = 5.54 \text{ K.S.}$$



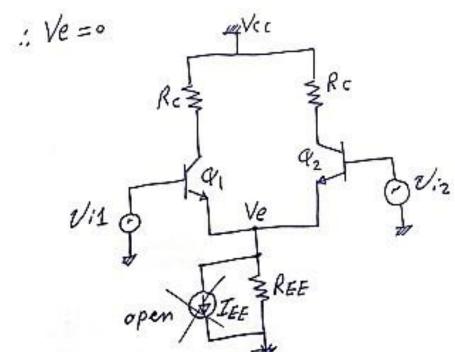


(b) The differential Mode gain: (Adm)

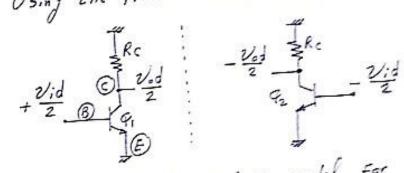
In general, 
$$Vii = Vic + \frac{Vid}{2}$$
 $Viz = Vic - \frac{Vid}{2}$ 

Exp. Differential input,

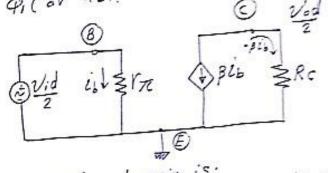
For Differential input, 
$$Viz = -\frac{2/id}{2}$$



Using the Half- Circuit Concept



Draw the small-signed Themodel For Picor Q2).



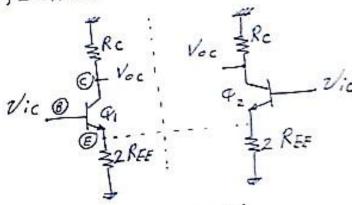
Differential mode gain is:  

$$Adm = \frac{20d}{2id} = \frac{(2.d/2)}{(2id/2)} = \frac{(-82b)Rc}{2b. Yr}$$

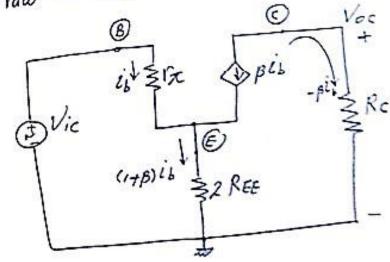




The half-circuit then will be:



Draw the small-signof x-model.



$$Acm = \frac{-\beta Rc}{V\pi + (1+\beta) 2REE} = \frac{-1}{5.5}$$

$$Rem = \frac{-\beta Rc}{V\pi + (1+\beta) 2REE} = \frac{-100 \times 10}{5.54 + 101 \times 2 \times 109}$$



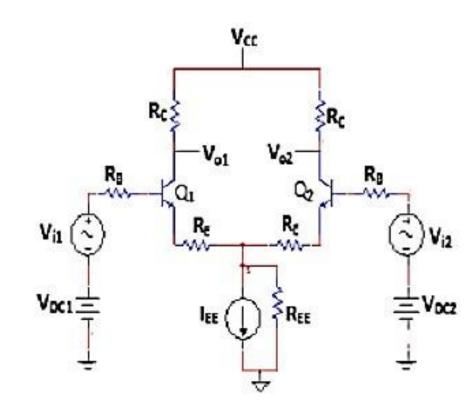
#### **Example**

#### **2:**

Analyze the differential amplifier circuit shown in Figure given:

 $R_c = 10 \text{ k}\Omega$ ,  $R_B = 1 \text{ k}\Omega$ ,  $R_E = 0.5 \text{ k}\Omega$ ,  $R_{EE} = 100 \text{ k}\Omega$   $I_{EE} = 2\text{mA}$ ,  $\beta = 200$ , Calculate:

- (a) The differential mode gain.
- (b) The common mode gain.
- (c) The CMRR in dB.



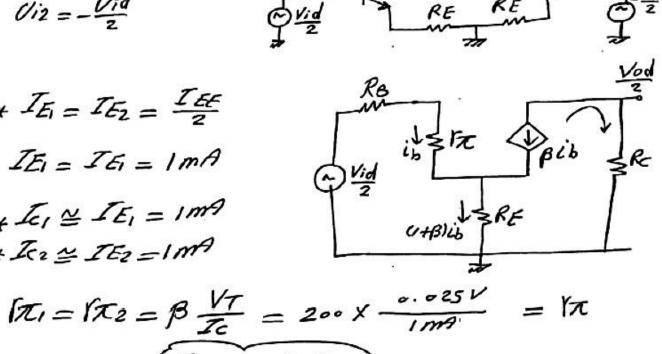




#### **Solution:**

(a) The differential mode gain.

$$\begin{array}{l} \text{Ui1} = \text{Uic} + \frac{\text{Vid}}{2} \\ \text{Ui2} = \text{Uic} - \frac{\text{Vid}}{2} \\ \text{Differential Mode gain} \\ \text{Ui1} = \frac{\text{Vid}}{2} \\ \text{Ui2} = -\frac{\text{Vid}}{2} \end{array}$$



$$Ad = \frac{Vod/2}{Vid/2} = \frac{Vod}{Vid} = \frac{(-\beta Ub)RC}{UbRB + UbRA + (I+\beta)UbRE}$$

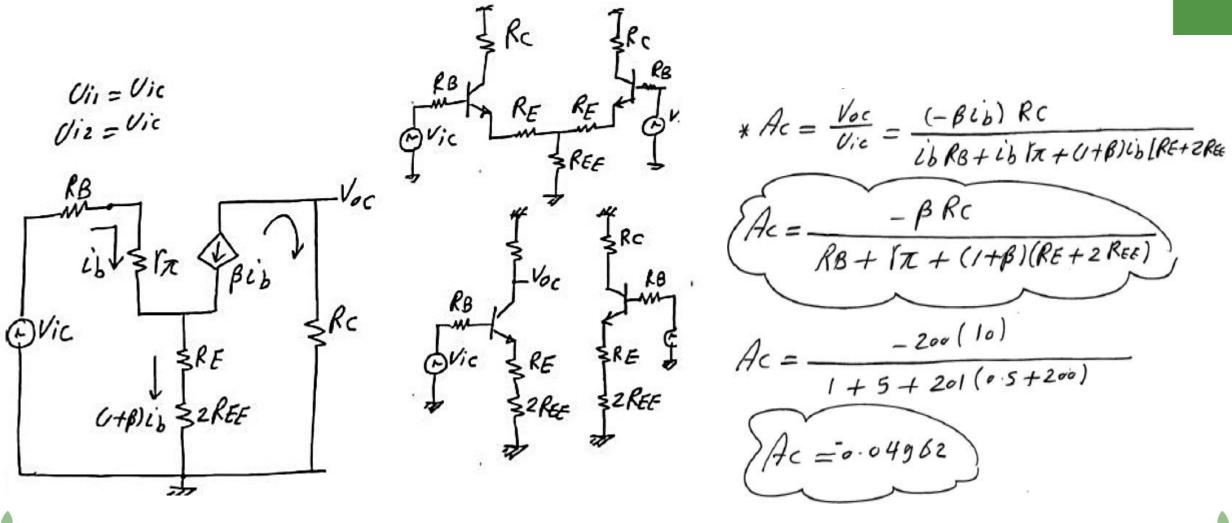
$$Ad = \frac{-\beta RC}{RB + (I+\beta)RE}$$

$$Ad = \frac{-200(10)}{I + 5 + (201 \times 0.5)}$$

$$Ad = -18.78$$



(b) The common mode gain.





(c) The CMRR in dB.

$$CMRR = \frac{Ad}{Ac} = \frac{-18.78}{-0.04962}$$

$$CMRR = 378.478$$

$$CMRR(dB) = 20 Log (378.478) = 51.561$$

$$dB$$



