.wrap_transet

bit loop

   out x. 1   since 0 $[T_3-1]$;

   jmp l x d. zero   snc 1 $[T_1-1]$;

do-one :

    jump bitloop   snc l $[T_2-1]$;

do_zero:

    nop   side   0 $[T_2-1]$;

.wrap

$x = 0$

	T_1	T_2	T_3
Current Instant	L3	L4	L8
Data in FIFO	Y	Y	Y
Stalled	N	N	N
Duty cycle	3	2	5
OSR	0	0	0
Smu x	0	0	0
LED state	0	0 L	0

$x = 0$

$x = 1$