CMPS1134 Fundamentals of Computing

Data Manipulation 1

Computer Science: An Overview
Eleventh Edition

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Chapter 2

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Chapter 2: Data Manipulation

- **□** Computer Architecture
- Machine Language
- □ Program Execution
- ☐ Arithmetic/Logic Instructions
- □ Communicating with Other Devices
- □ Other Architectures

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2

Computer Architecture

- Central Processing Unit (CPU) or processor consists of 3 parts:
 - Arithmetic/Logic unit
 Circuitry that performs operations on data (e.g. addition and subtraction)
 - **2. Control unit**Circuitry for coordinating the machines activities
 - 3. Registers
 Data storage cells that hold: data being manipulated by the CPU, the instruction being executed (instruction register), & address of the next instruction to be executed (program counter)
 - ☐ General-purpose registers☐ Special-purpose registers
- ☐ **Bus** connects the CPU and Main Memory
- Motherboard machine's main circuit board

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Figure 2.1 CPU and main memory connected via a bus

Central processing unit

Register unit

Unit

Bus

Control
unit

Registers

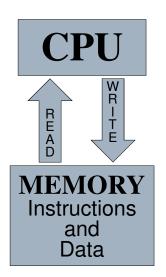
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Stored Program Concept

A program can be encoded as bit patterns and stored in main memory.

From there, the CPU can then extract the instructions and execute them.

In turn, the program to be executed can be altered easily.



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Terminology

- Machine instruction: An instruction (or command) encoded as a bit pattern recognizable by the CPU
- Machine language: The set of all instructions recognized by a machine

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6

Machine Language Philosophies

- □ Reduced Instruction Set Computing (RISC)
 - Few, simple, efficient, and fast instructions
 - Examples: PowerPC from Apple/ IBM/ Motorola and ARM
- □ Complex Instruction Set Computing (CISC)
 - Many, convenient, and powerful instructions
 - Example: Intel

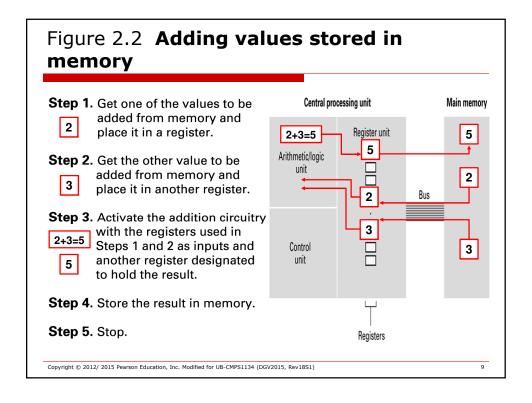
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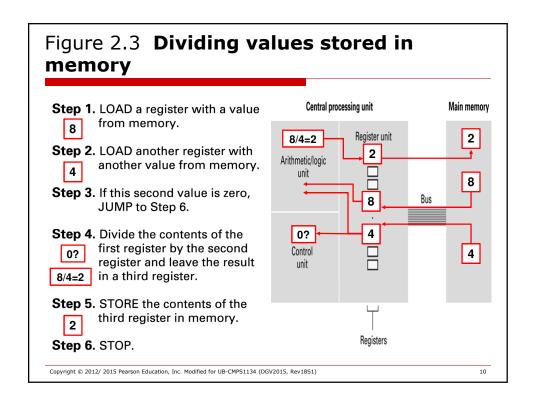
Machine Instruction Types

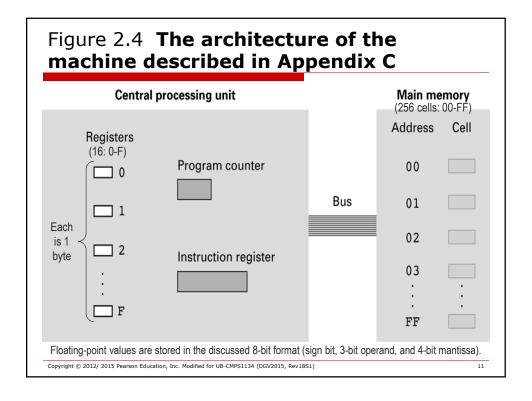
- □ **Data Transfer**: copy data from one location to another
- ☐ **Arithmetic/Logic**: use existing bit patterns to compute a new bit patterns
- □ **Control**: direct the execution of the program

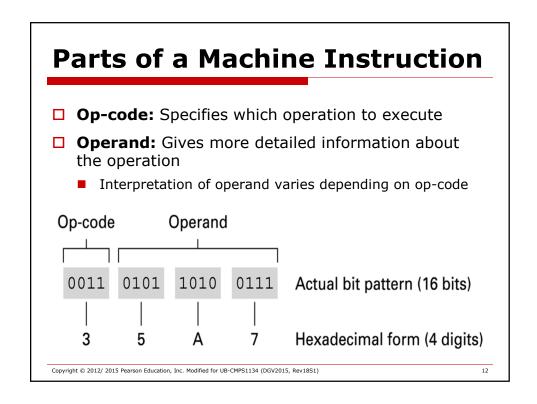
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8









Appendix C: A Simple Machine Language		
Op-code	Operand	Description
1	RXY	LOAD reg. R from cell XY.
2	RXY	LOAD reg. R with XY.
3	RXY	STORE reg. R at XY.
4	ORS	MOVE R to S.
5	RST	ADD S and T into R. (2's comp.)
6	RST	ADD S and T into R. (floating pt.)
7	RST	OR S and T into R.
8	RST	AND S and T into R.
9	RST	XOR S and T into R.
Α	R0X	ROTATE reg. R X times.
В	RXY	JUMP to XY if $R = reg. 0$.
С	000	HALT
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