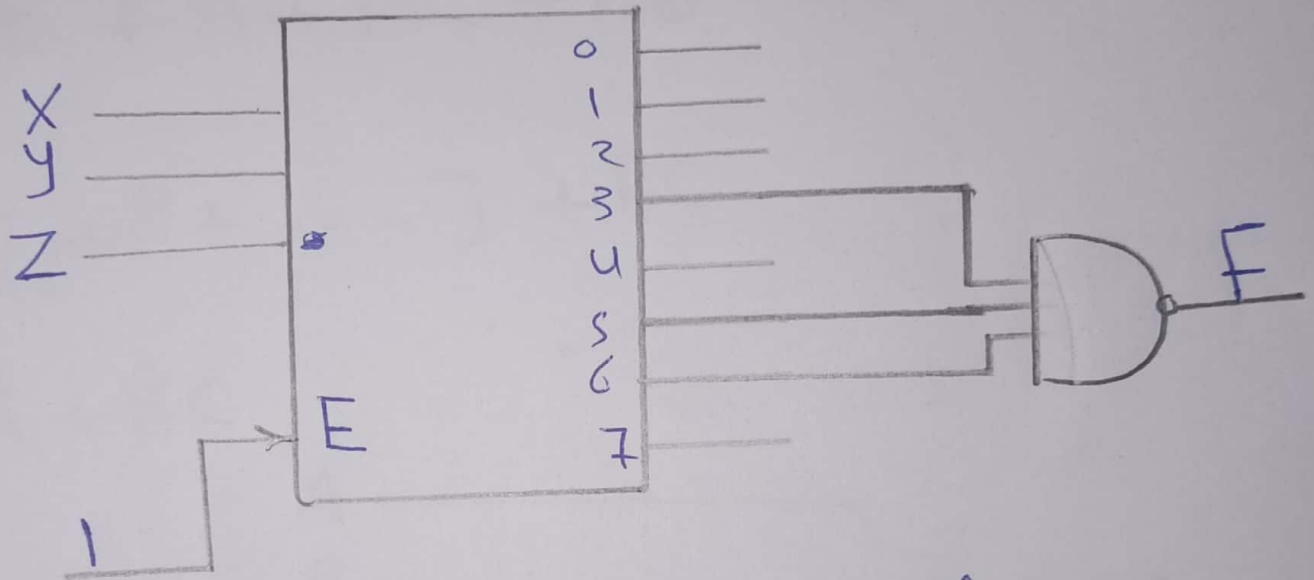


Quiz 1 answers

1 $F=1$ when ?



Z is the least Significant

$$F = \overline{(\quad)} \cdot (\quad) \cdot (\quad)$$

Below the equation, three red brackets are drawn under the three terms in parentheses. Red arrows point from each bracket to a red '0'.

یکفرد است
input واحد پس
 $0 =$

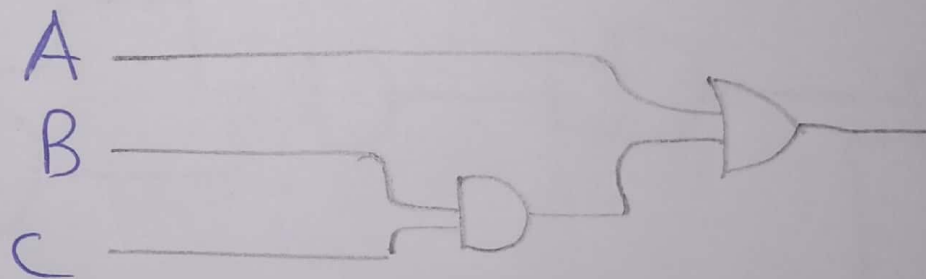
out of 3 inputs
are $= 0$

Q2 min number of gates
to implement $AB + A(A+c) + B(A+c)$

$$AB + A + AC + AB + BC$$

$$A[1 + B + \dots] + BC$$

$$= A + BC$$

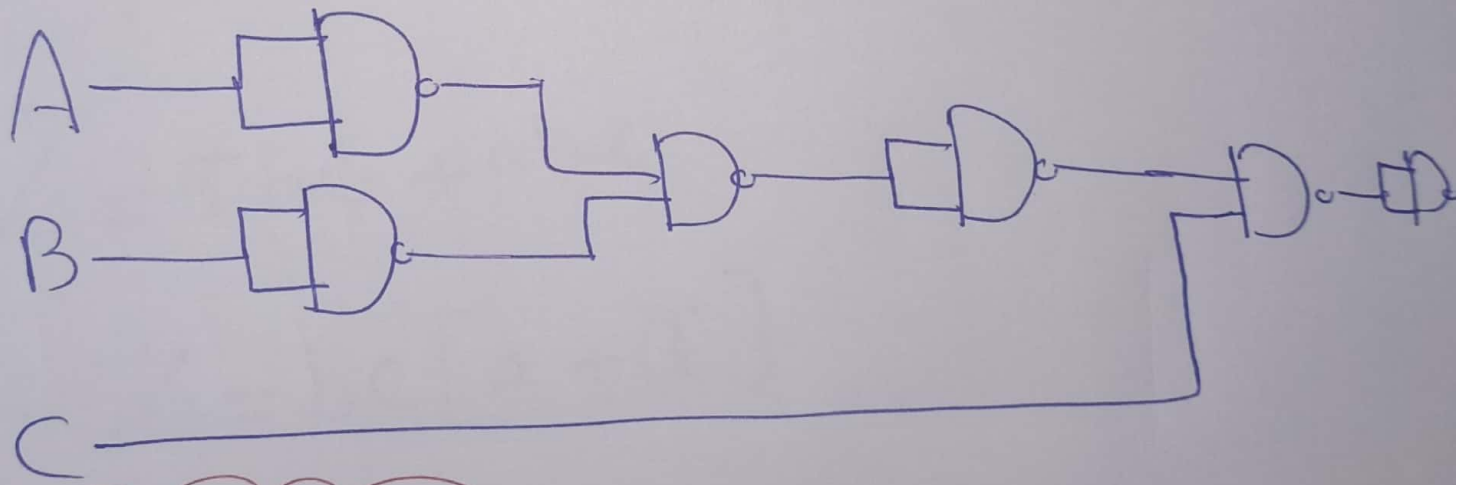


2 gates

Q3 $Z = \overline{A} \overline{B} C$ implement

Using 2-input NAND gates
(min number)

$$Z = \overline{\overline{\overline{A} \overline{B} C}}$$



5 gates

Q 5 Simplification of $f(A, B, C, D)$

$$= \sum_m(5, 6, 7, 13, 14, 15) + \sum_d(0, 8, 10)$$

Yield the function ---

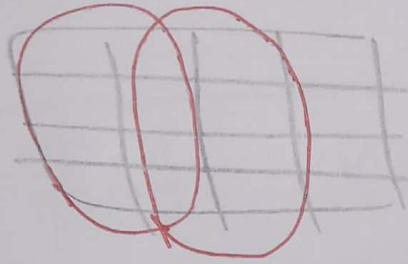
CD \ AB	00	01	11	10
00	d 0	1	3	2
01	4	5	7	6
11	12	13	15	14
10	d 8	9	11	d 10

$$F = \sum_m(5, 6, 7, 13, 14, 15)$$

ال د ملهاتش تأشير

Q6 Simplify

AB	00	01	11	10
CD				
00	1	d	1	d
01	1	d	1	d
11	1	d	1	d
10	1	d	1	0



$$= \bar{A} + B$$

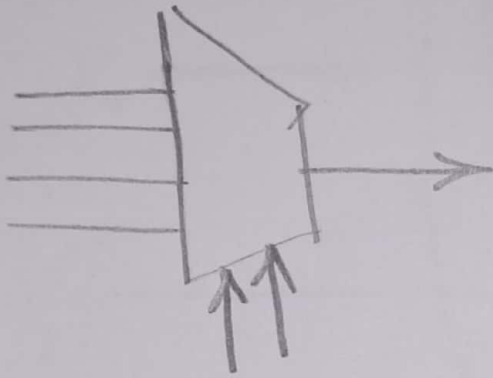
none of the others

Q7) which acts as a device that represent the highest of the inputs that are active

Priority encoder

Q8) Which has n selectors
and 2^n inputs

Mux



Q9) Find $X \oplus Y \oplus XY$

$$X \oplus Y (1 \oplus X)$$

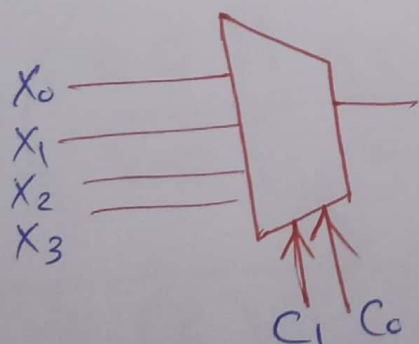
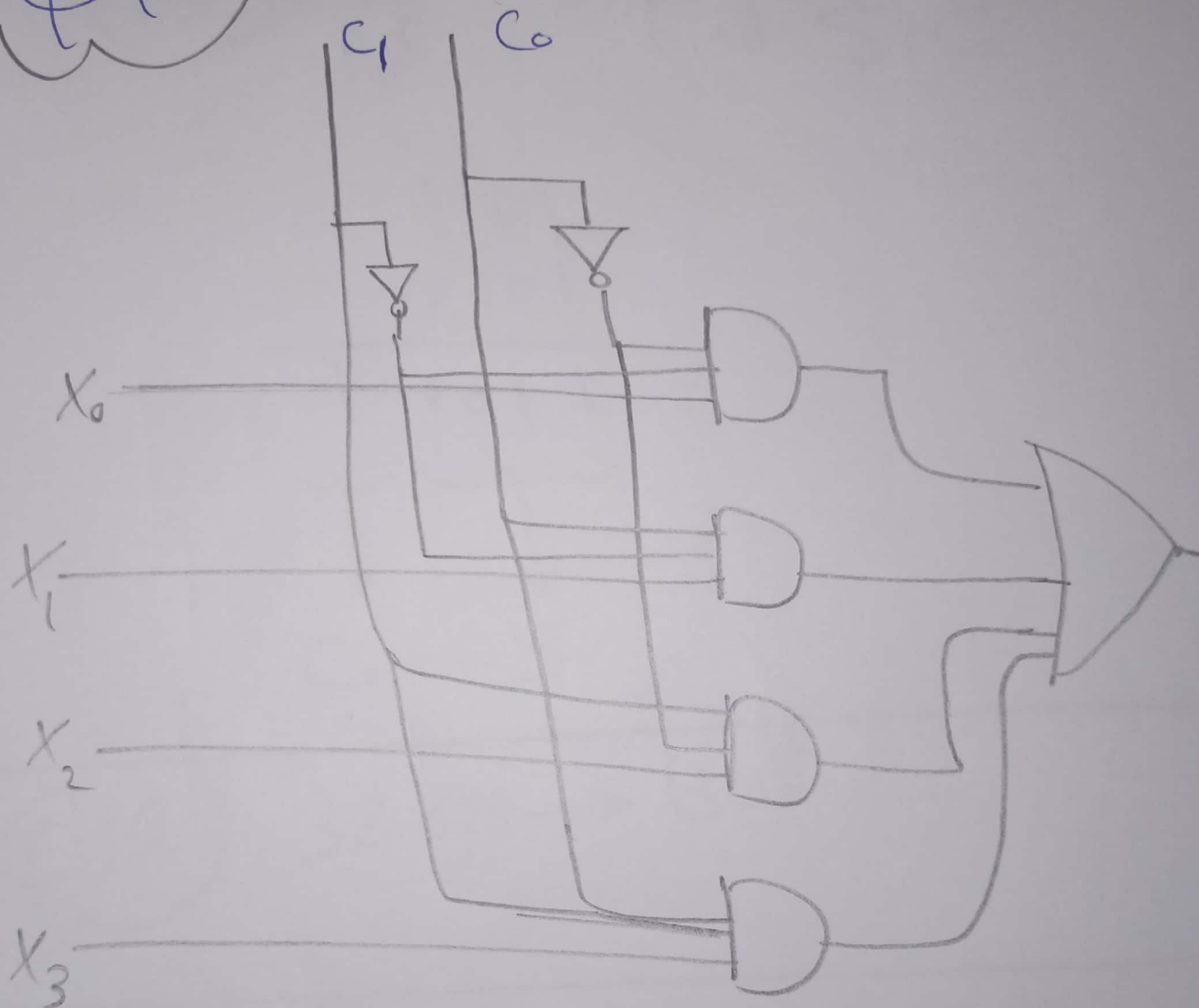
$$= X \oplus Y \bar{X} = \bar{X}(Y \bar{X}) + X(\bar{Y} + X)$$

$$= \bar{X}Y + X\bar{Y} + X$$

$$= \bar{X}Y + X = X + Y$$

#

Q10) the circuit acts as



Max

$$M = \overline{C_0} \overline{C_1} X_0 + C_0 \overline{C_1} X_1 + \overline{C_0} C_1 X_2 + X_3 C_1 C_0$$

Q11) $1101 + 0101$ and carry
input = 1 Find S & C_o

$$\begin{array}{r}
 \begin{array}{cccc}
 & 1 & 1 & 1 \\
 & 1 & 1 & 0 \\
 1 & 1 & 0 & 1 \\
 \hline
 1 & 0 & 0 & 1 \\
 \end{array} \\
 \begin{array}{cc}
 \downarrow & \downarrow \\
 C_o & S
 \end{array}
 \end{array}$$

Q12) $32.625 \rightarrow BCD$

$$\left(\begin{array}{ccc|cc}
 0 & 0 & 11 & 00 & 10 \\
 \hline
 & 3 & & 2 & \\
 \end{array} \cdot \begin{array}{ccc|cc}
 0 & 11 & 000 & 100 & 101 \\
 \hline
 & 6 & 2 & 5 & \\
 \end{array} \right)_{BCD}$$

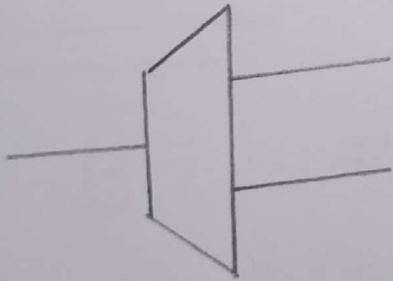
Q13) --- is the max no of
gates can be fed by my gate
output fan-out

Q 14 Simplest decoder
is ---

n inputs $\rightarrow 2^n$

$n=1 \rightarrow 2^1$

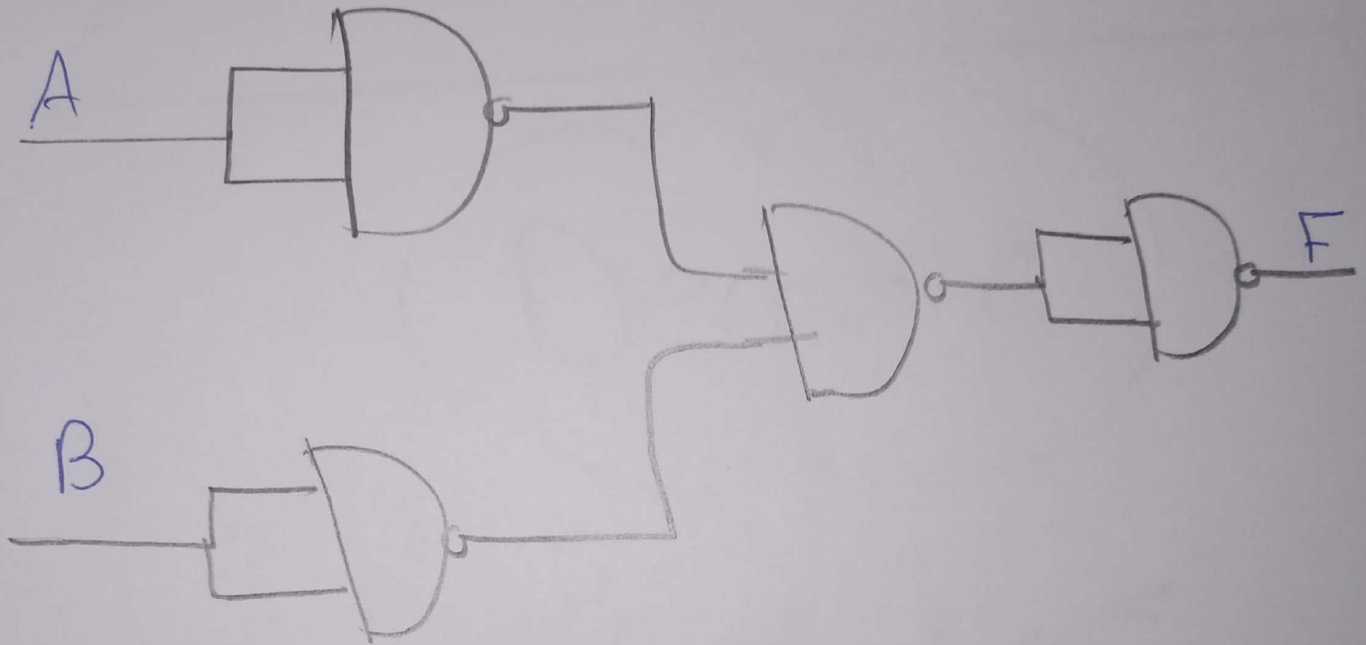
1-2 decoder



Q15 Which act as a circuit used to pass any one of several inputs to a single output?

Mux

Q16) the circuit represents



$$\overline{\overline{A} \overline{B}} = \overline{A \overline{B}} = A + B$$

NOR

~~///~~

Q17

Which activates only one output @ a time?

decoder

Q18

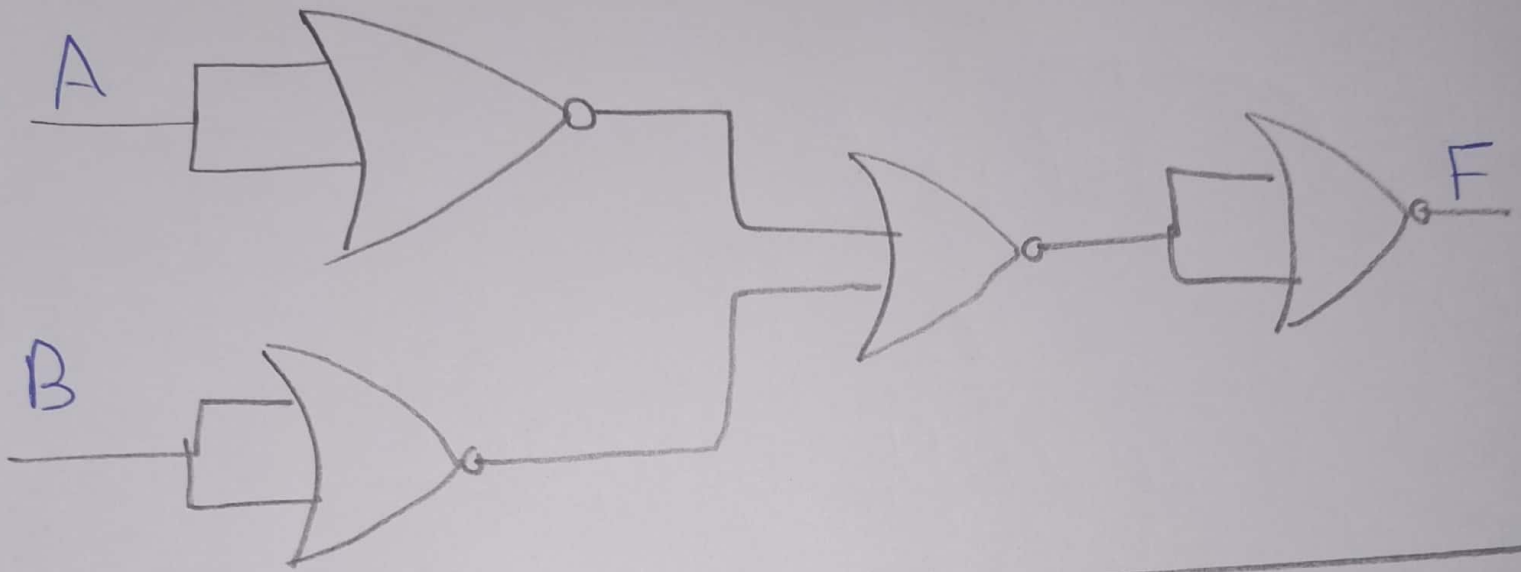
Simplist Mux is —

n ————— 2^n
Selector inputs

$n=1$ ————— 2^1

\therefore 2-1 Mux

Q19) Find the output



$$\overline{(\overline{A} + \overline{B})} = \overline{A}B$$

NAND
gate

Q20) if 2 inputs are active on a Priority encoder ---- will be coded -

The higher value

محمد
18/11/2022