

National University of Computer and Emerging Sciences, Lahore Campus



Course:	Digital Logic Design	Course Code:	EE-227
Program:	BS (Computer Science)	Semester:	Spring 2021
Duration:	10 Minutes	Total Marks:	10
Paper Date:	16-June-2021	Weight	3 %
Section:	D	Page(s):	2
Exam:	Quiz 2	Reg. No.	

Instruction/Notes: Calculators are strictly not allowed in all exams
Plagiarism will be dealt seriously causing an F in course

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1. Design a 4-to-16-line decoder with enable using five 2-to-4-line decoders with enable.

2. Design a combinational circuit that accepts a 3-bit number and generates a 6-bit binary number output equal to the square of the input number.