National University of Computer and Emerging Sciences, Lahore Campus



Digital Logic Design Course: **BS(Computer Science)** Program: **Duration:**

Paper Date: Section: Α Exam:

25 Minutes

Quiz-2

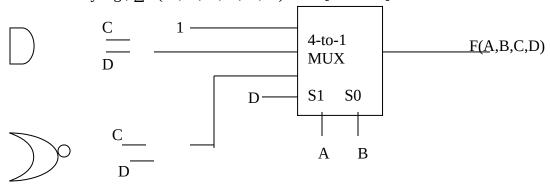
Course Code: Semester:

EE227 Fall 2018 10

Total Marks: Weight:

Page(s): **Roll Number**

Q1. Determine the following function implemented using a multiplexer. Write minterms only e.g., $\sum m(31,32,33,34,35,36)$ [5 marks]



F(A,B,C,D) =

[Q2] Design a dual 8x1 MUX using four single 4x1 MUX and a dual 2x1 MUX. [5marks]