

EE1005 – Digital Logic Design (BCS-2E)

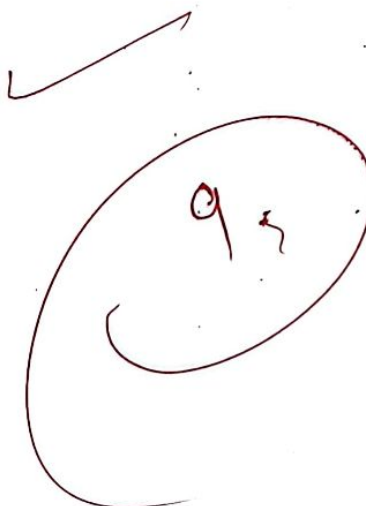
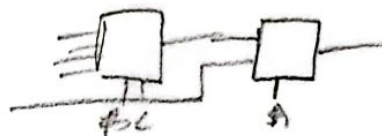
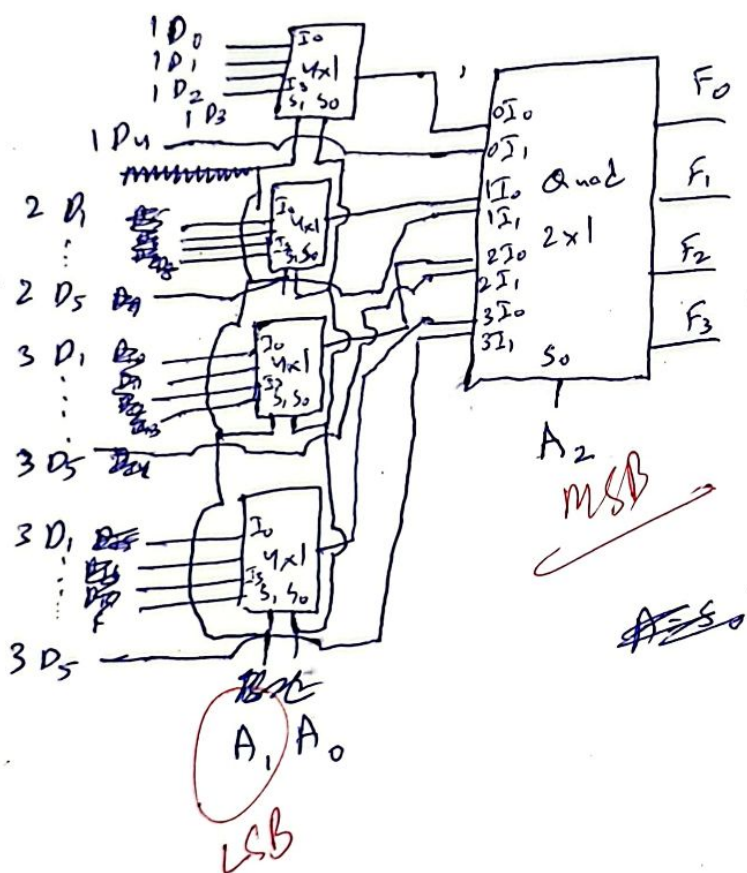
Quiz#2 (CLO-04)

Total Marks: 10

Time Allowed: 10 minutes

Roll #: [REDACTED]

Question: Construct a quad 5x1 multiplexer using four single 4x1 multiplexers and one quad 2x1 multiplexer. Label all input lines and selections properly. Also clearly mention the LSB and MSB of selection bits.



Date: March 28, 2024