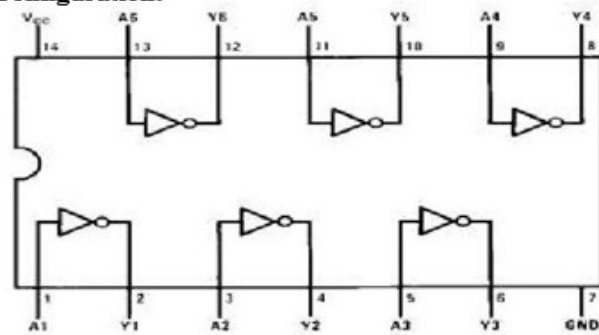
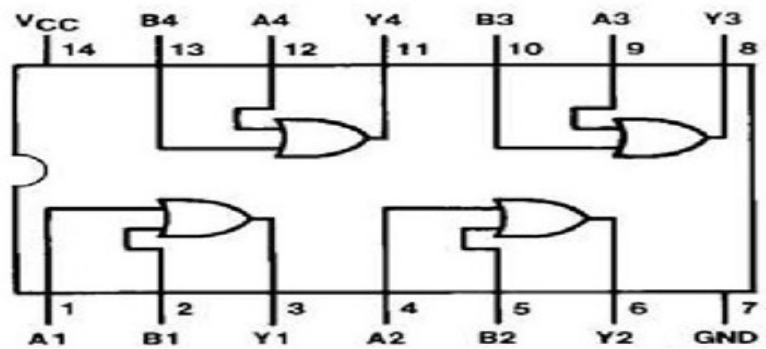


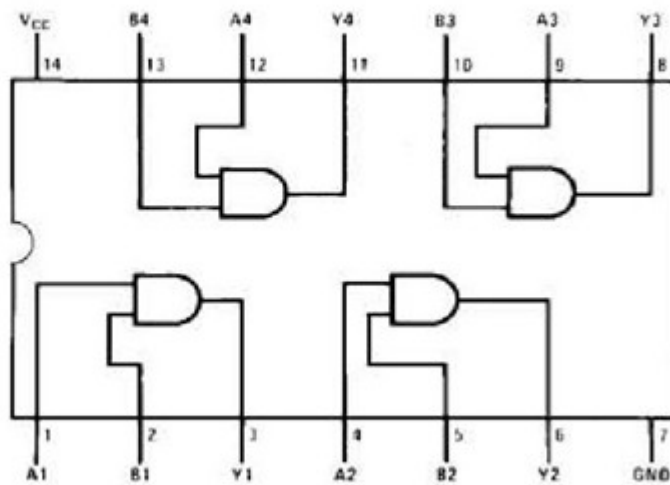
IC 74LS04 Pin Configuration:



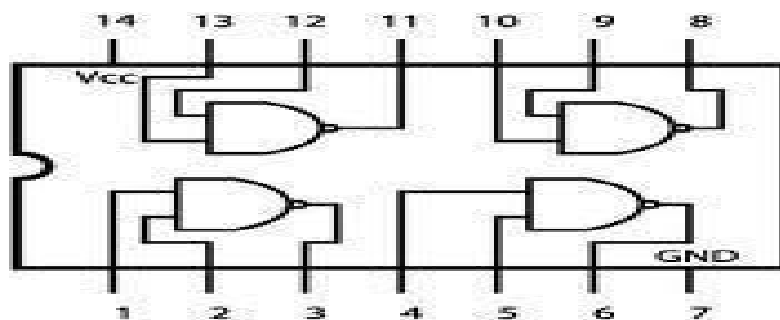
IC 74LS32 Configuration:



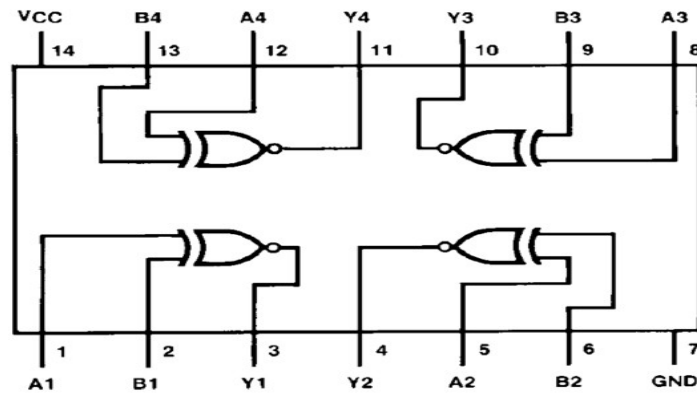
IC 74LS08 Configuration:



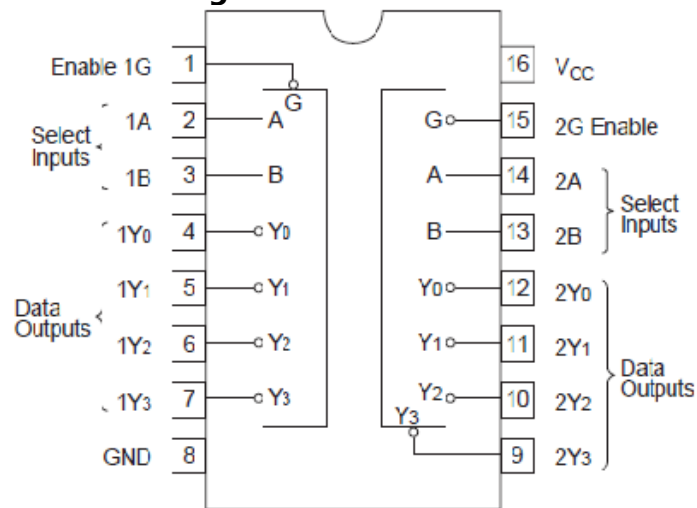
Pin Diagram of 74LS00 IC:



Pin Diagram of 74LS02:



74LS139 IC Pin Configuration



(Top view)

74LS138 IC Pin Configuration

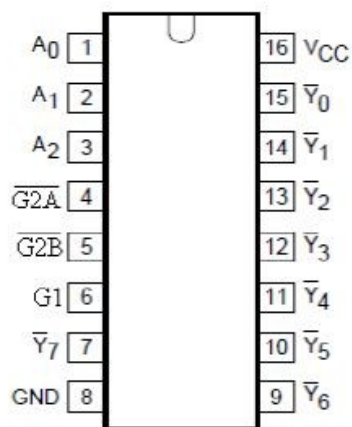


Fig 1. Pin Configuration

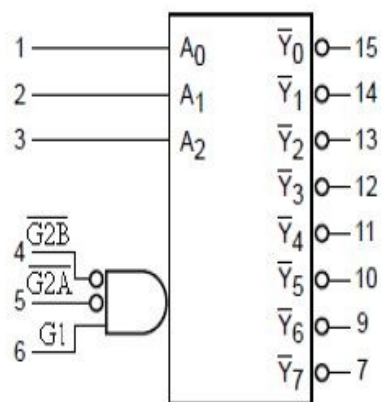
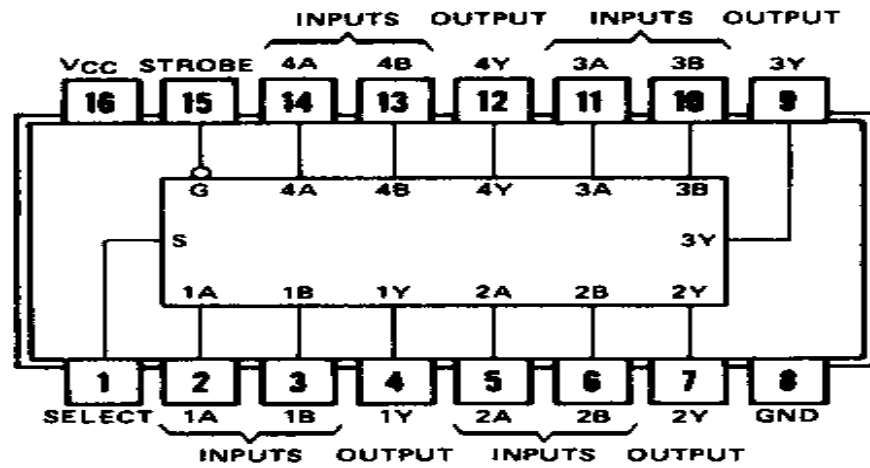


Fig 2. Logic Symbol

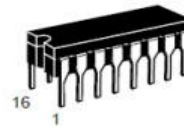
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| SELECT INPUTS | | INPUTS (a or b) | | | | | | OUTPUT |
|----------------|----------------|-----------------|----------------|----------------|----------------|----------------|--|--------|
| S ₀ | S ₁ | \bar{E} | I ₀ | I ₁ | I ₂ | I ₃ | | Z |
| X | X | H | X | X | X | X | | L |
| L | L | L | L | X | X | X | | L |
| L | L | L | H | X | X | X | | H |
| H | L | L | X | L | X | X | | L |
| H | L | L | X | H | X | X | | H |
| L | H | L | X | X | L | X | | L |
| L | H | L | X | X | H | X | | H |
| H | H | L | X | X | X | L | | L |
| H | H | L | X | X | X | H | | H |

H = HIGH Voltage Level
L = LOW Voltage Level
X = Don't Care

TRUTH TABLE



V_{CC} = PIN 16
GND = PIN 8

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