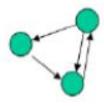
FSM-Example

Finite State Machines



Finite state machines (FSMs) are powerful design elements used to implement algorithms in hardware.

An FSM is a 6-tuple, $\langle Z, X, Y, \delta, \lambda, z0 \rangle$, where:

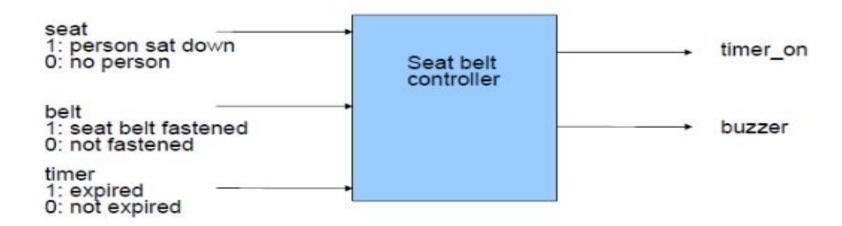
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Z is a set of states \{z0, z1, ..., zl\}, X is a set of inputs \{x0, x1, ..., xm\}, Y is a set of outputs \{y0, y1, ..., yn\}, \delta is a next-state function (i.e., transitions), mapping states and inputs to states, (Z \times X \to Y) \lambda is an output function, mapping current states to outputs (Z \to Y), and z0 is an initial state.
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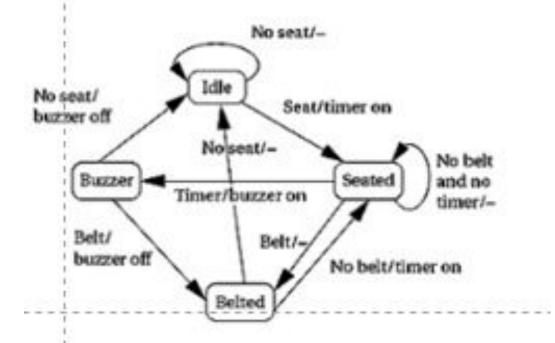
State Machine Example

Design a Simple Seat Belt Controller:

The controller's job is to turn on a buzzer if a person sits in a seat and does not fasten the seat belt within a fixed amount of time. This system has three inputs and one output.

The inputs are a sensor for the seat to know when a person has sat down, a seat belt sensor that tells when the belt is fastened, and a timer that goes off when the required time interval has elapsed. The output is the buzzer





State diagram