EXPERIMENTT NO: 9

Title: - DESIGN AND IMPLEMENTATION OF BINARY TO GRAY CODE CONVERTER

Objective:-

To design and implement 4-bit

- (i) Binary to gray code converter
- (ii) Gray to binary code converter

Tool required:-

1. SynaptiCAD Verilogger

THEORY:

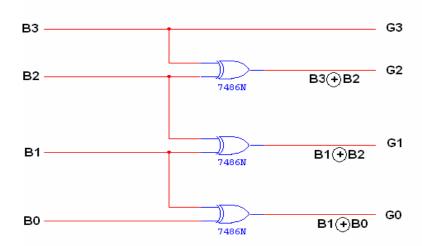
The availability of large variety of codes for the same discrete elements of information results in the use of different codes by different systems. A conversion circuit must be inserted between the two systems if each uses different codes for same information. Thus, code converter is a circuit that makes the two systems compatible even though each uses different binary code.

The bit combination assigned to binary code to gray code. Since each code uses four bits to represent a decimal digit. There are four inputs and four outputs. Gray code is a non-weighted code.

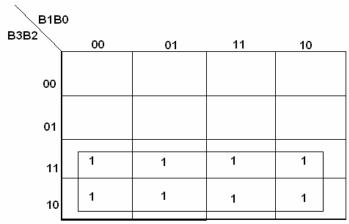
The input variable are designated as B3, B2, B1, B0 and the output variables are designated as G3, G2, G1, Go. from the truth table, combinational circuit is designed. The Boolean functions are obtained from K-Map for each output variable.

LOGIC DIAGRAM:

BINARY TO GRAY CODE CONVERTOR

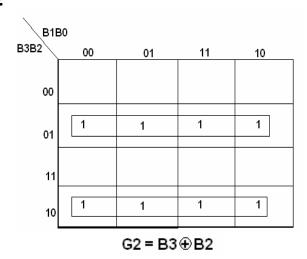


K-Map for G₃:

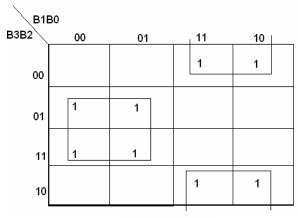


 $G_3 = B_3$

K-Map for G₂:

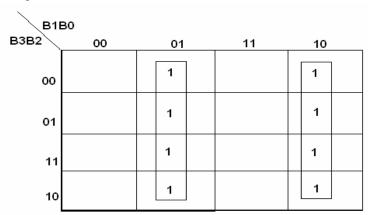


K-Map for G_1 :



G1 = B1⊕B2

K-Map for G₀:



G0 = B1 ⊕ B0

TRUTH TABLE: Binary input Gray code output

В3	B2	B1	В0	G3	G2	G1	G0
0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1
0	0	1	0	0	0	1	1
0	0	1	1	0	0	1	0
0	1	0	0	0	1	1	0
0	1	0	1	0	1	1	1
0	1	1	0	0	1	0	1
0	1	1	1	0	1	0	0
1	0	0	0	1	1	0	0
1	0	0	1	1	1	0	1
1	0	1	0	1	1	1	1
1	0	1	1	1	1	1	0
1	1	0	0	1	0	1	0
1	1	0	1	1	0	1	1
1	1	1	0	1	0	0	1
1	1	1	1	1	0	0	0

TASK:

- Implement Binary to Gray code Converter using Verilog
 a. Draw the diagram with the labels used in Verilog

 - b. Paste the Screenshot of the code
 - c. Paste the screenshot of the time diagram