Diode Applications

Property of diade: H conducts current in one direction while blocking in the other direction Diodes are used in

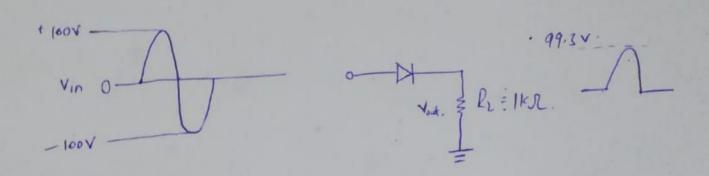
(a) DC power supply. 94 converts AC into constant DC vallage This DC voltage is used into television, CD players and most laboratory equipment. Block obagram of DC power supply:filter dc. Supply:Regulated dc AC Rectifier Filter Regulator Reclifier could be half wave Half wave Roctifier: Vint & R L Vint to th to 11. 12 -

(7)

considering the ideal model when the input is positive l'égèle diode behave as forward bial and conducts through the load resistor Re. Vin to til to til to During the negative half cycle diode act as reverse bias and no current will flow through R. . And voltage acros Rz 4 2000. So Vou = 0 during regative half eyele. Complete output voltage for Hw rectifiers. for Effect of the barrier potential in use of practical dister Np (Ind) = Vp(in) - 0.7v.

Example 2-2: Draw the output voltage of the rectifier for the indicated input voltages?

consider practical diode

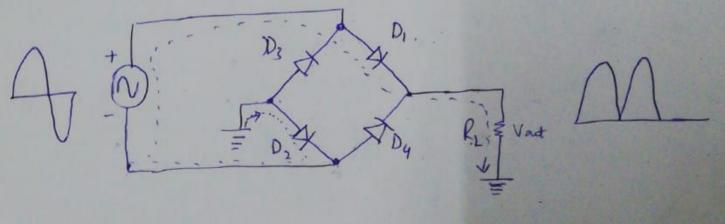


Full wave lectifiers:

A full wave rectifiers allows current through.
the whole yele.

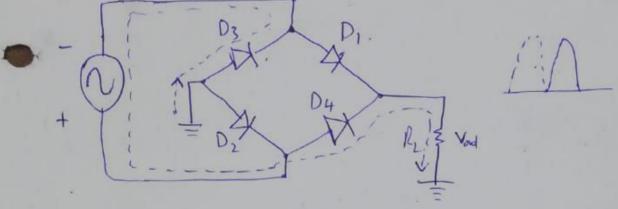
Vin Techifier Vot M

Bridge Full-wave Redifier:



(9)

During positive cycle. Di & Dz ad as forward bias. and conduct current. Dz & D4 ad as reverse bias. During negative Cycle Dz & D4 act as forward bias and conduct current in the same direction as it was during positive half cycle.



Practical model output of full wave Rectifier

Vout = Vin - 2.4.

Power Supply fillers and Regulators: Roctifies Filler Regulators

Fillers: Capacitor input filler, Recitified C. T & Rr.

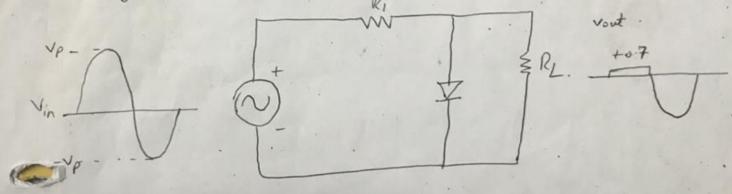
94 reduces ripples

Diode Limiting and clamping circuits:
Diode Limiter circuits are used to dip off
Some portion of signal.

Diode Limiters:-

Diode become forward biased for the half cycle after 0.7v (silicon). For -ve half.

Cycle diode become reverse biased.



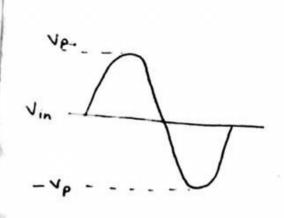
Vout = Vin $\left(\frac{R_L}{R_1 + R_L}\right)$

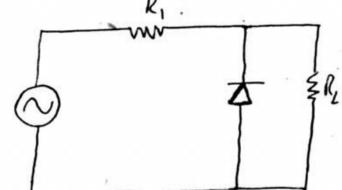
RIZZRL Vout = Vin -

clips negative half cycle:

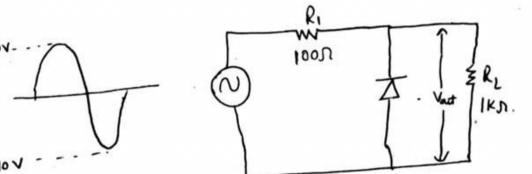
Diode become reverse bissed for the half cycle of the become become forward bissed after -0.70.







Example what would be output against RL?

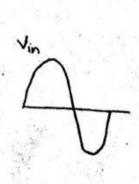


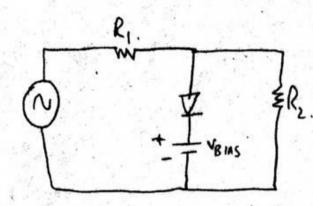
Diode is forward biased and conducts when the input voltage goes below -0.7V.

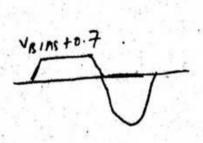
$$V_{\rho}(out) = \left(\frac{R_{L}}{R_{I} + R_{L}}\right) \cdot V_{\rho}(in) = \left(\frac{1kN}{1:1kN}\right) \times io = 9.09V$$

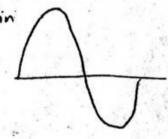
Biased Limiters :-

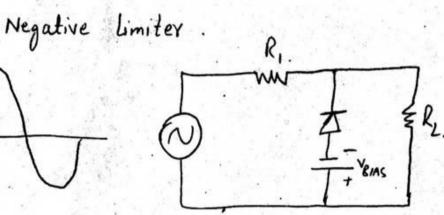
Positive limiter

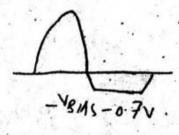






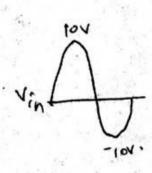


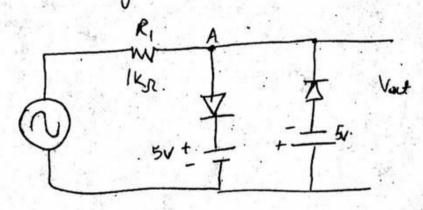




Example: Circuit combining a positive limiter

with a negative limiter.



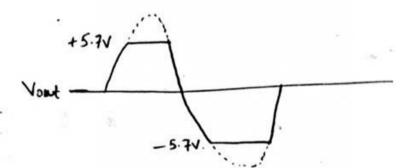


Voltage at point A reaches + 5.7V.

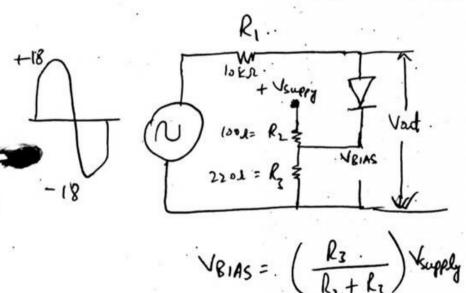
diode D1 conducts and limits the waveform.

Voltage at point B reaches - 5.7V.

diode D2 conducts and limits the -ve waveform.

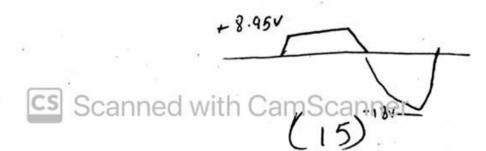


Voltage divider bias:- Positive limiter.



$$V_{g/AS} = \left(\frac{220}{100 + 220}\right)12 = 8.25$$

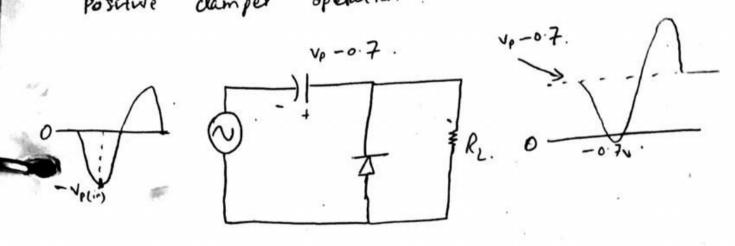
VB 14 8.25



Diode dampers:

dampers adds a de level to an ac Voltage.

positive clamper operation:

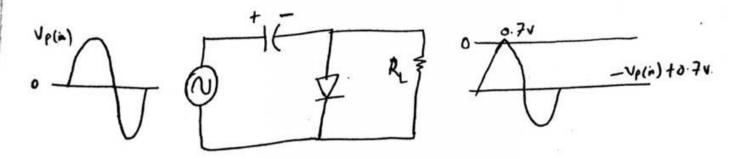


First half of negative peak diode out of F.B. and capacitor gets charged.

As second half of regative peak diode act as

R.B. and capacitor stort discharging. Capacitor become a battery source in series with ac signal and the value of this battery source offset the signal.

Negative clamper operation:-



Special purpose diodes:-Zener diodes, varador diodes, optical diodes.