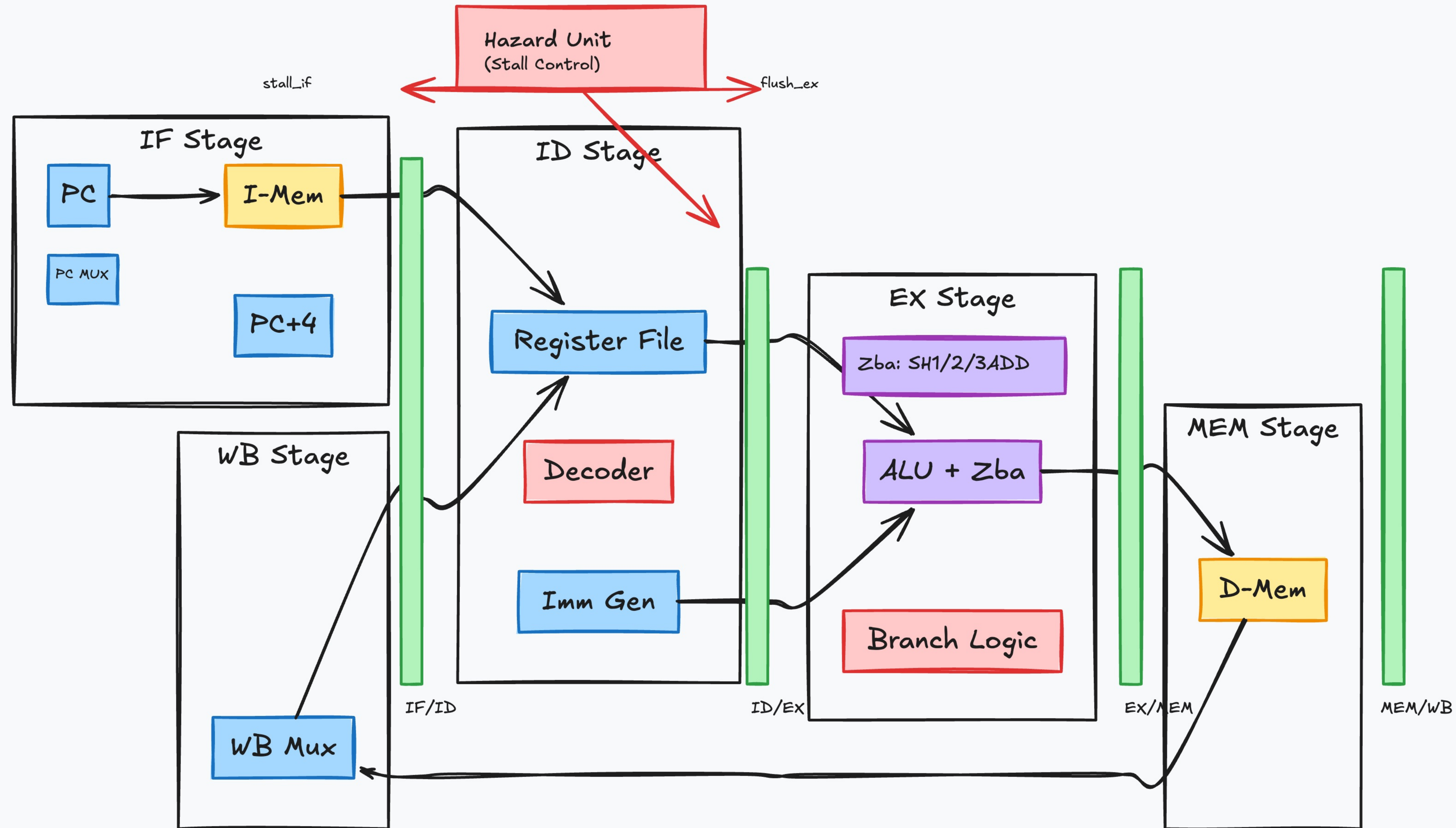


RV64I + Zba 5-Stage Pipeline Processor



LEGEND:

■ Datapath ■ Memory ■ Control ■ Zba Logic ■ Pipeline Reg

Control Signals: `alu_op`, `alu_src`, `mem_read`, `mem_write`, `reg_write`, `branch`, `jump`

Zba Operations: `SH1ADD`, `SH2ADD`, `SH3ADD`, `ADD.UW`, `SLLI.UW`, `SH1/2/3ADD.UW`