

Department of CSE

LAB REPORT

	arse Code and Name: 251[Electronic Circuits]					
Experiment no: 02						
Experiment name:	: Half-Wave Diode Rectifier Circuit					
Semester and Year: Fall-24	GROUP NO: 02					
Name of Student:	Course Instructor information:					
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2. (2022-3-60-188)						
3. (2023-1-60-204)						
4. (2023-2-60-305)						
Date of Report Submitted:	Pre-Lab Marks:					
18 November 2024	Post Lab Marks:					
	TOTAL Marks:					

Objective:

The objective of this experiment is to learn about the half-wave diode rectifier circuit and its characteristics and how it affects the output on an AC voltage. We also learn about the effects of a capacitor filter on the output of a rectifier circuit and how it reduces the peak to peak ripple voltage and the voltage disturbance of a rectifier circuit output, effectively regulating the DC output voltage and working as a power supply.

Circuit Diagram:

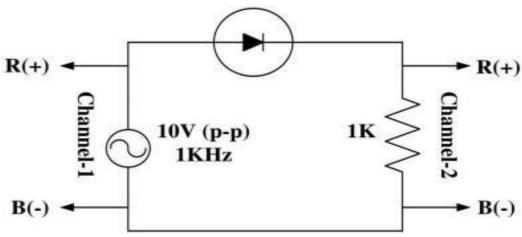


Figure 1. Set up for a half-wave diode rectifier circuit.

Theory And Experimental Methods

A rectifier is a circuit which coverts an AC voltage signal to a DC voltage signal. A half-wave rectifier is built using a single diode and can only rectify only the positive or negative half cycle of an AC voltage. And as a result, normally a rectifier circuit only outputs DC voltage for only half of the cycle and does not have any voltage for the other half. To fix this a capacitor is added to the rectifier circuit which acts as a filter and reduces the peak to peak ripple voltage. The higher the time constant, RC of the capacitor is, the lower the peak to peak ripple voltage becomes and the DC voltage becomes more stable.

Experimental Datasheet:

Measured value of resistance $\mathbf{R} = 0.973 \text{ k}\Omega$

 $\Delta VP = 960 \text{ mV} = 0.96 \text{ V}$

 $\Delta \mathbf{t} = 120 \mu s$

Peak to peak ripple voltage Vr = 360 mV = 0.36 V

Average value of output voltage Vo = 2.92 V

Labio2

Date: 12/11/2024

Group: 02

Group members:

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Naskullah Sijan -> 2023-1-60-204

Experiment name:

Half ware diode rectifier circuit.

Experimental Datasheet:

Measured value of AVp = 960 mV.

Measured value of At 2120 Ms.

Ripple voltage Vn 2 360 mV.

Avereage output vallage \$ 2.92V

Measured value of Resistance R= 0.973 KD

Post-Lab Report Questions:

From the experiment we get,

1. Measured value of peak to peak voltage, $\Delta VP = 0.96 \text{ V}$

Built-in Voltage =0.6 V

Difference = (0.96-0.6) V = 0.36 V

So, we have a different peak to peak value of built in voltage which is not desirable.

2. Measured conduction time, $\Delta t = 120 \mu s$

Calculated pre-lab conduction time, $\Delta t = 71.78 \mu s$

So, we can see that there was a large difference between the measured and calculated value of Δt in our experiment.

The difference between the measured value and calculated value, = (120-71.78) $\mu s = 48.22 \mu s$.

Our calculated and measured conduction time has a difference of 48.22µs.

3. We know, $\omega \Delta t = \sqrt{(2Vr/Vp)}$

Thus, Peak to peak ripple voltage, Vr= 0.273 V

Now, Pre-Lab value = 0.5 V

Measured Value = 0.36 V

Difference between calculated value and measured value = (0.36-0.273) = 0.087 VDifference between pre-lab value and measured value = (0.5 - 0.273) = 0.227 V So all of our values of peak to peak ripple voltage differs slightly, so either our measurement was incorrect or the experiment did not go properly.

4. Average output voltage, $V_{0avg} = Vp - Vr/2 = 0.78 V$

Measured average output voltage $V_{0avg} = 2.92 \text{ V}$

Difference: 2.92-0.78 = 2.14V

There is a difference between the measured and calculated values which should not normally happen.

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$$I_L = {}^{V0/R} = 3.23 \text{ mA}$$

 $I_{DAvg} = I_L(1 + \pi \sqrt{Vp/Vr}) = 2.40 \text{ mA}$

 $I_{DMax} = I_L(1+2\pi\sqrt{Vp/Vr}) = 3.28 \text{ mA}$

Measured value of Vr = 0.36 V Now,

Pre-lab values:

 $I_{DAvg} = 71.49 \text{ mA}$

 $I_{Dmax} = 138.22 \text{mA}$

Vr = 0.5V

Thus, difference of $I_{DAvg} = 69.36 \text{ mA}$

Difference of $I_{DMax} = 134.94 \text{ mA}$

Difference of Vr = 0.14V

6. Simulated half-wave rectifier circuit:

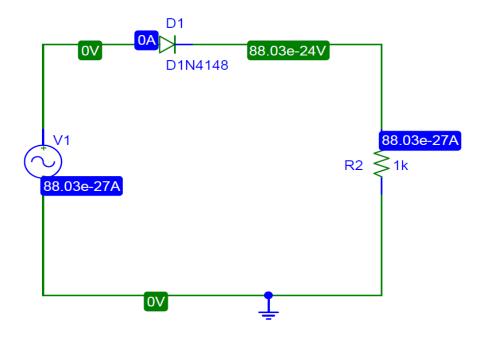


Figure: Half-wave rectifier circuit without capacitor.

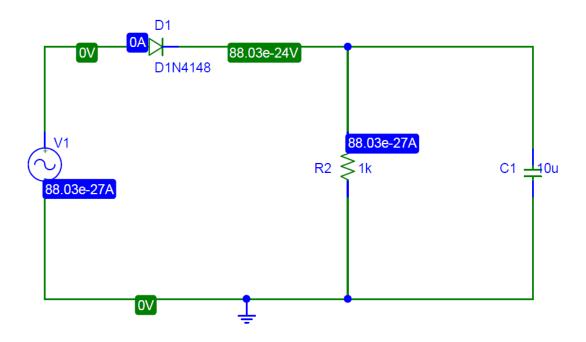


Figure: Half-wave rectifier circuit with capacitor.

> Simulated rectified sine wave without capacitor:

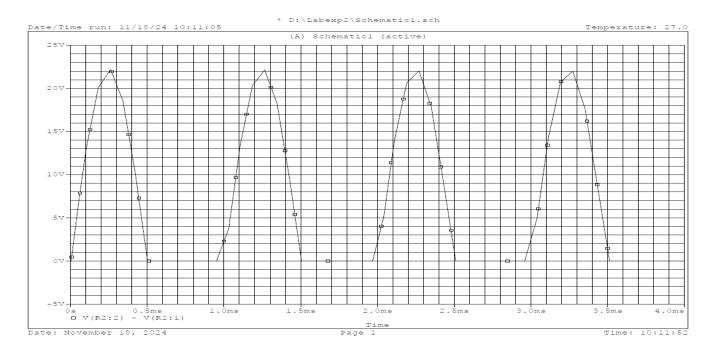


Figure: Simulated rectified sine wave with capacitor

> Simulated rectified sine wave when a capacitor is used:

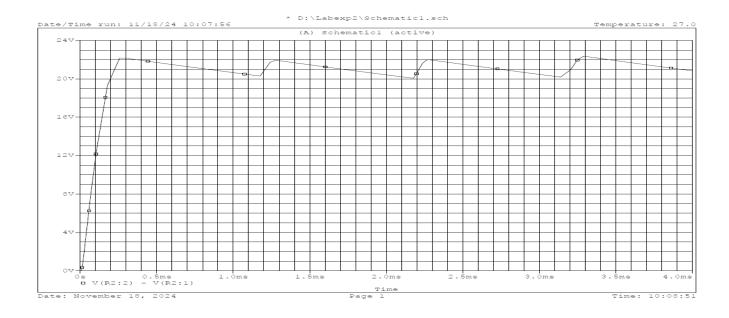


Figure: Simulated rectified sine wave

Conclusion:

From this experiment it can be concluded that diodes work as a one way rectifier and a half wave rectifier only conducts for half of an AC sinusoid voltage. Using a capacitor it can be turned into a stable DC voltage stream. In this experiment we learned to measure peak to peak voltage, ripple voltage, simulating a sinusoid using the oscilloscope. We learned the shape of the rectified voltage and how the rectified voltage becomes after adding a capacitor in the circuit too. Also from the comparisons between the experimental values and calculated values we see a lot of differences which indicates that the experiment did not go as expected and there were probably a lot of errors in setting up the oscilloscope and function generator properly. So we should be a lot more careful so that the experimental value matches with our calculated values for proper

Pre-Lab Report:

