# CMSI 387-01

## OPERATING SYSTEMS

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# **Assignment 0422 Feedback**

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### **Dining Philosophers**

- 2d The philosophers thank you for feeding them :) (+)
- 4a You've gotten the core solution right—but you need to make it clear to the user that this is the case. Thus, suggestions given inline are primarily refinements to output format so that the user can perceive what's happening more easily, particularly with respect to errors. But speaking of errors, you don't include any "sanity check" code or asserts that enforces proper behavior. More inline comments give hints on how/where to do this. (|)
- 4b Code is decently structured with logically separated/sequenced sections. (+)
- 4c I was able to read your code pretty easily, except for...TAAAAABBBBBSSSS! (/)
- 4d Your work demonstrates good use of the information available to you. (+)
- 4e Commit frequency and messages are appropriate for the work done. (+)
- 4f Submitted on time. (+)

#### **Paged Memory Address Translation**

- 2d You have successfully implemented paged memory address translation from the ground up. (+)
- 4a No issues with your code outside of that unnecessary bitwise-&. Otherwise works as spec'ed. (+)
- 4b One little separation of concerns tweak would be unhardcoding 256. But overall n.b.d. (+)
- 4c Almost clean code, no big issues, except for...TAAAAAABBBBSSS! (/)
- 4d You successfully utilized available information to implement paged memory address translation. (+)
- 4e Commit frequency and messages are appropriate for the work done. (+)
- 4f Submitted on time. (+)