

## Higher design



one stage lower abstraction

**Operand Two's complement according to precision and opcode and Msb**



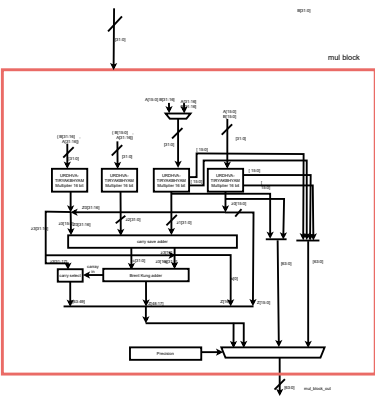
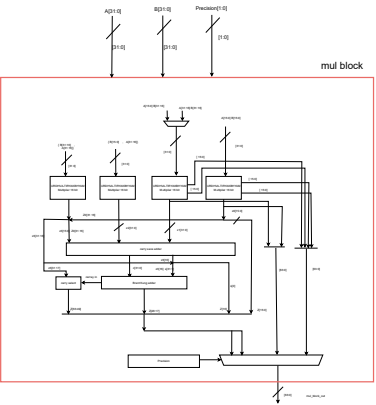
this block responsible for two's complement for output and give's output according to opcode



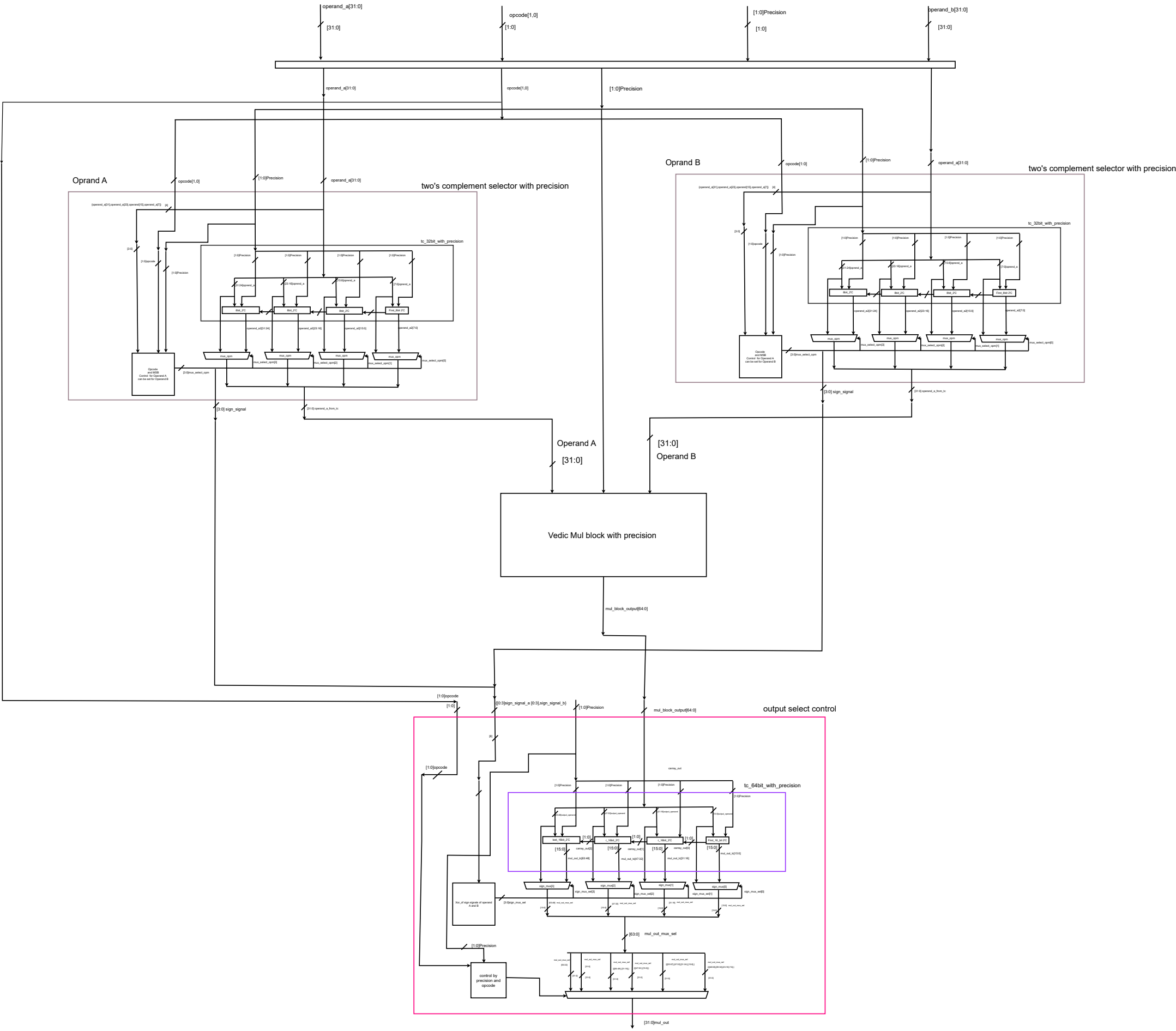
remaining bit's concatenate in this 8 bit hardware can be extended to 16 bit

last bit's concatenate in this 8 bit hardware can be extended to 16 bit

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|  |  |
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Whole system diagram



Whole system diagram

top module

