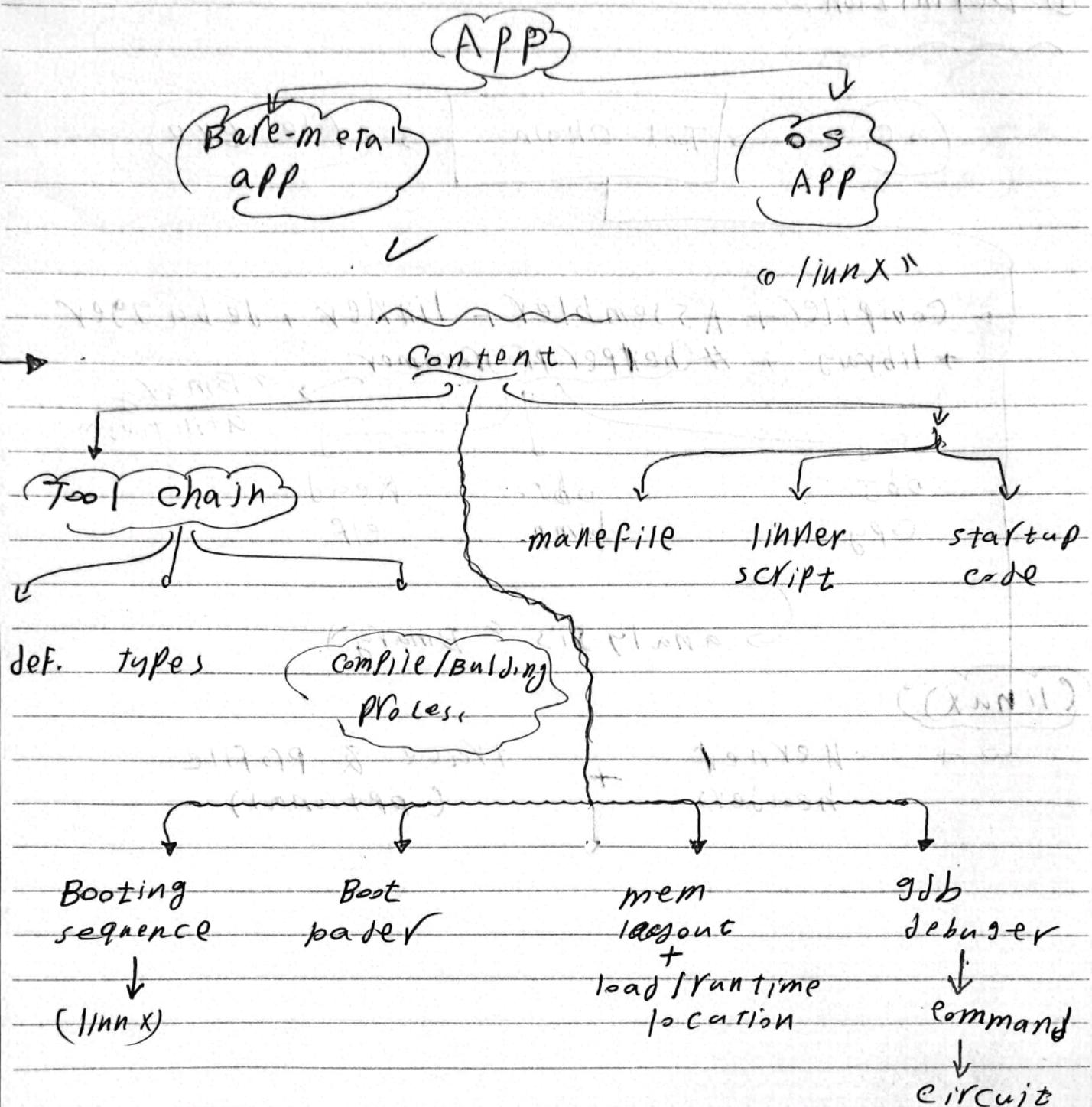
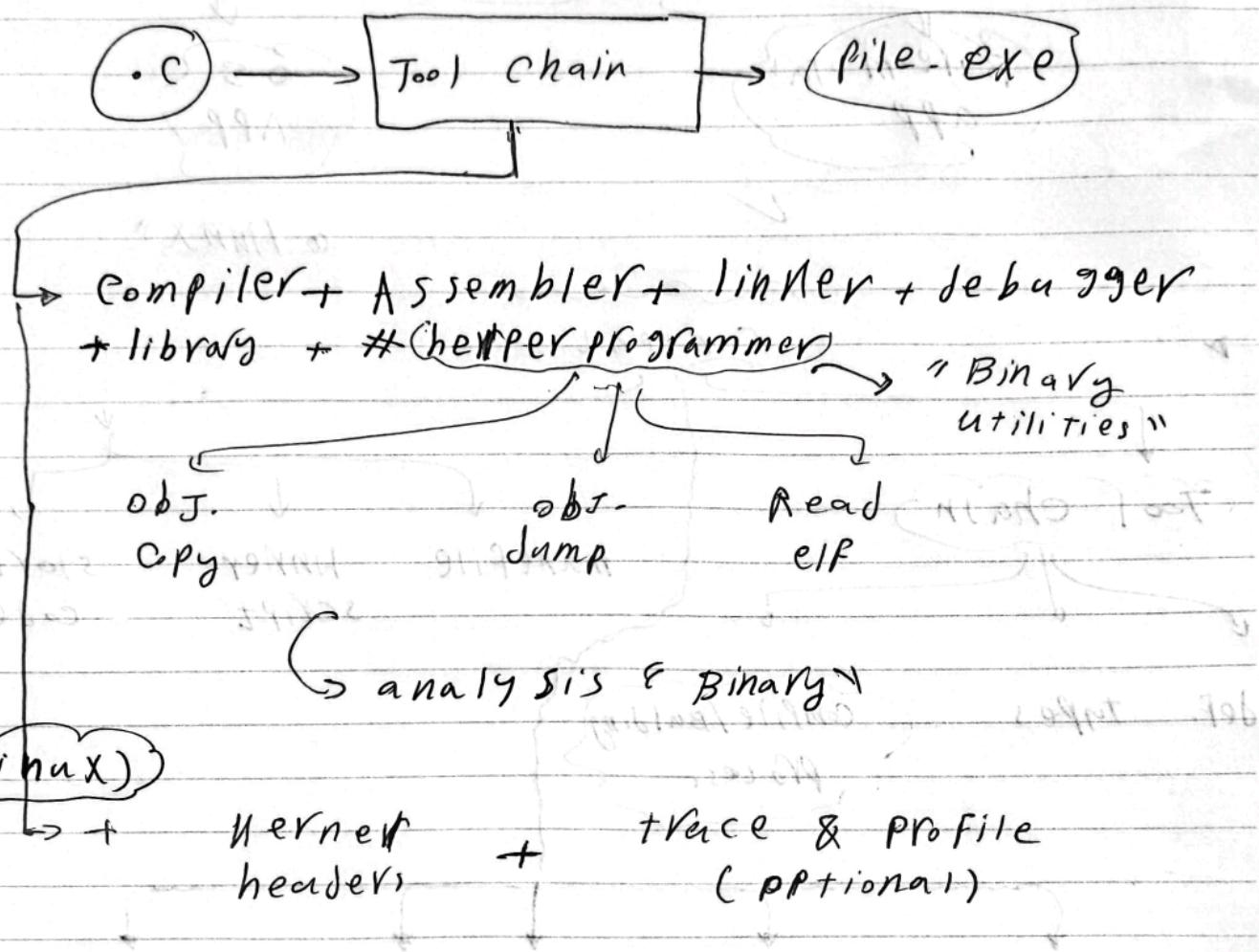


Embedded C



"Tool chain"

① Definition:-



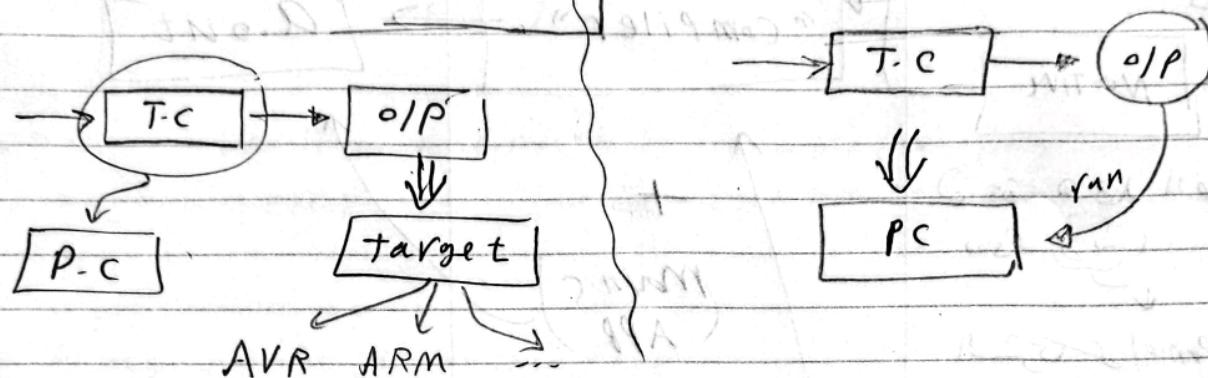
⑥ TYPES:-

cross

Native

(compilation) الأكود
الذى يكتب
على جهاز
(on other microcontroller)

الاكود الذى
يكتب على نفس
جهاز (run) و
يتطلب
(native)



libraries

static

C-C) ~~xx~~

Dynamic

C-C) ~~vv~~

* static lib)

(.o) (.o)

obj. files
C-O)

Linker

(Protect)

(-h)
(func)

(-c)
-h

→ .o → -o

(a)
(.h)

main.c
-a

main.exe

> gcc -c CAN.c -o CAN.o

(object file)

> ar rcs lib-can.a CAN.o

(static library)

> gcc main.c lib-can.a -o main.exe

(run command)



* CMD → gcc

① gcc main.c → a.exe

② gcc -c main.c → main.o

③ gcc main.c -I /usr/share/ []
 (header file) ↗ include best files

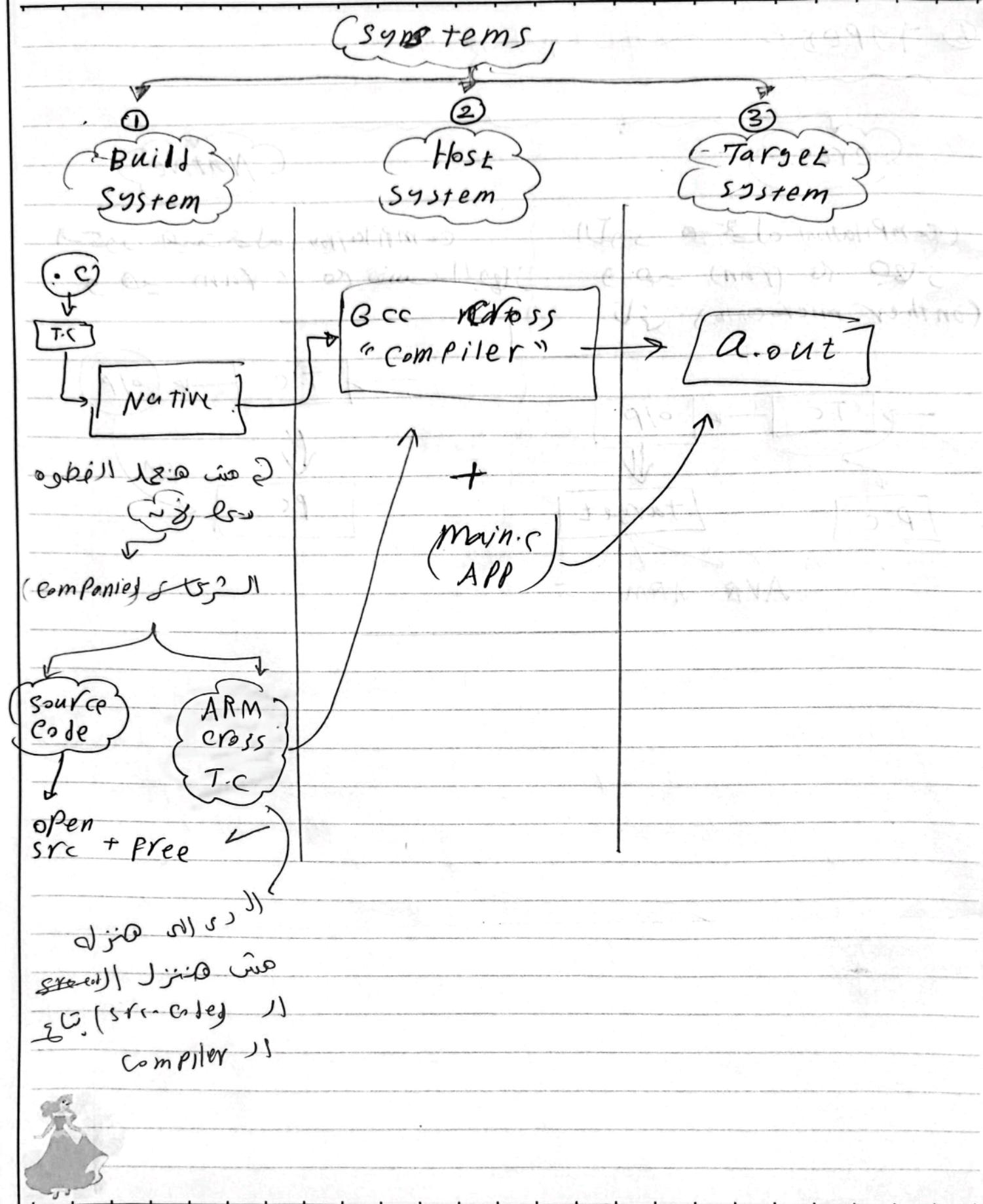
④ gcc -Wall main.c ⇒ Enable warning
 ⇒ -werror ⇒ Enable error

⑤ Pass options using file

↳ gcc main.c @options file

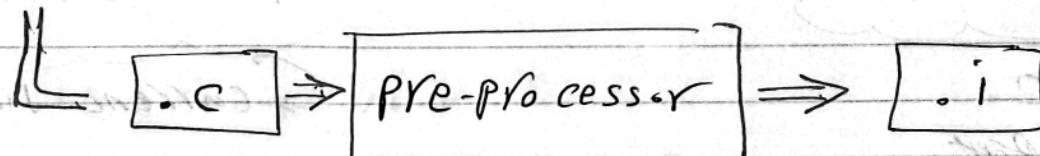
⇒ gcc --help





* Processor Directives

- 1) → #include
- 2) → #define
- 3) → conditional directive → #if #elif #else
#ifdef #ifndef
...
- 4) → #error
#warning
- 5) → string → #
concatenate → ##



→ TEXT replacement = ##

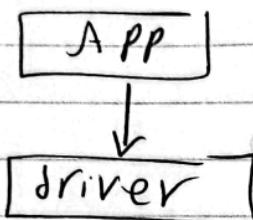
slice(s)

#pragma ⇒ Compiler directive

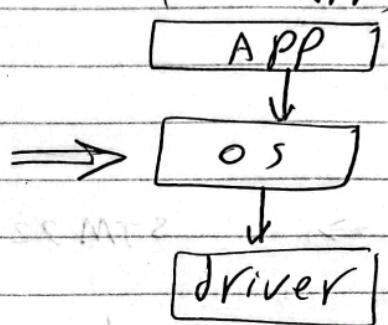


ARM Tool Chain

arm-none-eabi-gcc
(Baremetal APP)



arm-linux-gnueabi
(OS APP)



Hint

→ ARM Cross Tool Chain

GNU GCC

↳ free / open source

commonly used

arm cc

↳ Niel (n license)

→ IDE → STM32 Cube (✓)

→ without IDE → download (✓)

2] #define (macro)

a) object like macro

#define X 10
#define size 256

b) function like macro

notes

#define X 10

① int y = X; → int y = 10;

int X = 50; → int 50 = 50;

int XY = 10; → No replacement

printf("X = "); → // //

int X = 8; → // 0
C₆ CAPITA



P:

file.c

new1

Lib.h

new2

temp.h, main.c

@ → file.c → temp.h

⇒ *include "new1/new2 /temp.h"

@ main.c → lib.h

#include "... /lib.h"

To back



1] #include

↳ To include header file (.h)

*include <file.h>

(Preprocessor) ↳ current
standard library داده های از
کتابخانه استاندارد

↳ Built-in Files

*include "file.h"

current directory دایرکتوري

↳ قدرتمندی کلیه داده های
(std-lib)

*include "Path"

"Path"

Absolute Path

↳ PC

Relative Path

↳ current directory



TYPE def

By compiler

→ limited for
give symbolic names

types

macro (#define)

pre-processor

used for values
as well & types

typedef struct std^{*} PTR; #define PTR struct std^{*}

PTR^{*} m, n;

"Two pointers"

PTR x, y;

struct std^(x, y)
Pointer
Variable of
struct std type



* Comparison :-

* Define

enum

text replacement

② pre-processor

text replacement

② compiler

We can use → float
 ↳ sentences
 ↳ ...

must be const int
(NOT float)
error ↴

No memory space (↑)
(because) → size = int size

↳ local info
inside switch (✓)

any value
 X
 ↳ (x0, x1)
 + ↳ (x0, x1)

same value ↴
different names ↴

any value

$$[-2^{n-1} : 2^{n-1} - 1]$$



* ~~#define Z 10~~) → ~~order doesn't matter~~
* ~~#define X Z~~) → ~~order doesn't matter~~

int y = X; → int y = 10

Notes

- #define X 10 // warning
• #define X 20 // خطأ في زرقة

→ remove the define of the macro



(...) → ANY thing else

↙ (Var) الگوی خروجی (Value) ↘

(--VA_ARGS--) ()

*define narem (...) printf(--VA_ARGS--)



void main()

{

narem ("Hellooo\n");

(↑) 39512969 C

Ex: 3.

*define fun (a,...) printf(--VA_ARGS--, a)

fun(x, %d "));

↳ printf("x-%d", x);



Note \Rightarrow " Concatenation operator \Rightarrow \" Back slash

*define - (-,-) - ~~209A~~

(209A) \rightarrow 209A (mission 209A)

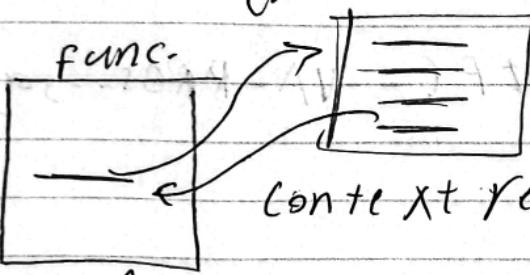
Adv. جئیل

dis-Adv. عیوب

D fast in exec.

D code size (4)

context sw



2) No type change

3) difficult to debug

Classical (old) way

new (code size)

new (time)



(2) function like macros:

→ "parameterized macro"

*define Add(x,y) $x+y$
 ~~not bestim~~
 ~~small mistakes~~
 ~~no space~~

int z = Add(X,Y) ; $\Rightarrow z = X+Y$

hint: *define Add_(x,y) $x+y$

int z = Add_(7,4)

int z = Call Add_(7,4)



3) Conditional directives

① ~~*if~~ cond-1

~~*elif~~ cond-2

~~====~~

~~*else~~

~~====~~

~~*endif~~

notes

D Comment:-

* We can't make a comment using it :-

~~*if~~ 0

~~====~~

~~*endif~~

2) Configuration

→ drivers

if

Compile

Check Val

#if

pre-processor

check macros Not values

ex: ~~#define~~ ~~X~~ 10

~~*if~~ X > 70



3] Conditional directives

① ~~*if cond-1~~

~~*elif cond-2~~

~~====~~

~~*else~~

~~====~~

~~*endif~~

notes

D Comment:-

* We can't make a comment using it :-

~~*if 0~~

~~====~~

~~*endif~~

2) Configuration

drivers

ip

*IP

Compile

pre-processor

Check Val

check macros not values

ex: #define ~~xx~~ 10

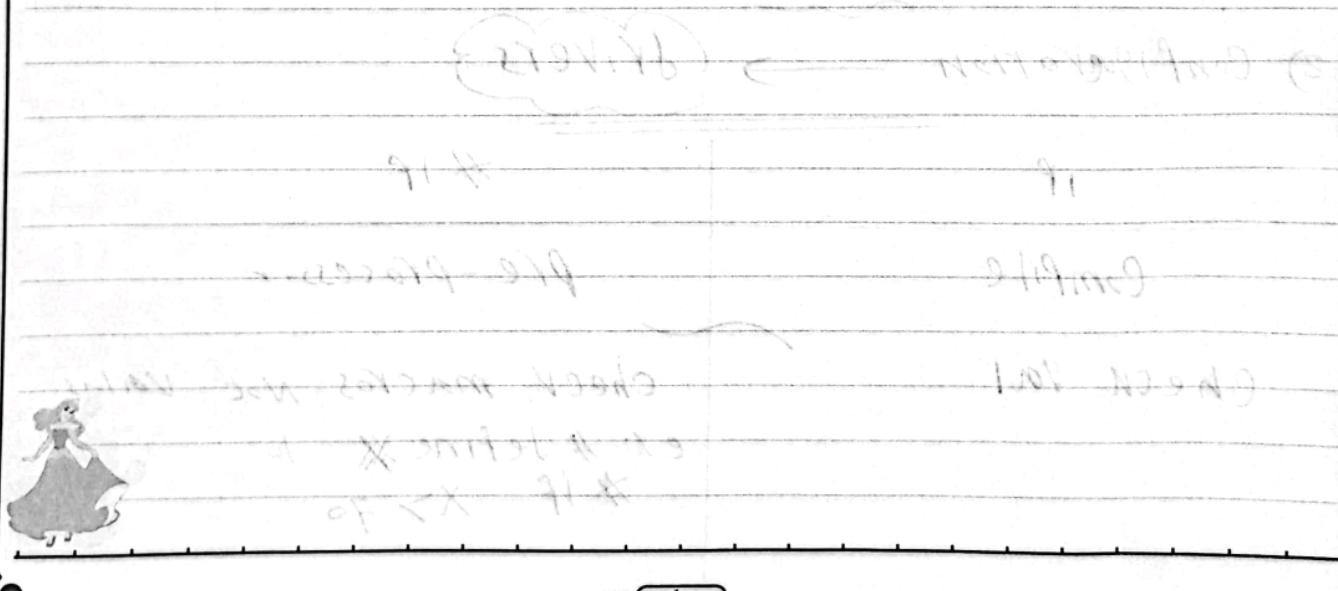
*if $x > 70$



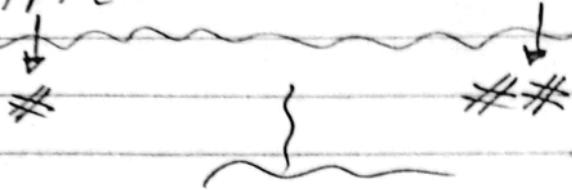
⇒ Pre-defined macros



⇒ Built-in ⇒ -- DATE --
-- FUNC --
-- FILE --
-- LINE --



5) stringification & concatenation?



① string :-

#define printf(X) printf(#X)

printf("Ali") → "Ali"

② Concatenation

#define conc(X,Y) X##Y

int X = conc(3,8);

int X = 385

Port driver



④ ~~# error~~ { → stop compilation + error message
* warning { → warning message

↳ used with \rightarrow if
↳ Not (if)

* if <—> { if <—>

#error `(` " ") } #error `("` - "

✓

if c →
error re-

(Parse) the text



② ~~#ifdef~~ {
~~#ifndef~~ {
 ↳ used as header file guard)

~~#ifndef~~ Filemacro

~~#define~~

~~#endif~~



~~#pragma~~

↳ Compiler directive

① ~~#pragma optimize ("O0", off)~~

↳ "No optimization"

② ~~#pragma once~~ ⇒ replace file guard

الزفاف في الملفات (file guards) هو مدخلة (Compiler dependent) لـ `#ifndef` و `#define`

③ ~~#pragma startup []~~

~~#pragma exit []~~

startup

exit

$64 > 65$

$64 < 65$

~~#pragma start~~

④ Add memory - section



Usage of Preprocessor Directives

① Configuration :-

```
#define size 256
char arr[size];
```

② Readability → (Professional code)

③ Portability

↳ `typedef unsigned char u8;`
 ↳ `#define u8 unsigned char`

hine

`#line` → قيم ارجاع

debugging ١٤ خط في نافذة



hint (common mistake)

*include <stdio.h>
*define max(a,b) $a > b ? a : b$

int main()

```
{  
    int x=8, y=7, z;  
    z = max(x,y)*2;  
    printf("z=%d\n", z);  
}
```

exp. value $\Rightarrow z = 18$

but

actually $\Rightarrow z = 9$

to avoid \Rightarrow use ()

$\Rightarrow z = 18$
 (max) \Rightarrow $z = 18$
 $z \neq max$

$\Rightarrow *define max(a,b) ((a)>(b)? (a):(b))$

($\Rightarrow z = 18$)



② Middle stage:

1) syntactic Analysis:-

↳ check → logical struct (datatype)
→ IR

فیونت تھیزیر (carving) لو گرفت و مکاری (int) کے لئے char_ptr بھی معرفت کریں۔

2) optimization :-

Why? →

- code-size (\downarrow)
- exe. time (\downarrow)
- memory size (\downarrow)

↳ How? \Rightarrow multi-level process

① Remove dead code ((return) & vars)

2) inline expansion of func. (مسروقات)
ولتكن احدى الاكواد هي

3) Register Alloc (GPR)

(General Purpose Reg.)

4) Loop unrolling \rightarrow For loop \downarrow Jgo = 0

لهذه الطور (stomach) ، ولكن في المكور غيري



* pre processor

↳ Text Replacement (#)

↳ Concatenation operator (^)

* Compiler

Frontend stage

Middle stage

Backend Stage

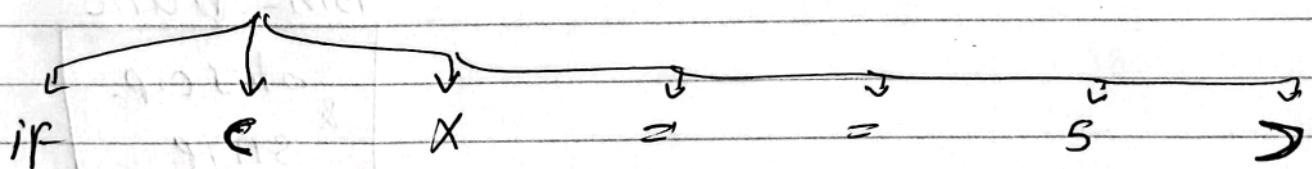
① Frontend stage :-

↳ source code Parsing

o IP \rightarrow Parse Tree $\xrightarrow[\text{Table}]{\text{Symbolic}}$ IR program

Ex:

if (x == 5)



↳ check syntax error \rightarrow Tokenization

Keywords

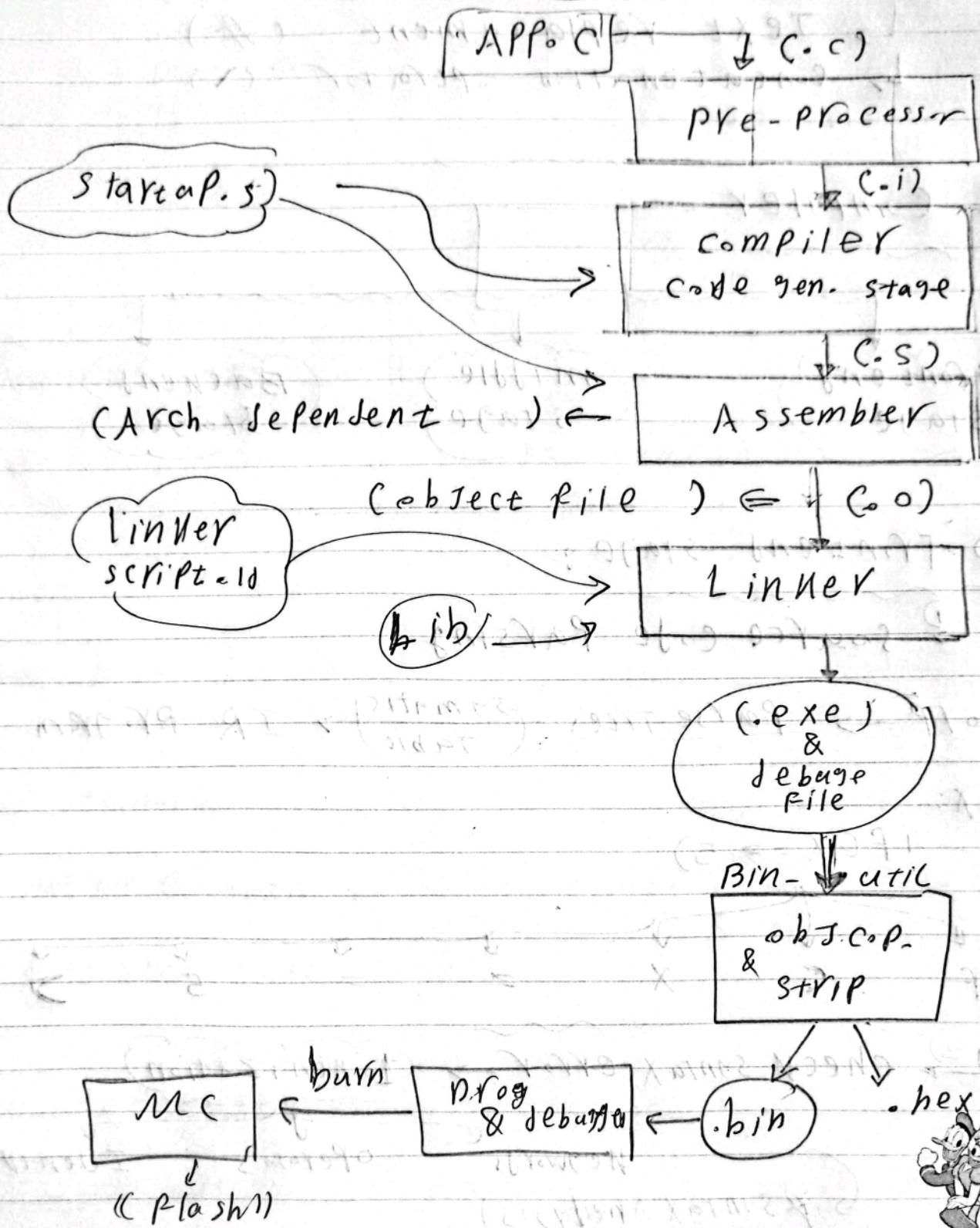
Operators

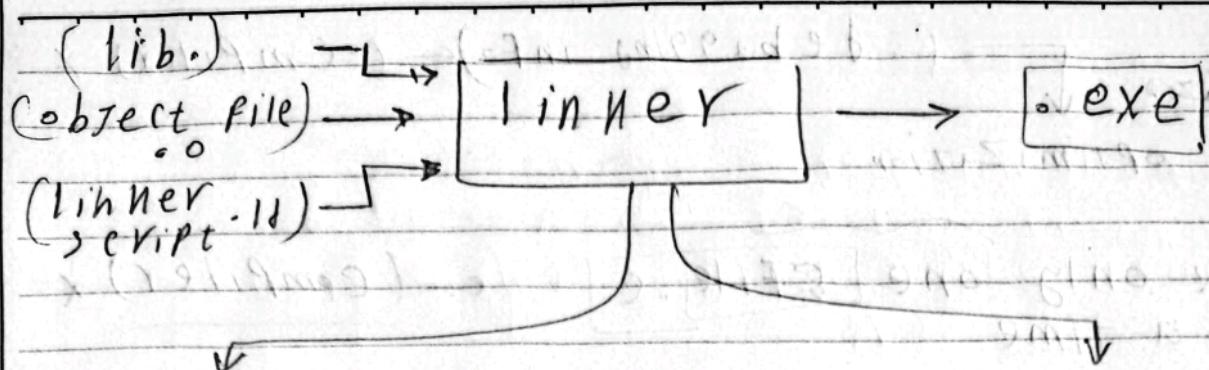
Identifiers

↳ Syntax Analysis



Compilation Process





Symbol
resolving

section
location

Symbol

(.o) → realloe
object files

// Physical
Addresses

File-map

* Compilation flags: (gcc --help)

- E → preprocessing only ⇒ (.i)
- S → compilation only ⇒ (.S)
- C → compilation + ASS ⇒ (.o)



لماذا (debugging info) ← (Compiler) *

optimization

parse only one .c-file ← (compiler) *

at a time

Import

① global & static
↳ extern

② func. (calling)

↙
(المدخلات من الملفات)
ـ تابعـ

Export

① global &
static variable

② func. (implementation)

③ debugging info
ـ معلومات عن الخطأ
ـ (file) ↴

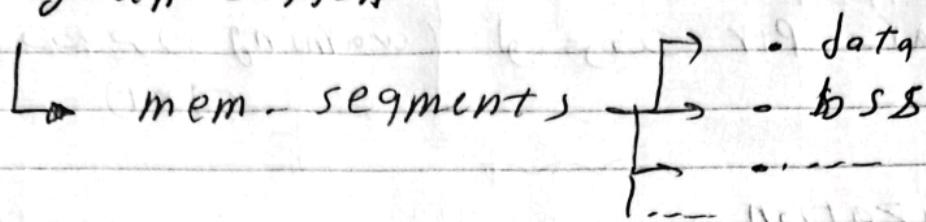


③ Backend stage

① code generation

→ convert (code.c) → Assembly

② memory allocation



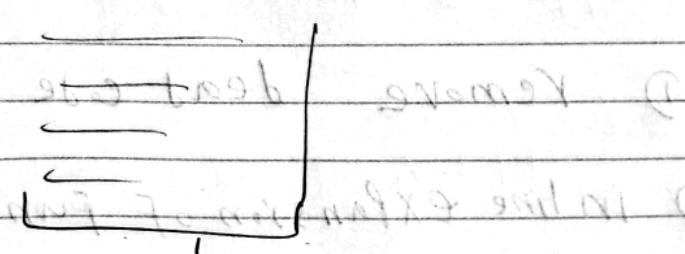
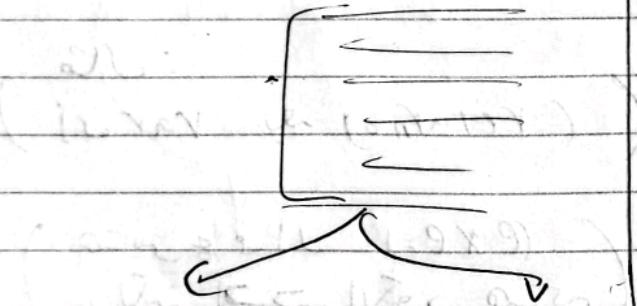
(classic compiler) → walloc

(unit)

(symbol table) ← (compiler)

symbols

Addresses



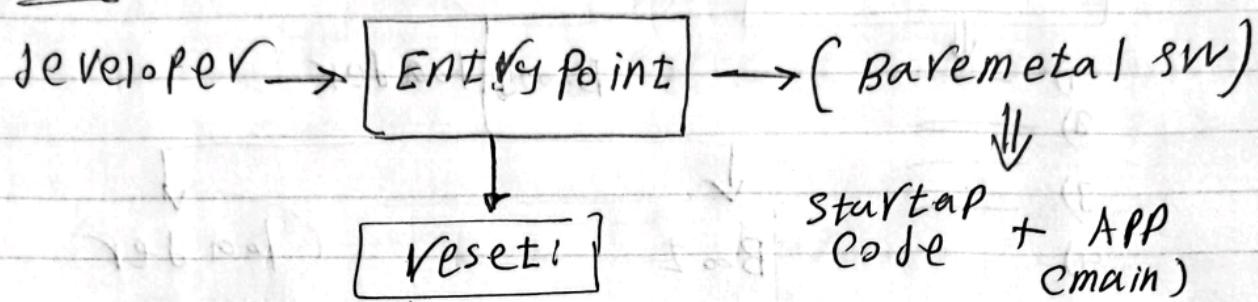
→ (Reallocate)



(TWO cases)

Case ①

Case ②

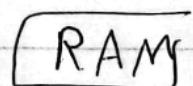
Case ① :

→ 1) Initialize stack (sp)

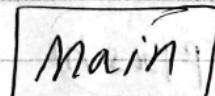
→ 2) Copy C-data

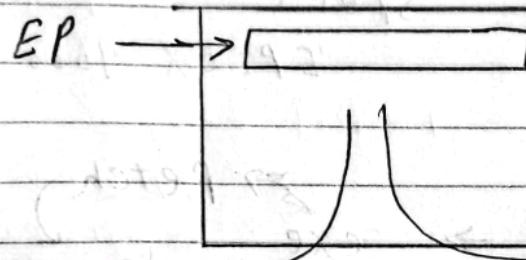
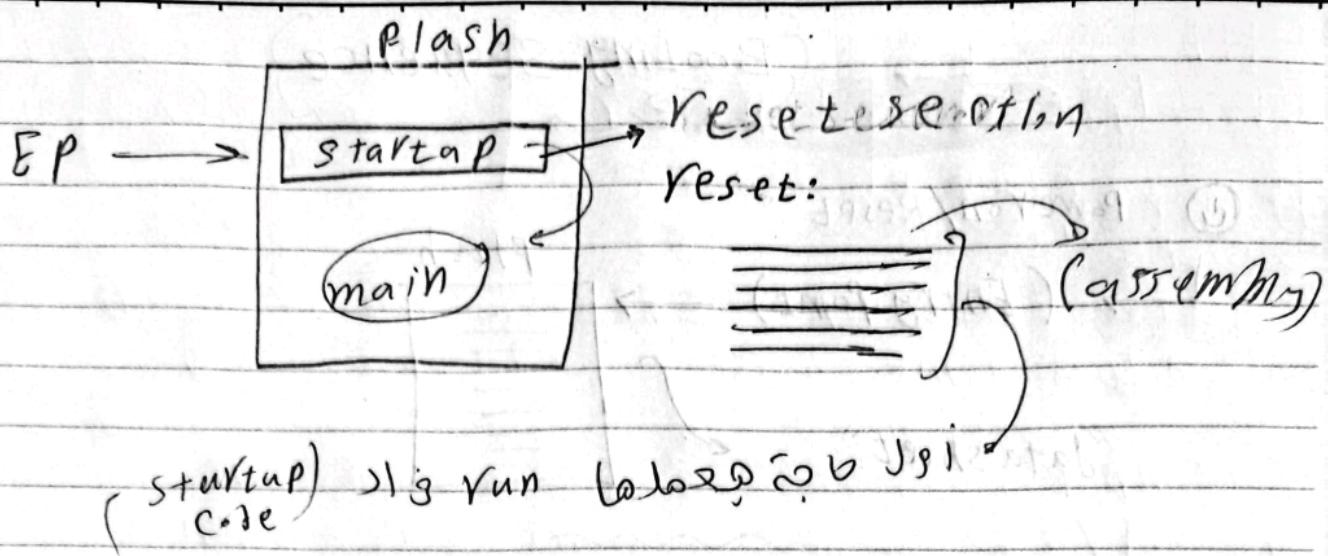


→ 3) Reserve C.bss



→ 4) branch / jump → Main





①
Boot loader

(OS) بـ (SW)

[Startup code + main]

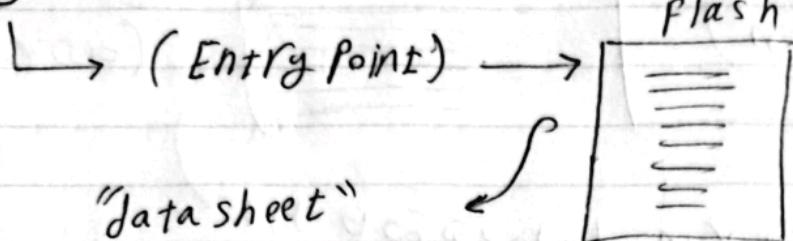
②
your Baremetal SW

[Startup code + APP]



Boot Sequence

① PowerOn/Reset



ex:

flash:

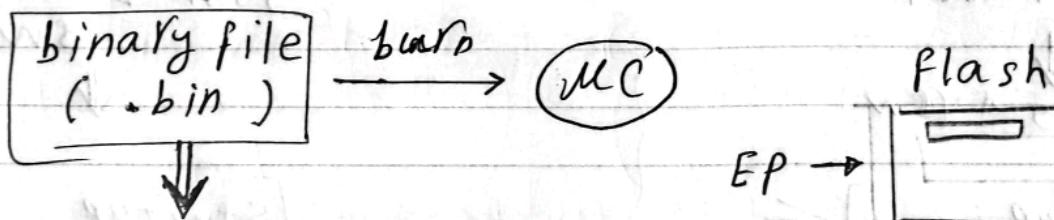
0x 0000 0000

0x FFFF FFFF

specs:

EP: 0x 1000 0000

* Instruction life cycle \Rightarrow
 fetch \rightarrow exe. \leftarrow decode



① (start up code) \rightarrow (.s) \Rightarrow "if stack Not initialized yet"

\rightarrow (.c) \Rightarrow "if stack initializes" \rightarrow (ARM cortex)

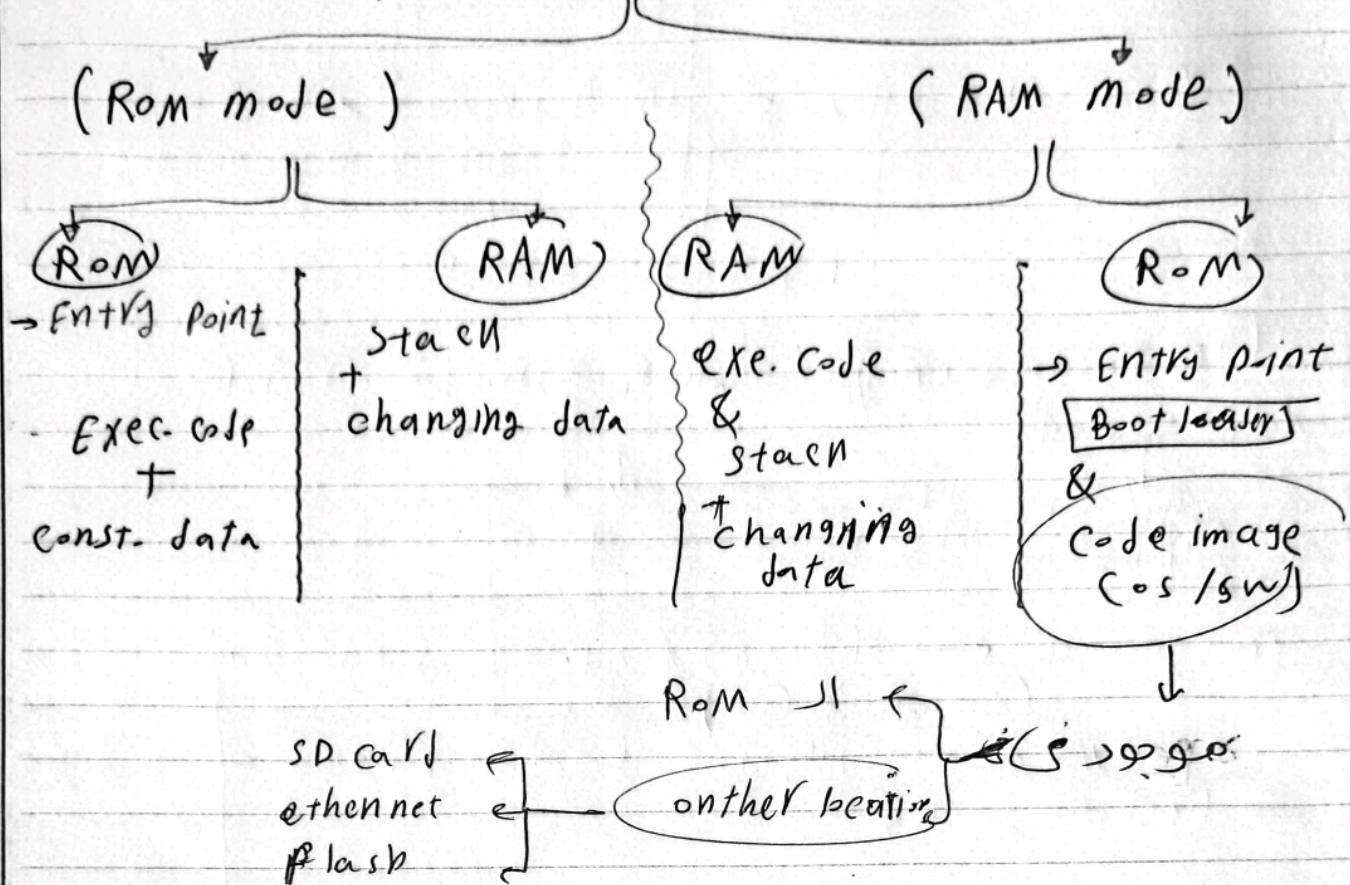
② code \rightarrow c.text

③ global & static (init \neq 0) \rightarrow .data

④ global & static (uninitialized) \rightarrow .bss



Two Running modes



(compare)

ROM mode

- very simple
- reg. smaller memory
- Fixed code Address
- Relative small code

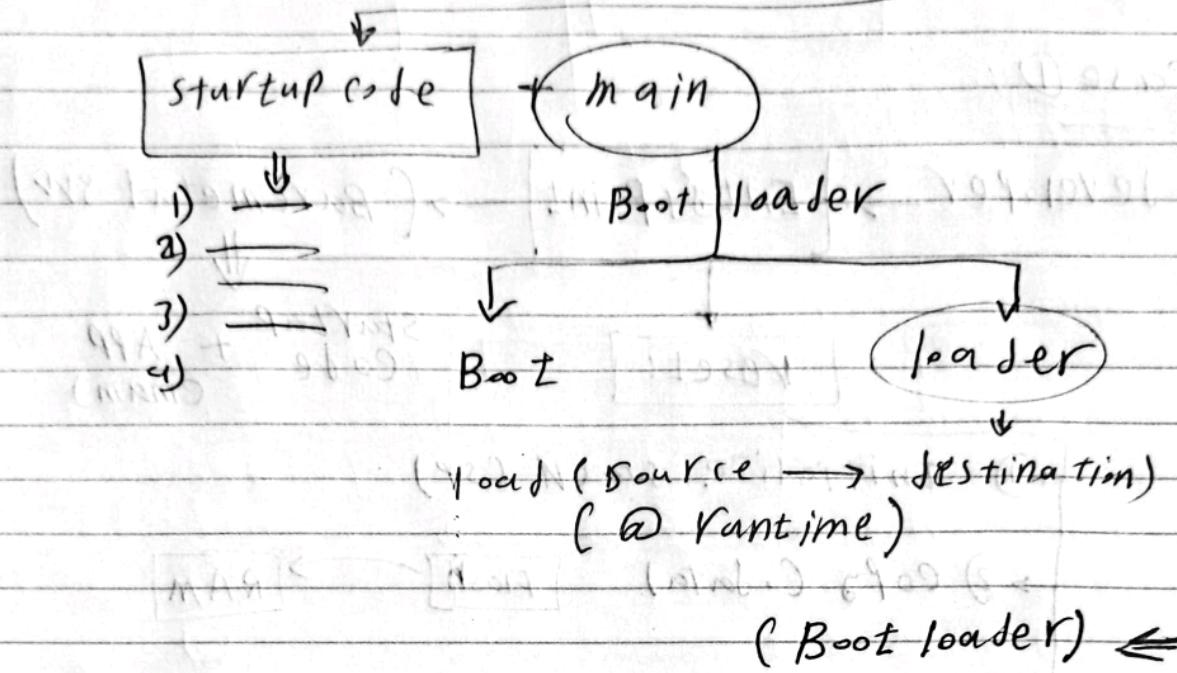
RAM Mode

- complex
- large code
- Relocatable mode
- fast



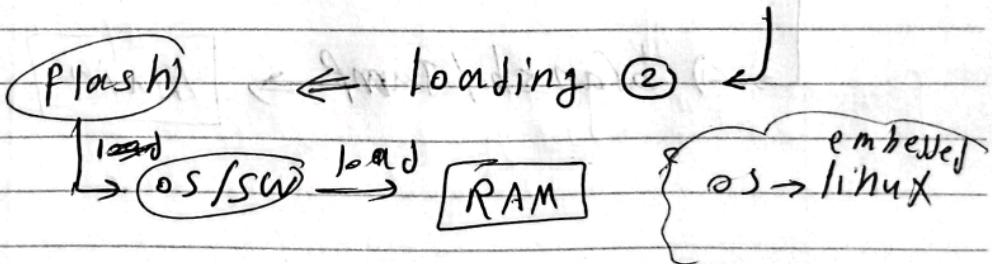
Case ②

Developer \Rightarrow Entry Point \rightarrow (Boot loader)



Prepherals
(modules) \hookrightarrow SPI

\Leftarrow Initializer ① \Leftarrow



(Startup code) \rightarrow Jump ③

\downarrow
main



hint \Rightarrow Reusable memory space