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| **Digital Logic Design** | | |
| Faisal Iradat, PhD | Quiz – 8 | Marks: 5 |

1. Design a synchronous counter that goes through the sequence

2 6 1 7 5 and repeat

using

* 1. D flip flops
  2. JK flip flops
  3. SR flip flops
  4. Show a state diagram, indicating what happens if it initially is in one of the unused states (0, 3, 4) for each of the designs.