



Software Selectable Pins

Internal Bootstrapping Resistors

Bootstrapping Pins Settings

Voltage of Internal LDO(VDD_S0I0)			Bootstrapping Pin Settings			
Pin	Default					
GPIO12/MTD1	Full–Down	0	Boot Mode		1	
			SPI Flash Boot		Download Boot	
GPIO0	Full–Up	1			0	
GPIO2	Full–Down	Don't-care			0	
Debugging Log on UOTXD During Booting						
Pin	Default		UOTXD Toggling		UOTXD Silent	
GPIO15/MTD0	Full–Up	1				
Timing of S0I0 Slave						
Pin	Default		Falling-edgeInput	Rising-edgeInput	Falling-edgeInput	Rising-edgeInput
GPIO15/MTD0	Full–Up	0		0	1	1

