

BLG 242E Logic Circuits Laboratory 2023 Spring – Syllabus

Week	Date	Activity	Homework
1	24.02.2023	No course	
2	3.03.2023	Week 1 (Introduction to Verilog – Video Lecture)	
3	10.03.2023	Experiment 1 (Group 1-15)	HW1
4	17.03.2023	Experiment 1 (Group 16-30)	
5	24.03.2023	Experiment 2 (Group 16-30)	HW2
6	31.03.2023	Experiment 2 (Group 1-15)	
7	7.04.2023	Experiment 3 (Group 1-15)	HW3
8	14.04.2023	Experiment 3 (Group 16-30)	
9	21.04.2023	No course	
10	28.04.2023	Experiment 4 (Group 16-30)	
11	5.05.2023	Experiment 4 (Group 1-15)	
12	12.05.2023	Make-Up	
13	19.05.2023	No course	
14	26.05.2023	Midterm Exam	

Grading:

- 4 Experiments (30%)
- 7 Homework (%10 (4 reports.) Experiment Reports + %30 (3 HWs) Verilog Homeworks)
- 3 Quizzes (10%)
- 1 Midterm Exam (20%)

VF Conditions:

- Attendance: Minimum 3 participation to 4 Experiments
- Minimum grade for an experiment: 30/100
- Make-up: Only one experiment and its quiz
- Quizzes:
 - There will be no quiz for the first experiment.
 - If you can't get 5 points out of 10 points from the quiz of the experiment, you are not allowed to attend the experiment.
 - Average of Experiments + Quizzes + Homeworks must be 30 points out of 80 points.