Course Name:	Computer Architecture	Course Code:	EE204
Program:	BS(Computer Science)	Semester:	Fall 2019
Duration:	30 Minutes	Total Marks:	20
Paper Date:	16-09-2019	Weight	~3
Exam Type:	Quiz 1d	Page(s):	2

Student : Name:_____ Roll No._____ Section:_____

Question 1 a [8]

Using four bit number, multiply 11 x 6 using original multiplier circuit (without optimization) discussed in the class. Value of each register is initialized in the table below. List different steps that will be performed in each iteration and the resulting value of each register.

Iteration	Step	Multiplier	Multiplicand	Product
0	Initial values	0110	00001011	00000000
1				
2				
3				
4				

Question 1 b [4]

Bits have no inherent meaning. Given the bit pattern
0000 0000 0011 0110 0010 1000 0000 0010

a. **Assuming it as a MIPS instruction identify source and destination register numbers.**

b. **Assuming it as a signed binary value, what is the corresponding integer value?**

Question 2 [8]

Input	Present State		Next state	
x	**A_i**	**B_i**	**A_{i+1}**	**B_{i+1}**
0	0	0	0	1
0	0	1	1	0
0	1	0	0	1
0	1	1	1	0
1	0	0	0	0
1	0	1	1	1
1	1	0	0	0
1	1	1	0	0

The state table shows the transitions of a sequential circuit with two memory elements.
a) Write down the Boolean expressions describing the next state.
b) Draw the circuit using the expressions.