## **National University of Computer and Emerging Sciences, Lahore Campus**

SO SO STANDARD OF THE SOUND THE SOUN	Course Name:	Computer Architecture	Course Code:	EE204
	Program:	BS(Computer Science)	Semester:	Fall 2019
	Duration:	30 Minutes	Total Marks:	20
	Paper Date:	16-09-2019	Weight	~3
	Exam Type:	Quiz 1c	Page(s):	2

Student: Name:	Roll No
Section:	

## Question 1 a [8]

Using four bit number, multiply  $6 \times 5$  using original multiplier circuit (without optimization) discussed in the class. Value of each register is initialized in the table below. List different steps that will be performed in each iteration and the resulting value of each register.

Iteration	Step	Multiplier	Multiplicand	Product
0	Initial values	0101	00000110	00000000
1				
2				
3				
4				

## Question 1 b [4]

- a. Assuming it as a MIPS instruction identify source and destination register numbers.
- b. Assuming it as a signed binary value, what is the corresponding integer value?

## Question 2 [8]

Input	Present State		Next state	
X	$\mathbf{A_{i}}$	$\mathbf{B}_{\mathrm{i}}$	$\mathbf{A}_{i+1}$	$\mathbf{B}_{i+1}$
0	0	0	1	0
0	0	1	0	1
0	1	0	1	0
0	1	1	0	1
1	0	0	0	0
1	0	1	1	1
1	1	0	0	0
1	1	1	0	0

The state table shows the transitions of a sequential circuit with two memory elements.

- a) Write down the Boolean expressions describing the next state.
- b) Draw the circuit using the expressions.