

Computer Architecture (CS-D) **Quiz4**

Date: 13-12-2018

Roll No: _____

Question1:

Below is a list of 32-bit memory address references, given as word addresses.

21, 166, 201, 143, 61, 166, 62, 133, 111, 143, 144, 61

For each of these references, identify the binary address, the tag, and the index given a direct-mapped cache with 16 one-word blocks. Also list if each reference is a hit or a miss, assuming the cache is initially empty. Answer your questions in the form of table(s) only.