National University of Computer and Emerging Sciences, Lahore Campus

STORING SHIPS STORY OF STORY O	Course Name:	Advance Computer Architecture	Course Code:	EE502
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Question 1 [12]

Consider the following MIPS assembly language loop. Assume that we run this code on the five stages pipelined data path.

- 1. Loop: ld R1, 100 (R4)
- 2. subi R1, R1, 4
- 3. ld R2, 200 (R4)
- 4. subi R2, R2, 4
- 5. sub R5, R1, R2
- 6. sw R5, 300 (R4)
- 7. subi R4, R4, 4
- 8. beq R4, R3, Loop
 - a) Find all possible hazards in the above code. Fill the following table by writing lines and register in front of particular hazard. Equality for branch is done in decode stage instead of execution stage.

With Forwarding		nrding	Add stalls in the above code to remove all hazards	Reschedule the code to remove as many stalls as possible	
Hazard	Lines	Register			
WAR					
WAW					
RAW					

Question 2 [8]

Examine the accuracy of branch predictors for the following repeating patterns (e.g., in a loop) of branch outcomes. Accuracy is defined as the percentage of guesses that are correct.

- (a) T, NT, T, NT, NT
- 1. What is the accuracy of always taken and always not taken static branch predictors?
- 2. What is the accuracy of a dynamic branch predictor? Assume this predictor starts in the "Predict taken" state.