NATIONAL UNIVERSITY
of Computer & Emerging Sciences, Lahore
Department of Computer Science

EE204 – Computer Architecture -- Quiz 1 (4 marks)

Name: _____   **Registration #: _____**

Question # 1: MCQs *(1 mark)*

- According to IEEE-754 Floating point standard, size of Exponent in double precision is
    a) 8 bits
    b) 32 bits
    c) 11 bits
    d) 20 bits
- How many outputs a quadruple 2 * 1 mux has?
    a) 2
    b) 3
    c) 4
    d) 5

Question # 2: Divide 8 (dividend) by 2 (divisor) using first version of division algorithm. Show the values of each register at each of the steps. *(3 marks – 0.5 marks for each iteration and 1 mark for writing final answer below)*

Iteration	Step	Divisor	Remainder
0	Init		
1			
2			
3			
4			
5			

Final Quotient: _____

Final Remainder: _____