National University of Computer and Emerging Sciences, Lahore Campus

THE REMEMBERS OF THE PROPERTY	Course Name:	Computer Architecture	Course Code:	EE204
	Program:	BS(Computer Science)	Semester:	Fall 2019
	Duration:	30 Minutes	Total Marks:	20
	Paper Date:	16-09-2019	Weight	~3
	Exam Type:	Quiz 1b	Page(s):	2

Student: Name:	Roll No
Section:	

Question 1a [2+2]

Bits have no inherent meaning. Given the bit pattern 0000 0000 0100 1010 0000 1001 0110 0010

- a. Assuming it as I-type instruction identify source and destination register numbers.
- b. Assuming it as an unsigned binary value, what is the corresponding integer value?

Question 1b [8]

 $\mathbf{a} \cdot \mathbf{x} + \mathbf{y}$

b. x * y

Question 2 [8]

A sequential circuit has two D flip-flops A & B, input y and one output z. The flip-flop input equations and circuit output are as follows.

$$\mathbf{D}_{\mathbf{A}} = \mathbf{A}\mathbf{y} + \mathbf{A}\mathbf{B'}$$

$$D_B = By' + AB$$

$$z = yA + yB$$

Draw logic diagram of the circuit and tabulate the state table