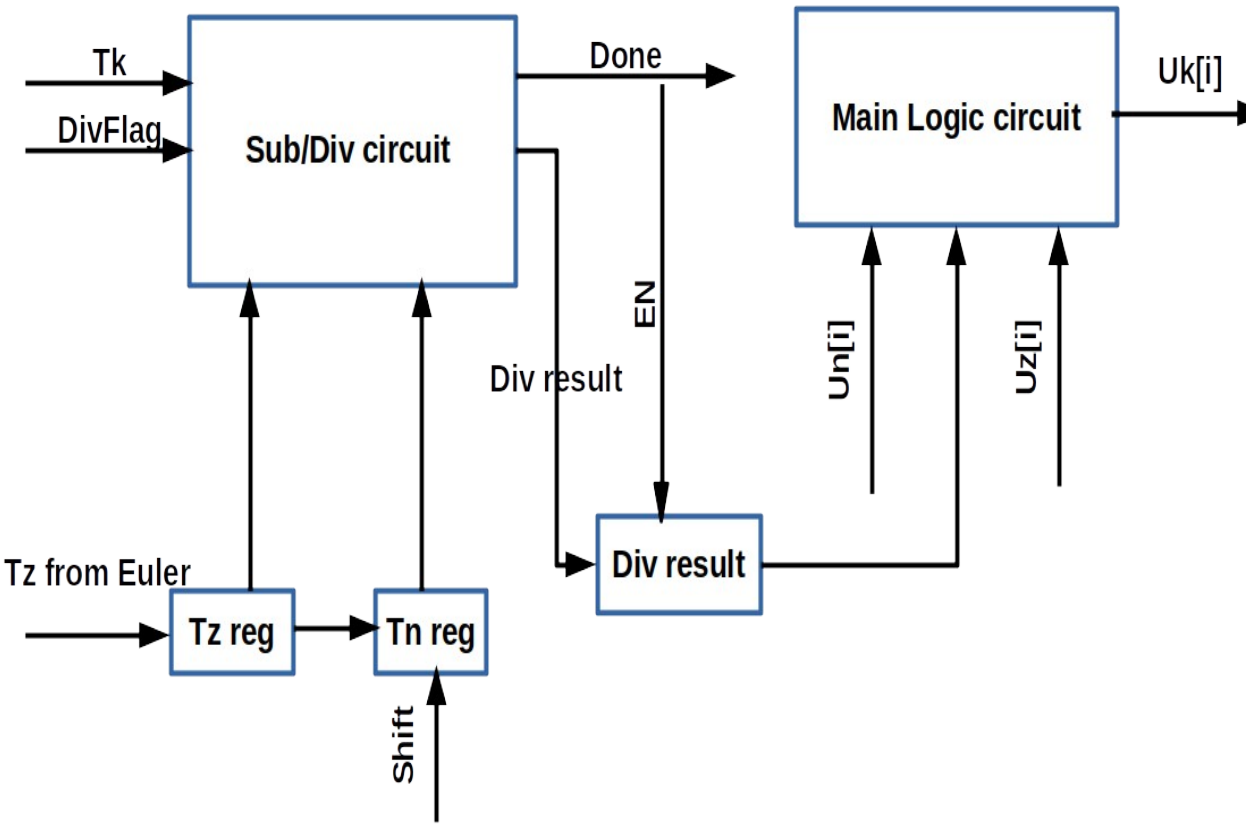
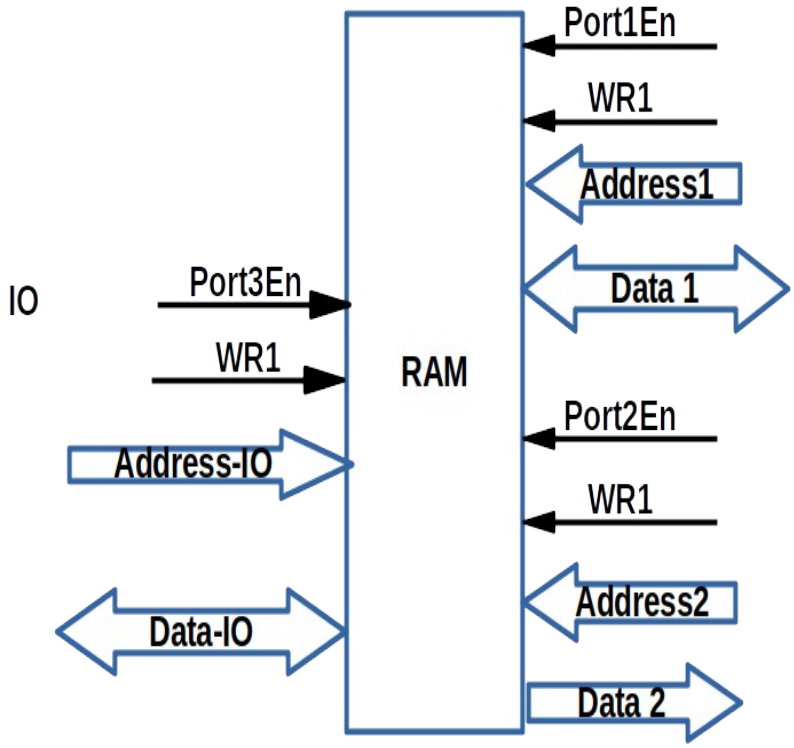
Interpolation:

- **blocks:**
  - **Sub/Div circuit :** to calculate (Tk-Tn)/(Tz-Tn) which change after N cycles.
  - **Logic circuit:** use the division output to calculate Uk[i].


Tk
DivFlag
Sub/Div circuit
Done
Main Logic circuit
Uk[i]
EN
Un[i]
Uz[i]
Div result
Div result
Tz from Euler
Tz reg
Tn reg
Shift

RAM:


Port1En
WR1
Address1
Euler
IO
Port3En
Data 1
WR1
RAM
Port2En
Address-IO
WR1
Address2
Data-IO
Data 2