

# Operating System

Date Jan, 14 2023  
M T W T F S S

OS:

- Interface b/w hardware and users
- OS is a collection of programs (software) that helps users to use the computer hardware.  
(OS)
- This OS gives you a particular beautiful view of a ugly hardware  
(OS)
- Software abstracting hardware
- Set of utilities to simplify application development / execution
- Control program
- Acts like a government

## Services of operating System

- User Interface
- Program execution (important service in OS)
- I/O operation
- File-System manipulation (manipulate → handle OS control)
- communication (Inter-process communication)
- Error detection
- Resource allocation (Memory allocate for programs in RAM)
- Accounting
- Protection and security

## Goals of operating system

- ① Convenience (User Friendly)
- ② Efficiency (use resource best efficiently way)
- ③ Portability (Portable for every hardware or enhance features without uninstalling)
- ④ Reliability
- ⑤ Scalability (update)
- ⑥ Robustness (handle unexpected errors)

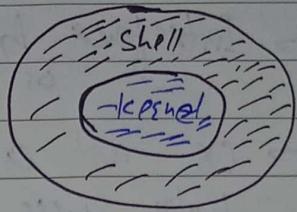


## Parts of OS

F

Kernel: Core

All the functionalities of OS  
(OS) functions of all operations are written  
in kernel

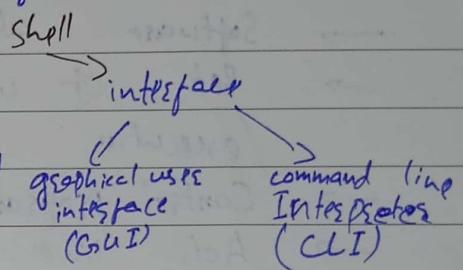


Shell: Frontend

kind of interface that uses the functionalities that  
are in kernel

System call

A system call is a way for  
programs to interact with OS



For any operations <sup>OS will</sup> ~~OS will~~ particular function  
is called system call

<sup>having special rights</sup>

Pivileged operations:

that only OS can do

unprivileged operation:

that user OS process ~~can~~ can do

Dual mode of operation: (used to implement protection)

2 modes

- 1) User mode (mode bit = 1)
- 2) Kernel/System/Supervisor/Pivileged mode (mode bit = 0)

## Types of operating Systems

### ① Uni-programming OS

OS allows only one process to reside in main memory (RAM)

→ single process can't keep CPU and I/O devices busy simultaneously

→ Not a good CPU utilization.

OS	M.M
Running Program (P1)	

### ② Multi-Programming OS

OS allows multiple processes to reside in memory

OS	→ Better CPU utilization than uni-prog OS
P1	→ Degree of multiprogramming
P2	No of running programs (processes) in M.M.
P3	
P4	→ As degree of multiprogramming increases, CPU utilization also increases (but upto certain limit).

#### Types

##### Preemptive

Running process can be forcefully taken out from CPU

→ this type not present in uni-prog OS

##### Non Preemptive

A process runs on CPU until it wants.

→ Either process terminates or goes for I/O operations



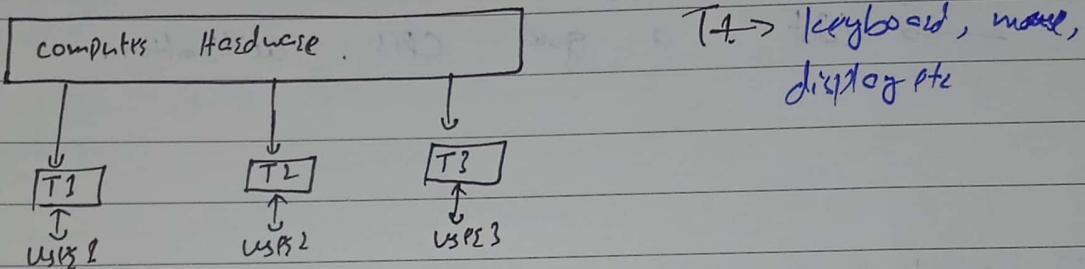
③ Multi-tasking OS / Time-sharing OS → enables users to execute multiple computer tasks at the same time.

Extension of multi-programming OS in which processes execute in round-robin fashion (fixed time for each process)

④ Multi-user OS

This OS allows to access single system simultaneously.

Unix, Linux → multiuser  
Windows → not

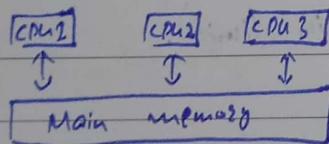


⑤ Multiprocessing OS

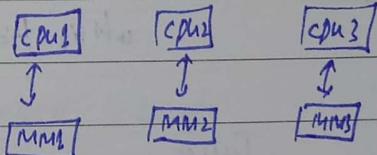
This OS is used in computer systems with multiple CPUs.  
Example: Windows NT, 2000, XP and Unix.

Types

Tightly coupled  
(shared memory)



Loosely coupled  
(distributed memory)



⑥ Embedded OS

An OS for embedded computer systems

→ Designed for specific purpose, to increase functionality and reliability for achieving a specific task.

→ user interaction with OS is minimum.

## ⑦ Real Time OS

Real time OS (RTOS) are used in environments where a large number of events, mostly external to the computer system, must be accepted and processed in a short time or within certain deadlines.

Ex

- OS used for socket launching  
→ Every process have deadline

Types

Hard RTOS

→ strict about deadline

Soft RTOS

→ some relaxation on deadline

## ⑧ Hand-held Device OS

OS used in hand-held devices

Points

- The process that are residing in main memory and are ready and waiting to execute are kept on a list is called a ready queue.

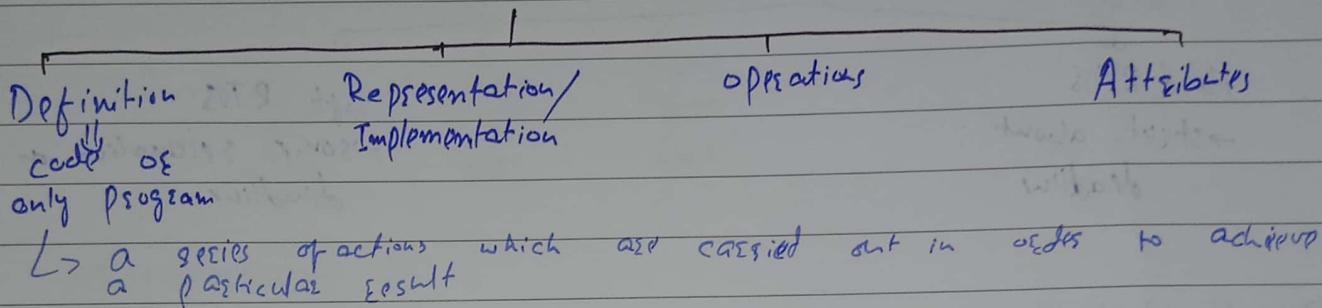
# Process Management

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## Process:

- Program under execution
- Process = Program + runtime activity
- An instance of program
- schedulable/Dispatchable unit (CPU)
- Unit of execution (CPU)
- Locus of control (OS)

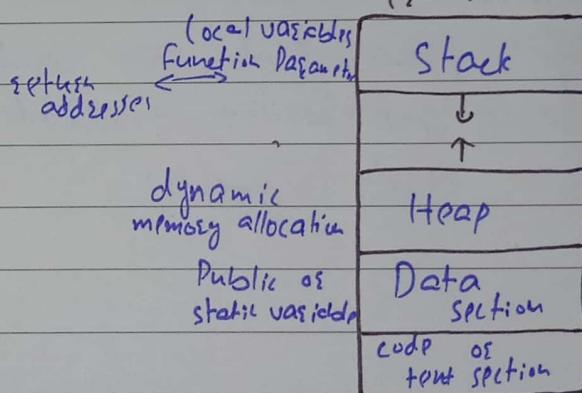
## Process as data structure:



## Representation of Process:

How process stored in memory  
every process stored in 4 section in memory

→ Heap and stack are not of fix size



## Operations on Process:

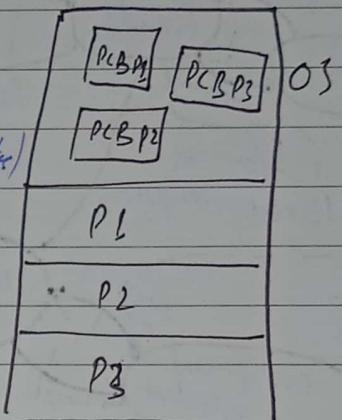
- Create (resource allocation)
- Schedule, Run
- Wait / Block
- Suspend, resume
- Terminate (resource deallocation)



## Attributes of a Process

- PID (Process ID)
- PC (Program counter)
- GPR (General Purpose registers)
- list of devices
- Type
- size
- Memory limits (Base + limit register)
- Priority
- state
- list of files
- Accounting information (CPU cycles)

OS stores attributes of process in data structure and that collection is called Process control block (PCB) also known as process descriptor.



## Context switching:

Stop running process and store its context in its PCB, and load the context of new process in CPU, and run new process.  
 Context load (content save)

PCB stored in OS Protected area

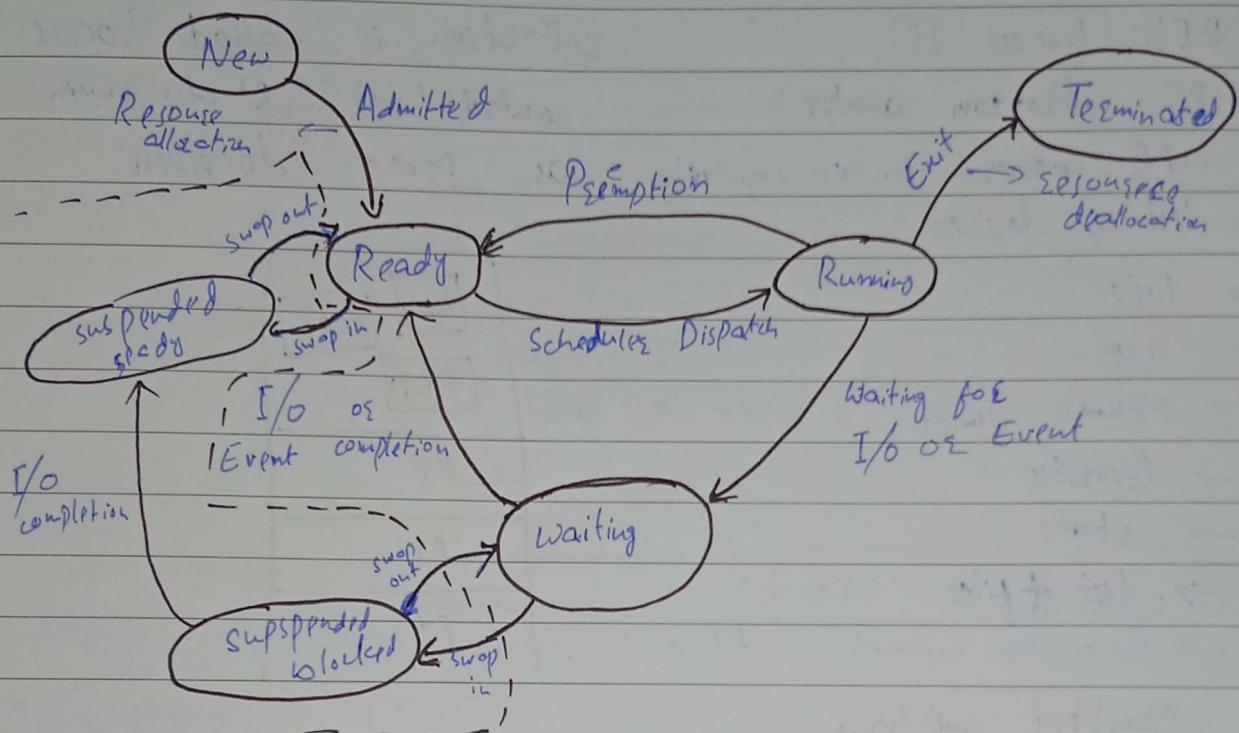
## Context:

The content of PCB of a process are collectively known as 'context' of that process.

## Dispatcher Function

context switch is done by dispatcher.

## Process State:



When you open an app then before executing process OS takes that process from new to ready state

Ready:

All process that are going to run<sup>in CPU</sup> stay in ready state

only 2 transitions running to termination and running to waiting done process itself

1 transition: running to ready (Preemption) done by forcefully(OS).

all other transitions done by OS.

New:

All installed process are in known to be in new state

Running:

Blocked All process which are waiting for any I/O or event

New: If refers to a new process that has been created but has not yet been approved for execution by OS.

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Q: n process admitted, m CPU mn

State	min. process	Max process
Running	0	m
Ready	0	n
Blocked	0	n

Non preemptive process

There is not any preemption transition  $\Rightarrow$ , There is not any way to go Running  $\rightarrow$  Ready state.

CPU bound process:

A process which stay more time with CPU  
or If the process is intensive in terms of CPU operation  
ex: Antivirus, IDE

IO bound

If the process is intensive in terms of IO operations

ex:

Printer, Playing music

IO bound process also use CPU b/c process come in I/O state from running state which is CPU

A process which has just terminated but has to relinquish its resources is called Zombie process



## Process Scheduling

Scheduling is needed b/c we can utilize each and every thing properly  
or for better resource utilization

### Scheduling Queues

Queues are in OS

- Job Queue ⇒ kept all processes which are in new state.
- Ready Queue ⇒ all processes which are in ready state
- Device Queue ⇒ all processes which are waiting for a specific device.

All queues store PCB's of process

### Types of schedulers:

- 1.) Long-Term Scheduler (Job)
  - 2.) Short-Term Scheduler (CPU)
  - 3.) Mid-Term Scheduler (Medium-Term)
- (Resource allocation)  
These are three functions in OS

Job scheduler / Long-term scheduler takes process from new state to ready state. The process initiated in two ways by user or by OS (some background processes).

Short-Term Scheduler decides which process has to run on CPU from all those process that are ready.

→ We need long-term and short-term schedules b/c of their frequency of usage.  
Short-term scheduler used frequently (usage).



### Mid-term schedules:

When you don't have space in main memory to open an app and you want to open it faster and click on it at time Mid-term schedules takes place it check which process(es) ~~are~~ <sup>for short time</sup> in active from long time take that one or more process ~~from~~ <sup>for short time</sup> from main memory to hard disk. This process is called swap out. Process when you're working is complete and you want to back that process which moved to disk and you open it, it will show you loading and this is called swap in and your process is start from there where you left.

swap out and swap in both is called swapping ~~colling~~  
(swapping done by Medium term scheduler)

swapping is also called colling if swapping is based on process priority

### Swap space:

Area of hard disk which only access by OS and where sp swap out process takes place in hard disk

when any process swap out its state will become suspended

from two states process can swap out from ready or waiting state if process swap out from Ready state its state become suspended ready and if process swap out from waiting state its state become suspended(Ready) blocked.

When process is swap out its PCB is still in OS



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When short term scheduler selects which process has to move from running state then dispatch takes place to context switchies

## CPU Scheduling

Function → Make a Selection

Goal:

- Minimize Wait time and Turn-around time
- Maximize CPU utilization (Throughput)
- Fairness (all type of process scheduled).



Long-term scheduler controls man degree of multiprogramming  
if there is few process in main memory long - term  
scheduler schedules new process if there is any , if there is  
maximum process in main memory then long-term scheduler doesn't  
schedule any process.

Mid-term scheduler reduces the degree of multiprogramming.

long term and Mid-term scheduler both controls the degree of  
multiprogramming

## Scheduling Times

1. Arrival Time (AT) → The arrival time of a process is when a process is ready to be executed.
2. Burst Time (BT) → The total amount of time required by the CPU to execute the whole process.
3. Completion Time (CT) → The time at which process completes its execution.
4. Turnaround Time (TAT) → Time from arrival to completion.
5. Waiting Time (WT) →  $WT = TAT - BT$
6. Response Time (RT) → Amount of time from arrival time and the time at which process first gets the CPU.
7. Scheduling length (L) →  $\max(CT) - \min(AT)$
8. Throughput → no. of processes executed per unit of time. ( $\frac{\text{No. of processes}}{\text{Total time}}$ )

Every process has no I/O operation (Assumption in all algorithms).

## Algorithms

① First come first serve:

Criteria:

→ Arrival time

if two processes come at same time

Tip: Breaks → People with smaller id first

Convoys effect:

If longest process place

first then short processes have

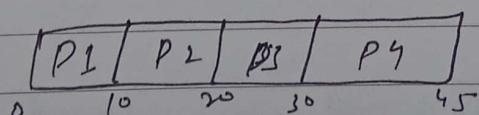
to wait more. (slows down system performance)

Type:

Non-preemptive.

Grant chart (always start from 0)

In non-preemptive scheduling waiting time is equal to response time.



## (2) Shortest Job First (SJF)

Criteria: Burst Time (smallest BT process first)

Tie breakers: FCFS

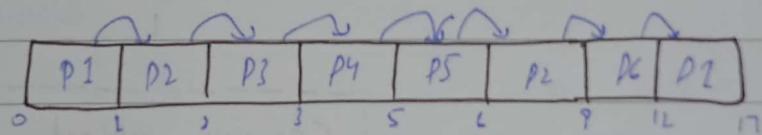
Type: non Preemptive.

## (3) Shortest Remaining Time first (SRTF)

Criteria: BT

Tie breakers: FCFS

Type: Pre-emptive



no of context switching = 7

Problems with SJF and SRTF:

1. Starvation (indefinite waiting for longest process).
2. No Fairness (if <sup>new</sup> short process coming then one long will only wait).
3. Practical implementation is not possible (b/c OS don't what is burst time of process).

## (4) HRRN (Highest Response Ratio Next)

Objective: Not only favours short jobs but decreases the WT of longer jobs.

Criteria: Response Ratio

→ Tie breakers: BT

Type:

Non-Preemptive

$$RR = \frac{W + S}{S}$$

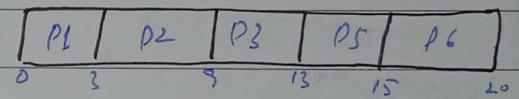
$W \rightarrow$  Wait time

$S \rightarrow$  Service/Burst time

$$RR \propto w$$

$$RR \propto \frac{1}{S}$$

Process	Arrival time	Burst time
P1	0	3
P2	2	6
P3	4	4
P4	6	5
P5	8	2



At time  $T$ :

$$RR(P_3) = \frac{5+4}{4} = 2.25 \quad RR(P_4) = \frac{3+5}{5} = 1.6 \quad RR(P_5) = \frac{1+2}{2} = 1.5$$

## (5) Priority based scheduling

Criteria: Priority

Tie-breakers: FCFS

Type → Non-Preemptive

Priority →  $P_1 > P_2 > P_3 > P_4 > P_5$

→ static → fixed

→ Dynamic → may increase or decrease by OS

Disadvantage:

→ Starvation: If high priority process keep

assessing then low priority process may wait till indefinitely

Solution of starvation: Aging → After a predefined

time period increase priority of all waiting processes by 1

→ Only applicable for dynamic priority system.

L9

Round Robin:

Criteris: AT + Q → Time Quantum

Tie-breaker → smallest Process ID First

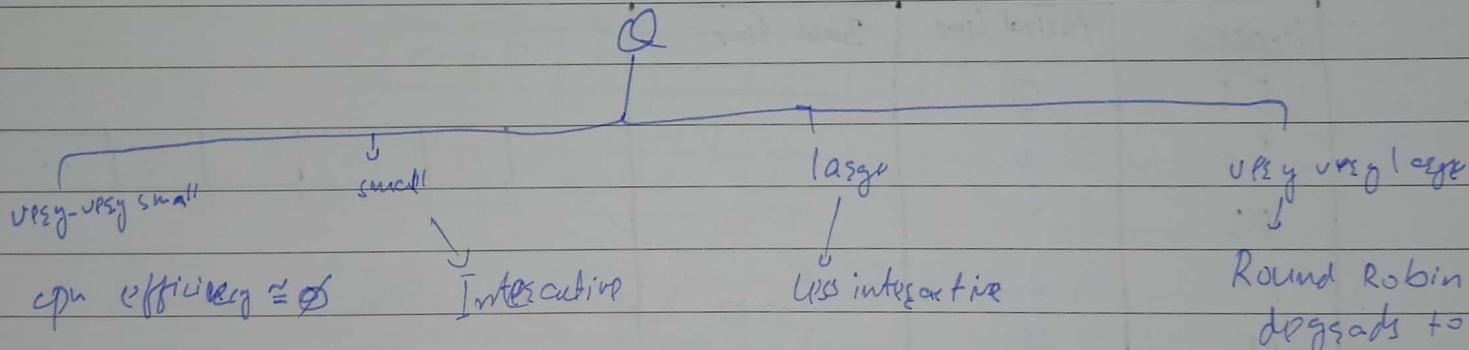
Type: Pre-emptive

Objective: Provides interactivity  
give fairness

Round-robin is not giving  
less waiting time

$$\text{No. of context switches} = \frac{\sum (\text{each process burst time})}{\text{Quantum time}}$$

What should be the Quantum value?



cpu spends more time  
in context switching  
and very less time in  
process execution.

Advantages of RR

- Fairness
- interactivity for time-sharing system
- don't depend on burst time

System call:

→ to access devices

File Related → open, create file etc

Device Related → Reposition, PTL etc

Information → getpid, attribute

Process Control → load, execute, fork, pclose etc makes them

Communication → PIPE(), shmpool()  
delete/create connections etc

In some OS we don't use directly system calls, we use API's for this OS local lib. it will use system call.

① fork()

↳ to create a child process (which is clone of parent and but with distinct ID).

→ Program

`fork();`

`fork();`

`printf("Hello");`

}

`Fork()`

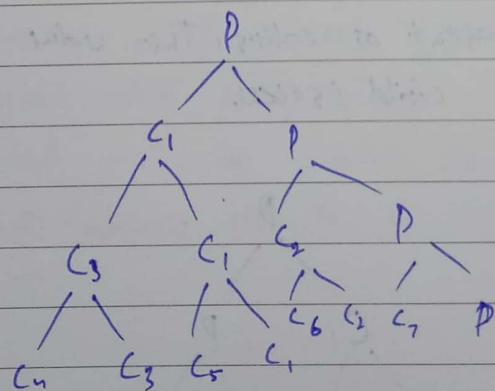
-1 (-ve)

+1 (+ve)

Error

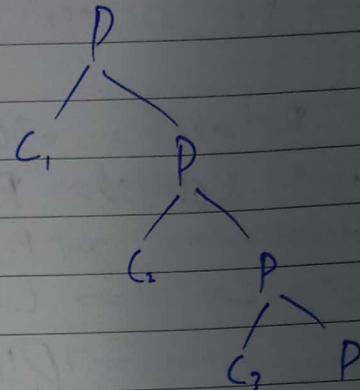
Parent

Child



Program

```
main{
    if (fork() && fork())
        fork();
    printf("Hello");
    return 0;
}
```



How System Call works

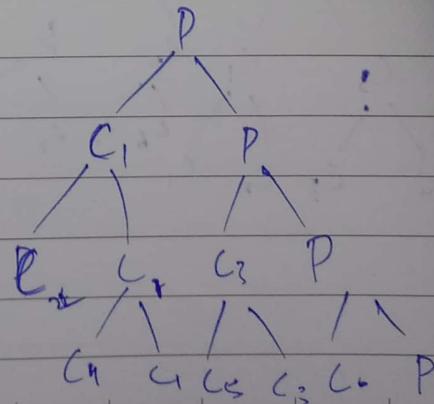
Request of Process from user space  $\Rightarrow$  interrupt request to kernel  $\Rightarrow$  control transfer to kernel  $\Rightarrow$  kernel checks if operation can be done

child process will start from immediately next statement. of fork() call.

kernel performs operation

- Negative value  $\Rightarrow$  creation of child process is unsuccessful
- Zero  $\Rightarrow$  Retured to the newly created child process.
- Positive value  $\Rightarrow$  Retured to parent or caller. The value contains process ID of newly created child process.

```
if (fork() || fork())
    fork();
    printf("B");
    return 0;
}
```



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## Booting:

→ Boot loader:

A program that loads the bigger program (e.g. OS)  
 OS ka sun hong sa phy ki sare chun khiladi boot kar  
 process.

Boot → OS to running

## Process in Linux:

The OS creates one task on startup

init: the parent of all tasks → pid is always 1  
 launched: replacement for init on exec

Daemon → Process that run in background that haven't any UI

grep -E " " . → to find something  
 / any string → dot  
 whole line

## Copy-on-Write:

"Create a copy on write"

When child process is created by fork() at both present child process map to same memory till the process has to read but as when process has to write OS map that memory separate for both

## Wait()

Blocks calling process until the child process terminates.  
If child process has already terminated, the wait call returns immediately.

## Waitpid(),

Options available to block calling process for particular child process with the first one.

pid -> waits (int x status)

wait call return value that passed in exit() in child process

## Zombie:

Instead b/w child terminating and the parent calling wait → the child is said to be zombie.

## ORphan

Process termination

Call to any exec function from a process with more than one thread shell result in all threads being terminated.

## New process Inherit

- ① Process ID ② PPID ③ Process group ID ④ Session membership
  - ⑤ Real group ID ⑥ Red user ID ⑦ Supplementary group IDs
  - ⑧ Current working directory ⑨ Root directory ⑩ Time left until alchemy
- clock signal.

## Exec

Replace the code with another code (binary code)  
executable file

→ func, exec is declared in <unistd.h>

→ Program is same.

If an exec occurs if return → else it returns nothing

① exec    int exec (const char \*path, const char \*arg, ..., NULL)

① Take the path of executable binary file

② Take arguments (can take more than one argument)

③ Takes NULL

If an

② execp

int execp (const char \*file, const char \*arg, ..., NULL)

Parameters:

① Take executable binary file

② Take arguments

③ Takes NULL

③ execvp

int execvp (const char \*file, char \*const arg[1]);

Parameters:

① Takes executable file

Takes arguments (we can pass all arguments in NULL-terminated array argu)



#### ④ execv

int execv( const char \*path , char \* const argv[]);

→ In execv char \* file is used to construct a pathname that identifies the new process image file. If the file contains a slash character, the file segment is used as the pathname for the file.

argv[0], ... argv[n]

Pointers to NULL terminated character strings - Task strings constitute the argument list available to the new process image. Must terminate the list with NULL pointer. argv[0] must point to filename that's associated with the process being started can't be NULL.

You can also use execv to run unix system commands such as ls, cp, rm etc

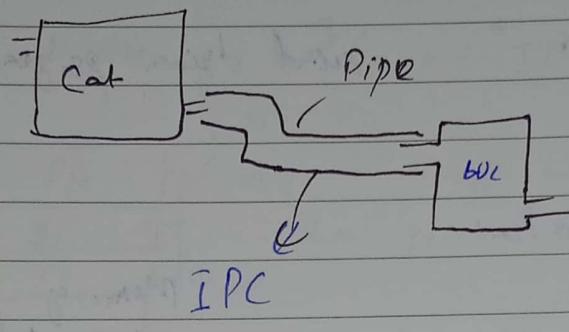
#### New Program Interface

- ① Process group ID      ② Open files      ③ Access groups
- ④ Working directory      ⑤ Root directory      ⑥ Resource usage and limits.      ⑦ Timers      ⑧ File mode mask      ⑨ Signal mask.

## Inter-Process-Communication (IPC).

Two communicate two process.

word count
WC - c (count char)
WC - L (= line)
WC - wL (= word)



OS cannot decide that process is still running or it is working when process is not responding (Process signal specification not included)

## Cascading:

The propagation of effect from one location to others location

If the OS (a signal job process) accept keyboard input from one to other screen p2 show hot = process not responding

## Signals:

PS runs → it will show all running process ID  
PS and /proc processes → it shows particular PID

- Information about processes
- Process may receive signals of asynchronous events
- Processes can affect each other if they are owned by the same user.
- Sending a signal kill → send a signal to terminate a process (int pid, int signal number) → to terminal
- Defining a signal signal → receive a signal signal (signal number, function),

int main ()

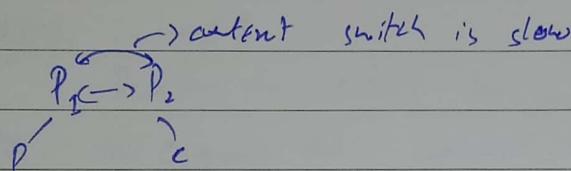
    signal (SIGINT, <sup>corrected  
number</sup> handles);  
     handles 'ctrl  
    white (1);  
     handles have been

    void handles (int sig) {

        event handlers  
         called driven program

    "Signal", sig; → Pstart

}



Memory will not  
     duplicate (on another memory)  
     when fast  
     is called b/c  
     copy or write is used

## Cooperating process:

Independent:

Process cannot affect or be affected by  
     execution of other process (no any communication with  
     any other process)

Cooperating

Process can affect or be affected by execution  
     of other process

Advantages of Process cooperation:

→ Information sharing

→ Computation speed up

→ Modularity

→ Continuation

Dangers

→ Data corruption, Deadlocks, increased complexity

→ Requires process to synchronize their processing

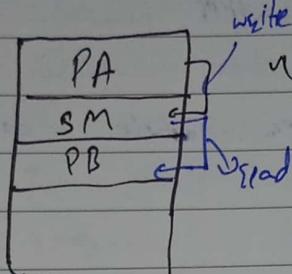
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## Two Types of Model

- Shared memory
- Message passing

1 Model  
2 IPC

→ Shared Memory → fastest IPC communication mechanism



write

If one process want to give data to another process then process writes that data in SM.

Produces process write on SM and consider  
process speed from SM

① Basic steps to use shared memory

① Request a memory segment that can be shared by processes to OS

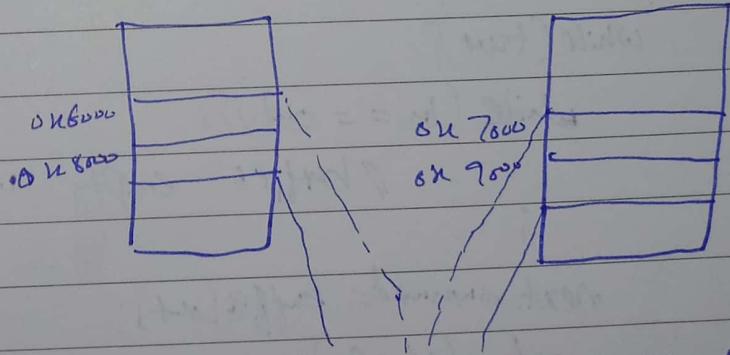
② Associate a part of that memory to whole memory with the address of the calling process

OS Maps memory segment in the address space of several processes to read and write in the memory segment w/o calling OS function

\* Shared memory region in two or more process, there is guarantee that the regions will be placed at same base address

form

① Shared memory region resides in address space of process that initiates communication



Two Process don't write at same location simultaneously

shared memory segment



2 5 2 1 d

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## Producers-Consumers example

Producer: Produces information that will be consumed by consumer  
Consumer: Consumes information that produced by the producer

Two versions of producers

- ① Unbounded buffer → unlimited buffer
- ② Bounded buffer → limited buffers

producer can produce one item while consumer is consuming another item.

while (true){

    while ((in + 1) % Buffer-size == out){

        // Wait do nothing

}

    buffer[in] = next\_produced

    in = (in + 1) % Buffer-size;

}

    item next\_consumed

Scheme allows at

most Buffer-size items in the buffer  
at the same time

while (true){

    while (in == out){

        // buffer empty wait

}

    next\_consumed = buffer[out];

    out = (out + 1) % Buffer-size;

}

Inter-related process communication is performed using Pipes or Name pipes.  
 Unrelated process communication performed using Name pipes or by IPC techniques (shared memory and message queue)

- Problems with pipes, FIFO and message queues → information exchange b/w two processes goes through kernel it works as
- Server reads from input files
  - Server writes this data in a message using pipe, FIFO or message queue.
  - The clients reads the data from IPC channel, again requesting data to be copied from the kernel's IPC buffer to the client's buffer.
  - Finally data is copied from the client's buffer.

total four copies of data (2 Read, 2 write)

### Message-Passing model

- Cooperating process communicate by exchanging messages (exchanging messages with one another).
- Implement easily than shared memory.
- Useful in distributed environments (Network based large).
- Implement using system calls and is more time consuming  
 ex: chatting

Two operation

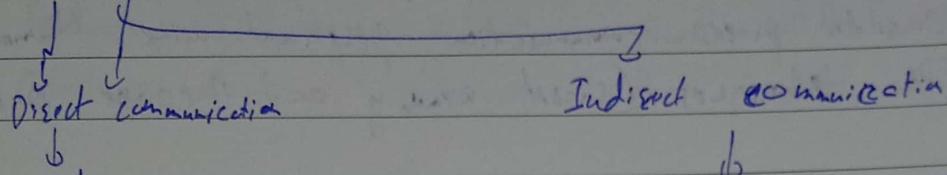
- 1) Send message
- 2) Receive message

### Logical Message Passing model

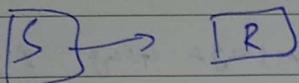
- ① naming
- ② synchronization
- ③ buffering



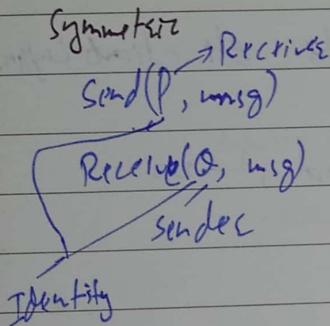
① Naming → Does the oak deserve its identity (Reflexive) because it has charge



Sender receives ko direct message bhijta hai  
Woh mai koi Third Party nahi hai.



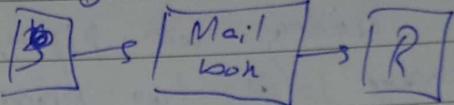
## Direct communication



A Link associated with only one pair of communicating nodes.

- link may be unidirectional but is usually bidirectional

disAdv: Process must know the name  
of the process(es)



→ Each mail box has unique id

## Indirect Communication

OS Working

→ Create Mailbox (M.B.)

→ Put a message in M.B

of later message from M.B.

and send to species

McIlroy delete kasye

Send( ) -> M.B ( → Recv( )

Send( $\mu_1$ ,  $\overline{\mu_2}$ )

Receive (M, msg)

## Peoples first of communication

→ A link is established b/w a pair of processes only if both shared Mailbox.

→ A link may be associated with more than two processes

Prada

$$P_1 \rightarrow \overline{[M, B]} \rightarrow P_2$$

only one PSEU can exercise at a time  
solutions create self-selective links

Solution: Notify which process can see upstream

Solutions: One way of element synthesis is

→ Each pair of processes may have 28 communication links

- From sender side synchronization ensures that receiver is ready to receive message before send until receiver is ready
- From receiver side requests a message from sender process that sender process is ready to send message ~~receives~~ receives Date 20 M T W T F S S suggests until sender is ready

## Synchronization

Communication takes place through calls to send() and receive functions.

→ Different design to implement each function include message passing by blocking or unblocking also known as synchronization and a ~~asynchronous~~

- ① Blocking send
- ② Unblocking send
- ③ Blocking receive
- ④ Unblocking receive

Blocking Send: <sup>Sender</sup> → block until the receiver is ready  
sending process is blocked until the message is received by receiving process or mailbox

Unblocking = : Sender can send the message if no waiting receiver is there.

Blocking Receive: Receiver blocks until sender sent the message  
receive process blocks until a message is available

Unblocking Receive:

Receiving process receives either message or NULL

## Buffering:

Message exchanged via any type of communication  
 Besides in a temporary queue.

These types of Queue.

- ① Zero capacity: (Message passing system without buffering)  
 Maximum length 0. Sender wait until  
 recipient receives the message.
- ② Bounded capacity  
 Finite size of queue
- ③ Unbounded capacity  
 Infinite size of queue.

→ Communication in client/server system

## Sockets:

Used for communication in client server system  
 → Socket is the endpoint of the communication protocol

A pair of processes communicating over a network employ  
 a pair of sockets → one for each process

A socket is identified by an IP address concatenated  
 with a port number

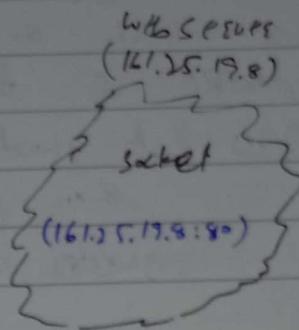
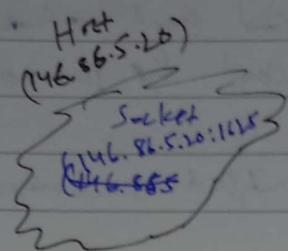
All ports below 1024 are considered well known

$$\text{FTP} = 21 \quad \text{Total port} = 2^{16}$$

$$\text{Telnet} = 23$$

$$\text{HTTP or Web Server} = 80$$

## Communication using sockets



- when a client process initiates
- request for connection, it is assigned a port by host computer

Packets travelling b/w the hosts are delivered to the appropriate process on the destination port number

Sockets can be

- Connection oriented (TCP) socket
- Connectionless (UDP) socket

in Java socket can also be  
Multicast socket  
class data can be sent to multiple  
recipients

Considered low level modes of communication

→ shows unstructured stream of bytes

## Remote Procedure call

Processes that are residing in different system connected over a network wants to connect.

→ RPC is a protocol that one program can use to request a service from a program located in another computer on a network, without having to understand the network details.

→ we must use a message based communication scheme to provide a remote service

→ messages exchanged in RPC communication are well structured and are thus no longer just packets of data



Stubs: → Client side proxy for the actual procedure on the server

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→ Each message is addressed to an RPC daemon listening to a port on the remote system, and each contains an identifier of the function to execute and the parameters to pass that function.

then function is executed output is passed to requester in separate message

How client to invoke a procedure on a remote host-

→ RPC system hides the details that allow communication to take place by providing a stub on the client side.

→ Typically a separate stub exists for each separate remote procedure.

→ When the client invokes a remote procedure, the RPC system calls the appropriate stub, passing it the parameters provided to the remote procedure. This stub locates the port on the server and marshals the procedure.

→ Parameters marshalling involves packaging the parameters into a form that can be transmitted over a network.

→ Stub then transmits the message to the server using message passing.

→ A similar stub on the SERVER side receives this message and invokes a procedure on the server.

→ If necessary, return values are passed back to the client the same technique.

### Issues

- ① Differences in data representation on the client and server machines
- ② Little endian ③ Big endian  
not occur in IPC

### Solution:

RPC defines a machine-independent representation of data. One such representation is known as External data representation (XDR)

Client side: Programmatic marshalling involves <sup>copying</sup> machine dependent data into XDR

- ② RPC calls can fail, or be duplicated and executed more than once as acted upon exactly one. Rather than at most once. Most local procedures call has

the "exactly once" functionality. It is more difficult to implement.

→ fixed port addresses

→ Rendezvous mechanisms or match makes (store port no and their func-

PIPE → s1 → OSUL

getchar → How many bytes have been written  
P0 → Writeto() → PIPE

one way

P,

Read()

file descriptor

#include <sys/types.h>  
int Pipe(int fd[2])

o fd[0] & fd[1]

R  
Read

W  
write

o Return '0' - success  
'-1' - fail

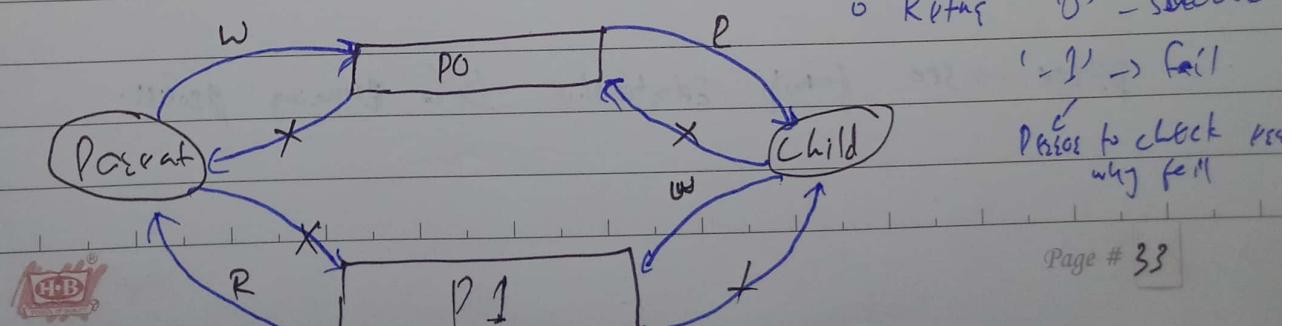
Please check  
why fail

⇒ child and parent process communication

⇒ FIFO working

⇒ write 512 bytes

⇒ Read 1 byte



### Ordinary pipes:

→ UNIX / LINUX allows communication b/w selected processes

→ Communication is in standard producer-consumer style  
 → unidirectional

→ In windows analogous pipes

two way

→ use two ordinary pipes out for P, out for C

### Named Pipes:

→ more powerful than ordinary pipes

→ communication b/w selected or unrelated process

→ Several Process can share pipe

→ bidirectional

→ exist after communication finished.

Present in both UNIX and Windows      More powerful than UNIX

### UNIX

→ FIFO

→ Bidirectional but half duplex

→ Same machine communication

→ Byte oriented data transfer  
 (Msg treated as a continuous stream of bytes)

### Windows

Bidirectional and full duplex

Intermachine communication is also allowed

Byte oriented or message oriented transmission

has a & each in  
 discrete unit

ps2ee → see family relationship b/w running process.

# THREADS

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Component of process  
or

Threads are inde-  
pendent of other  
threads

- (lightweight process) (or) Subpart of Process
  - (or) Flow of execution through the process code
- Provide a way to improve application performance through parallelism.

Kernels are generally multithreaded

Shared Among Threads

- Code Section
- Data Section
- OS Resources
- Open files and signals
- Address space

Unique for each Thread

- Thread ID
- Register Set
- Stack
- Program counter

## Advantages

1. Responsiveness → may allow continued execution if part of process is blocked
2. Faster context switch
3. Resource sharing
4. Economy → cheaper than process creation
5. Communication
6. Utilization of multiprocessor architecture → Scalability

## Types of Thread

1. User level thread → user process manages it
  2. Kernel level thread → OS kernel manages it
- } Thread will manage by the process

If multithreading implemented in the kernel (OS) → OS aware of it

If uses processes implemented the multithreading then OS will not aware of it only <sup>use</sup> process aware of it

### User Thread

### Kernel Multithread

- ① Created without kernel intervention
- ② Context switch is very fast
- ③ If one thread is blocked, OS blocks entire process
- ④ Creation can run on any OS
- ⑤ Faster to create and manage

kernel itself is multithreaded

Context switch is slow

Individual thread can be blocked

Specific to OS

Slower to create and manage

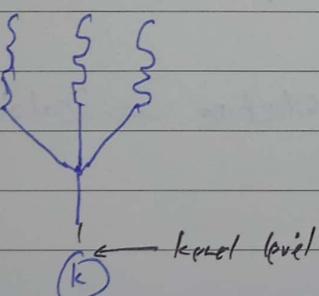
management done by user level threads library

### Models

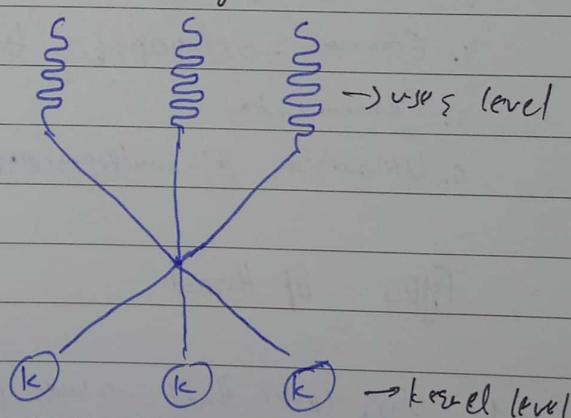
#### ① Many to one

suffers from lock contention  
 → blocking

→ INT parallel b/c only one bt in kernel at a time



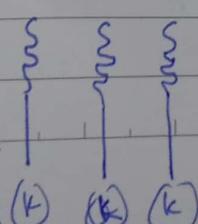
#### Many to Many



#### ② one to one

one thread

→ lower suffered lock contention  
 overhead + more kernel level threads



$k_{\text{level}} \leq \text{user level threads}$

→ suffers from none

gcc inputfile -o outputfile -lpthread

### Multithreading

Execution model that allows a single process to have multiple code segments (threads) that run concurrently within the context of that process

- In multiple processes each process operates independently of the others
  - One thread can read/write, or change another thread's data
- | Process model has two concepts
- | (i) Resource grouping  
(ii) Execution

### Multiplexing or Multiprocessor

Programmers must accurately use threads

- Divide independent problems and assign to thread
- keep a balance in dividing activities, don't over burden one or more threads
- Split data too, so threads can work properly.
- Data should be split properly, so that dependency among threads remain minimum
- Testing and debugging of the multithreaded applications is difficult

one thread can read, write or change another thread's data

### Data Parallelism

dividing large batch data sets into smaller chunks that can be process independently by different cores simultaneously perform some operation on each . commonly used in scientific computing.



## Task Parallelism:

Processing different tasks parallel on different cores. Each core work on separate tasks and results are combined to obtain final output. Commonly used in web servers, database servers and game engines.

CPU has cores as well as hardware threads.  
 Value returned by `create_new_thread` → it tells why error occurred during creation of thread  
 Creating a new thread  
 if it will return 0 if it creates successfully

① `int pthread_create(pthread_t* thread, NULL, void*, void*)`

→ A → is pointer of thread ID. It should be different from all threads.

→ B → points to a `pthread_attr_t` structure whose contents are used to thread creation time to determine attributes for the new thread. NULL means use default attrs.

→ C → is address of function which we are going to use as thread

D → is argument to function. Only one is allowed

② `pthread_join(pthread_t thread, void**)`

is used to in main program to wait for the end of particular thread

A → thread ID of particular thread

B → used to catch return value from thread

`pthread_t → datatype`



#include <pthread.h>

void indicates that pointer does not have specific type of data

void \*(void \*arg) → This can take any type of data and it is pointed to memory location where data is placed

and into function we have to cast the data to appropriate data type int \*pts = (int \*)arg;

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For Ex:

int Pthreadexit(void \*arg);

## Types

### Synchronization

TWO PARTIES decided something and doing something that was decided (Two parties should agree each other everything).

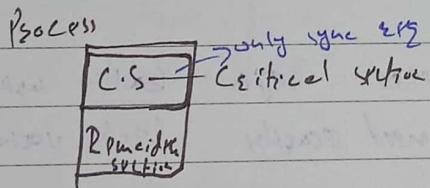
Problems without synchronization

- Inconsistency
- Loss of data
- Deadlock

### Critical Section

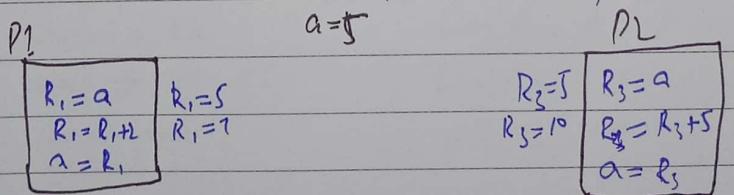
Critical section is a code of segment where the shared variable can be accessed (communication occurs).

There is no synchronization exp. for each instruction execution



### Race Condition:

A Race condition is an undesirable situation, it occurs when the final result of concurrent processes depends on the sequence in which the process complete their execution.



Result depends on which finish last



## Requirements of Critical Section Problem Solution.

1. Mutual Exclusion: → If one process is using C.S then other process cannot use that C.S
2. Progress: → If no entity in C.S and atleast one process wants to enter in C.S then it should be allowed
3. Bounded waiting: → Waiting of process should be bounded

Solution 1: Using lock

Pre-empt after each instruction

Boolean lock = false;

P0

while( $f_{true}$ ) {

    while(lock);

    lock = true;

CS

    lock = false;

RS;

}

P1

while( $f_{true}$ ) {

    while(lock);

    lock = true;

CS

    lock = false;

RS;

i) Does not

satisfy mutual

exclusion

if a process

preempted after

while(lock);

ii) Progress is s

Fail b/c you provide security to shared section but using with  
shared variable and that variable also need security (lock variable also  
use synchronization)



Solution 2: Using Turn

int turn = 0;

- Mutual Exclusion is satisfied
- Progress is not satisfied.
- Bounded waiting also satisfied

while (turn) {

while (turn != 0);

CS

turn = 1;

→ RS;

}

while (turn) {

→ while (turn != 1);

CS

turn = 0

→ RS;

}

2 Process will

execute only in  
strict alternation  
manners

Solution 3: Petessons Solution

Boolean flagB1;

int turn;

while (turn) {

flag[0] = turn;

turn = 1;

while (flag[1] && turn == 1)

CS

flag[0] = false

RS;

}

flag[0] = 0

flag[1] = 0

while (turn) {

flag[1] = turn;

turn = 0;

while (flag[0] && turn == 0)

CS

flag[1] = false

RS;

}

Problem:

Peterson solution

isn't practical b/c

flag[0] = f + f + can not solve CS

flag[1] = f + problem for more

turn = f + ! than two processes

at same time.

Checking for bounded waiting

P0 enters in CS

P1 waits outside CS

P0 comes out

flag[0] = f + f + can not solve CS

flag[1] = f + problem for more

turn = f + ! than two processes

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## Balency Algorithm

→ Processes numbered 0 to N-1

→ num is an array N integers (initially 0)

— Each entry corresponds to a process  
 $(lock(i))$

$$num[i] = \max(num[0], num[1], \dots, num[N-1]) + 1$$

for ( $P=0$ ;  $P < N$ ;  $P+P$ ) {

    while ( $Num[P] \neq 0$  &  $num[P] < num[i]$ );

}

Critical section

$unlock(i)$  ;

$num[i] = 0$ ;

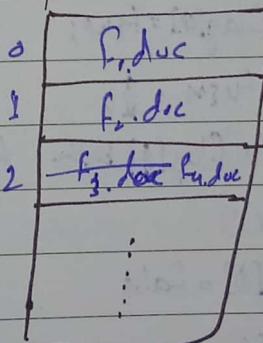
|

Point  $spooler$

value of in

- 1) Load  $R_i, m[in]$
- 2) Store  $SD[R_i]$ , "fif N<sub>in</sub>"
- 3) inc  $R_i$
- 4) store  $m[in], R_i$

spooler directly  $\rightarrow SD$



Problem loss of data

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do {

$(\underline{a}, \underline{b}) \subset (\underline{c}, \underline{d})$  if  $a \leq c$  or if  $a=c$  and  $b \leq d$

choosing[i] = true;

numbers[i] = max(numbers[0], ..., numbers[n-1]) + 1;

choosing[i] = false

for (j=0; j < n; j++) {

    while (choosing[j]); ←

        if ((numbers[j] != 0) && ((numbers[j], j) < (numbers[i], i)));

}

CS

numbers[i] = 0;

RS

Shared data

choosing [0...n-1] of Boolean

numbers [0...n-1] of int

} while();

DO will means waits for any process



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Synchronization Hardware → instruction support by CPU → can be provided to user synchronization.

- 1) Test And Set()
- 2) Swap()

privileged instruction

Implementation of semaphore with waiting queue

typedef struct

int value;

struct process \*list;

} semaphores;

value → shows how many processes

are currently in blocked

state

wait(semaphore \*s)

s → value--

if (s → value <= 0){

add process to s → list;

block();

}

block() operation suspends

the process that invokes it

Symmetric solution:

Same code

for all processes otherwise

solution is Asymmetric

signal(Semaphore \*s){

s → value++

if (s → value >= 0){

remove a process p from s → list

wakeup(p);

?

?

→ Deadlock

→ starvation

Remedy for busy waiting: Rather than engaging in busy waiting the process can block itself, block operation. Places a process in waiting queue associated with the semaphore, and the state of the process is switched to the waiting state. Then control transfers to CPU scheduler for selecting another process. Wakeup() process that is blocked, should be restarted when some other process receives from signal() - process become waiting  $\rightarrow$  Ready

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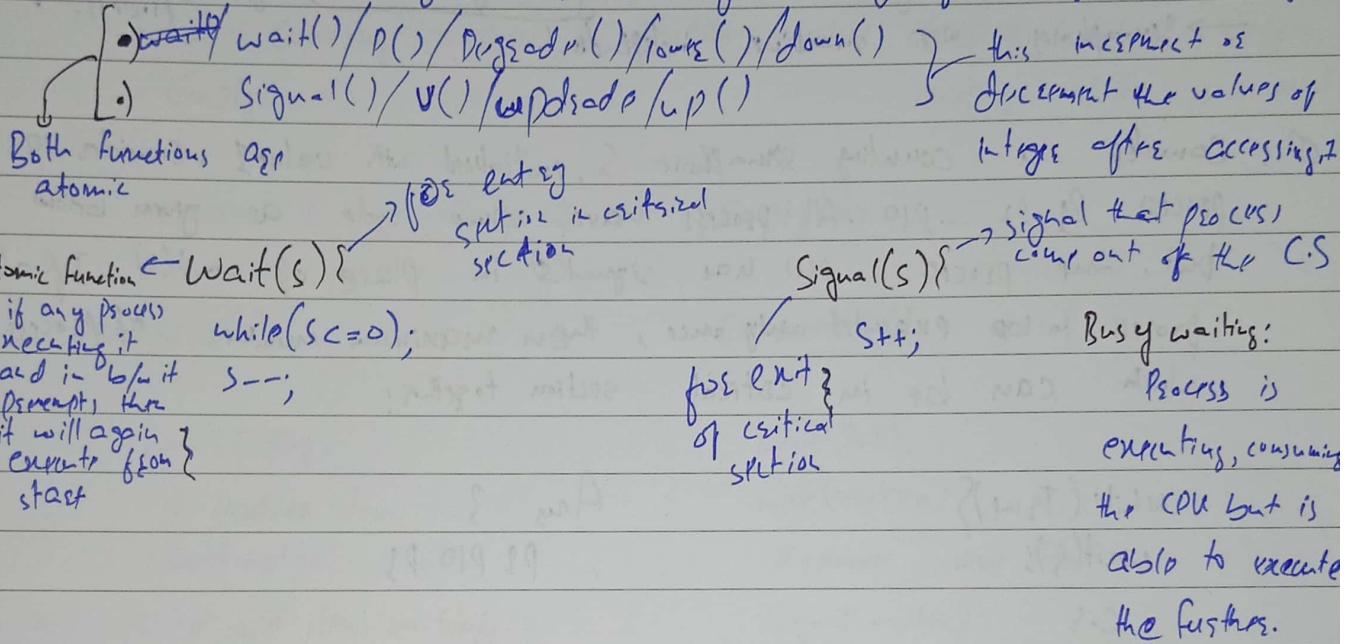
## Synchronization tools

$\rightarrow$  Atomic function can pre-empt by

1. Semaphore
2. Monitor

### ① Semaphore

It is synchronization tool which only place Integer value which can only be accessed by using following functions only.



### Types of semaphores

#### Binary Semaphore

can store Only 2 values 0 or 1

$\rightarrow$  use for provides the mutual

Excluding for implement

The solution of critical section.

#### Critical section Solution

$s=1$

```

while (Tsup) {
    wait (s);
    CS;
    signal (s);
}

```

while (1) {

wait (s);

CS;

signal (s); }

#### Counting Semaphore

unlimited domain of values

(Any integer)

$\rightarrow$  use when we will have

to provide the limited

access or we have to control access of particular

resource that has

multiple instances



## Characteristics of Semaphore:

- Used to provide mutual exclusion
- Used to control access to resources
- Solution using semaphores can lead to have deadlock
- Solution using semaphores can lead to have starvation
- Solution =  $= = =$  be busy waiting solutions
- Semaphores may lead to a priority inversion  $\rightarrow$  get CS before high priority processes.
- Semaphores are machine independent.

Q:1 Consider a counting semaphore  $S$ , initialized with value 1. Consider 10 processes  $P_1, P_2, \dots, P_{10}$ . All processes have same code as given below but, only process  $P_10$  has  $signal(S)$  in place of  $wait(S)$ . If all processes to be executed only once, then maximum number of processes which can be in critical section together:

while ( $T_{cpu} > S$ )

wait( $S$ );

C-S

signal( $S$ );

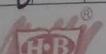
}

Ans 3

$P_1 P_{10} P_3$

Q:2 A shared variable  $x$ , initialized to 0, is operated on by four concurrent processes  $w, x, y, z$  as follows. Each of the processes  $w$  and  $x$  reads  $x$  from memory, inc by 2, store it to memory and then terminates. Each of the processes  $y$  and  $z$  reads  $x$  from memory, dec by 3, store it & = 0. Each process before reading  $x$  invokes  $wait()$  operation on a counting semaphore  $S$  and invokes  $signal()$  function on the semaphore  $S$  after storing  $x$  to memory. Semaphore  $S$  initialized to two. What is the max possible value of  $x$  after all process complete execution.

Ans = 4



# Classical Problems

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## ① Bounded Buffer (Producers-Consumers Problem)

Producer block → if buffer is full  
Consumer block → if buffer is empty

### Variables

- Mutex: Binary semaphore to take the lock on buffer (Mutual Exclusion).
- Full: Counting semaphore to denote number of occupied slots in buffer.
- Empty: = = = = empty slots = = = =

$$\text{Mutex} = 1, \text{Full} = 0, \text{Empty} = n$$

### Producers()

```
wait(Empty)  
// produce item  
wait(mutex)  
// add item on buffer  
signal(mutex)  
signal(Full)
```

### Consumers()

```
wait(Full)  
wait(mutex)  
// remove an item from buffer  
signal(mutex)  
// consume the item  
signal(Empty)
```

## (2) Readers-Writers problem

Situation where we have a file shared by many people.

- If one of the people tries to editing the file, no other person should be reading or writing at the same time, otherwise changes will not be visible.
- However, if some person is reading the file, then others may read at the same time.

Solution:

- If writer is accessing file all other readers and writers will be blocked.
- If any reader is reading, then other readers can read but writer will be blocked.

Variables

mutex: Binary semaphore → provide mutual Exclusion

wst: == to restrict readers and writers if writing is going on.

readcount: Number of active readers, Int value.

$$\text{mutex} = 1, \text{wst} = 1, \text{readcount} = 0$$

Writer()	<table border="0"> <tr> <td>Reader()</td><td>// Perform Reading</td></tr> <tr> <td>wait(mutex)</td><td>wait(mutex)</td></tr> </table>	Reader()	// Perform Reading	wait(mutex)	wait(mutex)
Reader()	// Perform Reading				
wait(mutex)	wait(mutex)				
wait(wst)	<table border="0"> <tr> <td>readcount++</td><td>readcount--</td></tr> </table>	readcount++	readcount--		
readcount++	readcount--				
// perform writing	<table border="0"> <tr> <td>if (readcount == 1)</td><td>if (readcount == 0)</td></tr> <tr> <td>wait(wst)</td><td>signal(wst)</td></tr> </table>	if (readcount == 1)	if (readcount == 0)	wait(wst)	signal(wst)
if (readcount == 1)	if (readcount == 0)				
wait(wst)	signal(wst)				
Signal(wst)	<table border="0"> <tr> <td>Signal(mutex)</td><td>signal(mutex)</td></tr> </table>	Signal(mutex)	signal(mutex)		
Signal(mutex)	signal(mutex)				
3	}				



## (2) Dining - Philosophers Problem

- $k$  Philosophers seated around a circle.
- There is one chopstick b/w each philosopher.
- A philosopher may eat if he can pick up the two chopsticks adjacent to him.
- One chopstick may be picked up by any one of its adjacent followers but not both.

Solution using Binary Semaphore

`chopstick[k] = {1, 1, 1, ...}`

This solution can lead to deadlock

```

    {
        wait(chopstick[i])
        wait(chopstick[(i+1)%k])
        // eat
        signal(chopstick[i])
        signal(chopstick[(i+1)%k])
    }

```

Some ways to avoid deadlock

- 1.) There should be at most  $(k-1)$  Philosophers on the table
  - 2.) A philosopher should only be allowed to pick his chopstick both are available at the same time
  - 3.) One philosopher should pick the left chopstick first and then right chopstick next, while all others will pick the right one first then left one.
- for last solution only two wait and signal condition will

# DEADLOCKS

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## Operations on Resources

↳ Hardware, software

### 3 operations on resources

- 1.) Request: Request required for a resource to OS
- 2.) Use: When OS allocates resource to process then process uses it.
- 3.) Release: When user is complete the process Release the resource

## Deadlock:

If two or more processes are waiting for such an event which is never going to occur

## Necessary Conditions for deadlock

Deadlock can occur only when all following conditions are satisfied

- 1.) Mutual Exclusion: If one process using a resource then other process can't use it.
- 2.) Hold & Wait: each deadlocked process should hold atleast one resource and should wait for atleast one resource.
- 3.) No-preemption: No any forceful preemption of resources
- 4.) Circular wait:

Two instances of resource if two same resource are present (e.g printer)

## Resource Allocation Graph (directed).

vertex

Edges

Process

O

having single instance

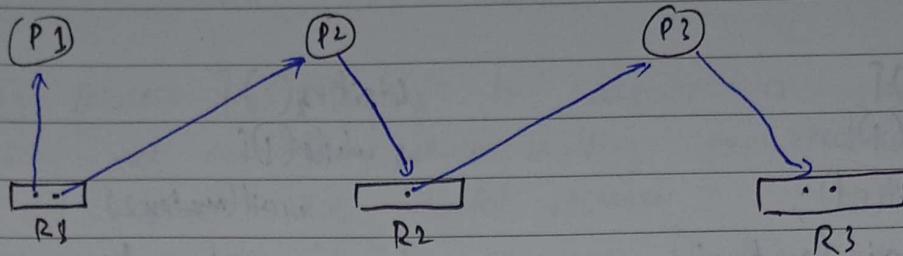
Resource

multiple instances

Request  
from process to  
resource

Allocation  
from instance  
to process

Ex:-



## Recovery from Deadlock:

1. Make sure that deadlock never occurs • Prevent system from deadlock or avoid deadlock
2. Allow deadlock, detect and resolve
3. Pretend that there is no any deadlock.

P2

## Deadlock Prevention.

- (1) Make sure that one of the necessary conditions for deadlock will never occur in a system like this.  
Prevent any of four necessary conditions to occur

## Preventing Mutual Exclusion

- increase the resources (can't be implemented)
- prepare a process like this that not share resources required (all process is independent) not possible again they require resources

## Preventing Hold and wait

- A process will either hold or wait but not together
- If all resources are available then acquire all or just wait for all (starvation can occur)



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## Second Solution: Writers Precedence

Readers()

while(1){

wait(ed);

wait(mutex1);

readCount++;

if (readCount == 1)

wait(wrt);

signal(mutex1);

signal(ed)

// Read

wait(mutex1)

readCount--

if (readCount == 0)

signal(wrt);

signal(mutex1)

}

}

Writers()

while(1){

wait(mutex2);

wrtCount++;

if (wrtCount == 1)

wait(ed);

signal(mutex2);

wait(wrt);

// write

signal(wrt);

wait(mutex2);

wrtCount--;

if (wrtCount == 0)

signal(ed);

signal(mutex2);

}

}

Continuing...

- ② If process is trying to acquire a resource which is not available, while holding some resources; then process will release the allocated resources
- ③ If a process holds resources but not using them, then there will be poor utilization of resources

### Devoiding No Pre-emption

$P_1$  request for  $R_1$  &  $R_1$  is held by  $P_2$ ,  $P_2$  is also in wait(necessary) for other process/resource or may preempt  $R_1$  from  $P_2$  and will give it to  $P_1$ . Problem: not putting type of Resources can be pre-empted.

### Devoiding Circular wait:

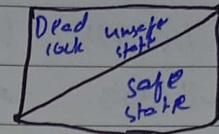
- ① All resources have been given sequence numbers (unidirectional)  $R_1, R_2, \dots, R_n$
- ② Any process while holding a resource  $R_i$  and Request for  $R_j$  ( $j > i$ )
- ③ If a process is holding a resource  $R_i$  and wants another resource  $R_j$  where  $j < i$ ; then process will have to release  $R_i$  and will have to acquire  $R_j$  first



## Deadlock

## Avoidance

To keep safe system (processes) deadlock avoidance OS helps



In deadlock avoidance, the request for any resource will be granted if the resulting state of the system doesn't cause deadlock in the system.

Deadlock avoidance will be implemented using the Banker's Algorithm.

## Banker's Algorithm:

Banker's Algorithm is a Resource allocation and deadlock avoidance algo that tests for safety.

Process	Allocation	Max	Available	Needed	
P <sub>1</sub>	1	3	2, 4, 5, 6, 7, 12	2	(P <sub>3</sub> , P <sub>1</sub> , P <sub>2</sub> , P <sub>4</sub> )
P <sub>2</sub>	5	8		3	safe sequence.
P <sub>3</sub>	3	4		1	There can be
P <sub>4</sub>	2	7		5	most than one safe sequence

System has 12 resources

D Executes P<sub>3</sub>  
 " " P<sub>3</sub>  
 " " P<sub>2</sub>  
 " " P<sub>4</sub>

} all processes can execute hence it is in safe state



Q: 2

Process	Allocation			Max			Available			Need		
	A	B	C	A	B	C	A	B	C	A	B	C
P <sub>0</sub>	0	1	0	7	5	3	x 3	3	2	7	4	3
P <sub>1</sub>	2	0	0	3	2	2	x 5	3	2	1	2	2
P <sub>2</sub>	3	0	2	9	0	2	x 7	4	3	6	0	0
P <sub>3</sub>	2	1	1	2	2	2	x 10	5	3	0	1	1
P <sub>4</sub>	0	0	2	4	3	3	x 10	5	7	4	3	1

	Allocation	Available
Ex: after	3 3 2	
P <sub>1</sub>	5 3 2	
P <sub>2</sub>	7 4 3	
P <sub>0</sub>	7 5 3	
P <sub>2</sub>	10 5 5	
P <sub>4</sub>	10 5 7	

Safe sequence  $\leftarrow P_1, P_2, P_0, P_2, P_4$

we have n numbers of processes & n numbers of resources

① Allocation: matrix of size nxm

② Max:  $\Rightarrow = \text{nxm}$

③ Need:  $\Rightarrow \rightarrow \text{nxm}$

④ Available: array of size nm



### Banks's Algorithm:

1.) Let work and finish be vectors of length  $m$  and  $n$  respectively.

Initialize work = available

finish[i] = false for  $i = 1 \dots n$

2.) Find an  $i$  such that both

(a)  $\text{finish}[i] == \text{false}$

(b)  $N_{\text{need}}[i] \leq \text{work}$  if no such  $i$  exists go to step 4

3.)  $\text{work} = \text{work} + \text{allocation}[i]$

$\text{finish}[i] = \text{true}$

go to step 2

4.) if  $\text{finish}[i] = \text{true}$  for all  $i$

then the system is in a safe state.

What will happen if process  $P_1$  requests one additional instance of resource type A and two instances of Resource type C?

$$\text{Request}_{P_1} = \langle 1, 0, 2 \rangle$$

- ① Request is valid or not  $\rightarrow$  stop 1 if invalid;  $\text{Request}_i \subseteq \text{Need}_i$
- 2.) Enough resources available  $\rightarrow$  stop 2 if yes;  $\text{Request}_i \subseteq \text{available}_i$ , else wait
- 3.) Allocate and check safety  $\rightarrow$  if safe  $\rightarrow$  request granted  
 $\downarrow$   
 o/w denied.

$$i) \text{Allocation}_i = \text{Allocation}_i + \text{Request}_i$$

$$ii) \text{Need}_i = \text{Need}_i - \text{Request}_i$$

$$iii) \text{Available} = \text{Available} - \text{Request}_i$$

## Questions

1.) Consider a system with 3 processes A, B and C. All 3 processes require 4 resources each to execute. Minimum number of resources the system should have such that deadlock can never occur?

Ans → 10

2.) System with 4 processes A, B, C and D. All 4 processes require 6 resources each to execute. Max number of resources the system should have such that deadlock may occur.

Ans → 20

3.) System with 3 processes that share 4 instances of the same resource type. Each process can request maximum of  $k$  instances. Resource instances can be requested and released only up to at a time. The largest value of  $k$  that will always avoid deadlock is  $k=2$

4.) Consider a system with  $n$  processes with single resource R. Each process requires  $k$  instance to execute. What is the maximum instances of R to cause a deadlock?

$$R = n(k-1)$$

$$\text{For no deadlock} = R = n(k-1) + 1 \Rightarrow nk - n + 1$$



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5.) Consider two processes  $P_1$  and  $P_2$  accessing the shared variables  $X$  and  $Y$  protected by two binary semaphores  $SX$  and  $SY$  respectively both initialized to 1. Note that the usual semaphore operators,  $P$  and  $V$

$$SX = 1 \quad SY = 1$$

$P_1$ :

$P_2$ :

while true do {

L1: ...

L2: ...

$$X = X + 1$$

$$Y = Y - 1$$

$V(SX);$

$V(SY);$

while true do {

L3: ...

L4: ...

$$Y = Y + 1;$$

$$X = Y - 1;$$

$V(SY);$

$V(SX);$

Avoid deadlock the correct operates at L1, L2, L3 and L4 respectively

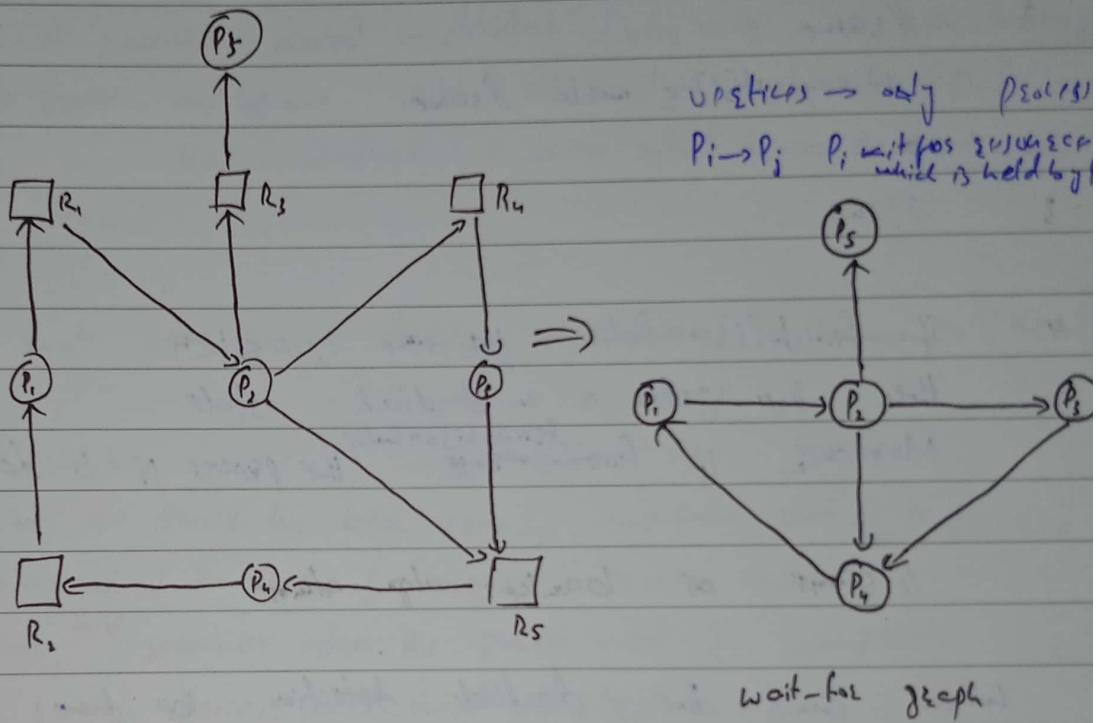
- (A)  $P(SY)$ ,  $P(SX)$ ;  $P(SX)$ ,  $P(SY)$  ✗
- (B)  $P(SX)$ ,  $P(SY)$ ;  $P(SY)$ ,  $P(SX)$  ✗
- (C)  $P(SX)$ ,  $P(SX)$ ;  $P(SY)$ ,  $P(SY)$  ✗
- (D)  $P(SX)$ ,  $P(SY)$ ;  $P(SX)$ ,  $P(SY)$  ✓

## Deadlock detection

- When all resources have single instances  $\rightarrow$  wait-for graph
- When resources have multiple instances  $\rightarrow$  detection algorithm

### 1.) Wait-for Graph

Created from Resource allocation graph



check for cycle if exist then deadlock else no

if resource has more than one instance then is not any guarantee that deadlock occurs.



2.)

If all processes can execute  $\Rightarrow$  No deadlock.

Algorithm

1 // some work = available . for  $i=0 \dots n-1$  if Request $[i]=0$   
 then finish $[i] = \text{true}$  else finish $[i] = \text{false}$

2 // same

b) Request $[i] \leq$  work // same

•

3 // same

4 if finish $[i] = \text{false}$  for some  $i$ ,  $0 \leq i \leq n$

then then system is in deadlock state

Moreover, if ~~finish $[i] = \text{false}$~~   $\text{finish}[i] = \text{false}$  the process  $P_i$  is deadlocked.

// same as banker's algo almost

When should this deadlock detection be done? Frequently or infrequently? both are time consuming.

- 1) Do deadlock detection after every resource allocation  $\Rightarrow$  Time consuming
- 2) = = = only when there is an exec. (in  $CPH$  speed decreased)

## Recovery from deadlock

Three basic approaches

- 1.) Inform the system operator and allow him/her to take manual intervention
- 2.) Terminate one or more processes involved in the deadlock
- 3.) Preempt resources

### Process Termination.

- 1.) Terminate all processes involved in deadlock (Very costly in terms of time and performance)
- 2.) Terminate process one by one until the deadlock is broken (Time consuming to break deadlock) (Problem which process to select next to terminate)

Factors that can go into deciding which process to terminate:

- 1.) Process priorities
- 2.) How long the process has been running, and how close it is to finishing
- 3.) How many and what type of resources  $\rightarrow$  people holding
- 4.) How many <sup>more</sup> resources does the process need to complete.
- 5.) How many process will need to terminate.
- 6.) Whether the process is interactive or batch.

### Resource Pre-emption

Imp issues to address when preempting resources

- 1.) Selecting a victim  $\rightarrow$  select which process and resource
- 2.) Rollback  $\rightarrow$  Rollback the progress that done by preempted if it takes resources from other processes
- 3.) Starvation  $\rightarrow$  Aik hi process so bar bas resource ka early ho.



# MEMORY MANAGEMENT

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↳ RAM

→ A Module of OS

## Functions of Memory Management

1.) Memory allocation

2.) Memory deallocation

3.) Memory protection → A process can access only its own address space remaining memory is protected from process.

Memory fragmentation: is when the sum of available space in memory is large enough to satisfy a memory allocation request but the size of any individual fragment OS (continuous fragments) is too small to satisfy that memory allocation request.

## Goals of Memory Management

1.) Maximum utilization of space (Minimum Memory fragmentation).

2.) Ability to run larger programs with limited space

↳ using virtual memory concept.

## Memory Management Techniques

### Contiguous

Entire process should be stored in MM on consecutive locations

### Non-Contiguous

process can be stored on non-contiguous locations

Fixed Partition

Variable Partition

Paging

Segmentation

Only one can be implemented at a time

### ① Contiguous Memory Management:

#### i) Fixed Partition

The MM is divided into fixed no of partitions and each partition can have different size, and each partition can be used to accommodate more or less processes.

→ Max degree of Multiprogramming is limited by number of partitions



Memory Management Requirements:  
→ Speed is not the only issue  
→ Relocation  
→ Protection  
→ sharing  
→ Physical Memory Organization

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## Partition Allocation Policy

- 1.) First Fit
- 2.) Best Fit
- 3.) Worst Fit → It remembers the previous partition
- 4.) Next Fit → same as first fit but it remembers the previous partition and search next from beginning of it

whichever policy is used there is internal fragmentation (in blocks partition there is lot of waste).

If extra space is allocated to process more than required space then wastage of that <sup>extra</sup> space is known as internal fragmentation.

## Variable Partition

MM is not divided into partitions initially. When a new process arrives, a new partition is created for same size as process size.

If enough space is available to store a process but not contiguous hence the wastage of space is known as external fragmentation.

Solution  $\Rightarrow$  compaction:

collect entire allocated process into one side of memory, so that other side of memory can have entire free space.



## ② Non-Contiguous Memory Management

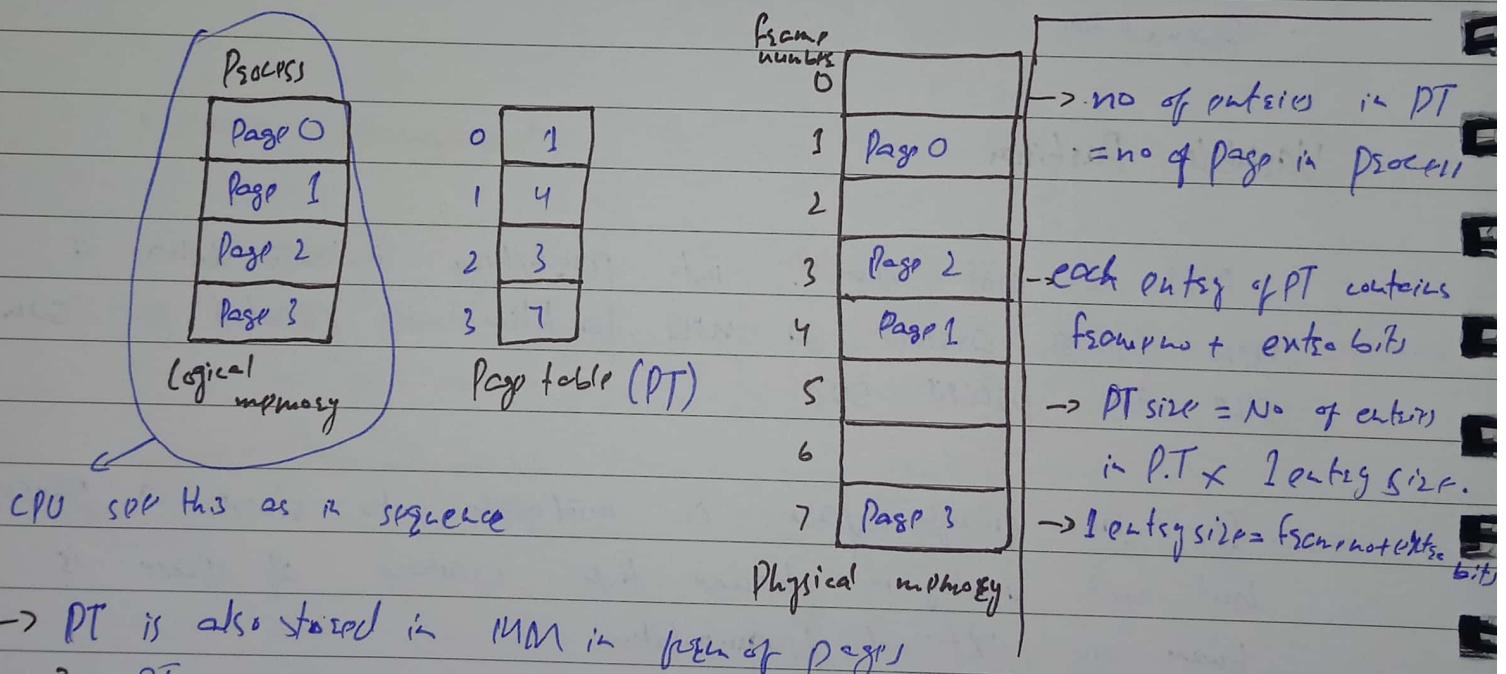
Process is scattered in memory, not allocated at one area

### Two Techniques

- Paging: Scattered in same size of memory Areas
- Segmentation: Scattered in variable size of memory Areas

### Paging

- ① Process is divided in equal size of pages.
- ② Physical memory is divided in same equal size of frames.
- ③ Pages are scattered in frames.



- PT is also stored in MM in form of pages
- PT is maintained by OS.

④ Processor will have a view of process and its pages

⑤ Page Table is used to map a process page to a physical frame.

Each entry of page table starts from no

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A word or a byte ~~separates~~ CPU at a time  
whenever CPU requests any specific content

- 1) Find out content present in which page
- 2) Search in P.T to get the frame no in which page is stored
- 3.) Get that frame no and get the content from that frame.

2 times the physical memory is accessed  $\rightarrow$  one for P.T  
 $\rightarrow$  one for content.

## Address Translation

Let

Page size = 2 bytes

Process. size = 4 pages =  $4 \times 2B = 8B$

Physical mem size = 8 frames =  $8 \times 2 = 16B = 2^4 B$

Physical ~~mem~~<sup>add</sup> 4 bits ~~size~~<sup>size</sup> 4 bits

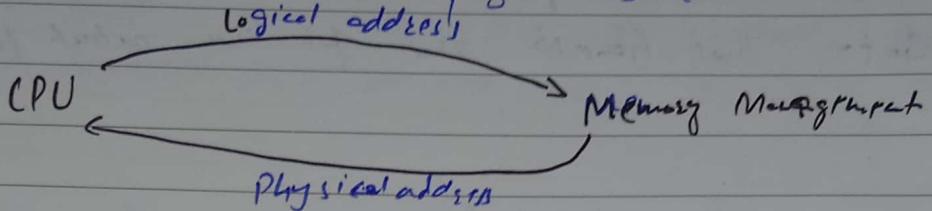
Physical address	Physical Mem	Frame No
0000		000
0001	a	001
0010	b	010
0011	c	011
0100	d	100
0101	e	101
0110	f	110
0111	g	111
1000		
1001		
1010		
1011		
1100		
1101		
1110		
1111		

Logical address	Process	Page No
0000	a	00
0001	b	01
0010	c	10
0011	d	11
0100	e	
0101	f	
0110	g	
0111	h	

6 bytes of  
page

log across

for access a CPU generates own address but it is not physical address it is logical virtual address now how to access actual physical address here Memory Management comes in Picture to access physical address



CPU always generates logical address

logical address gives info → same for physical memory



$2^k$  Pages  $\rightarrow$  k no of bits required for Page No  
 $k = \log_2(\text{No of Pages in Process})$

$$L = \text{bits for byte No} = \log_2(\text{Page size})$$

$$\therefore L = \text{No of logical address bits} - k$$

collection of all logical addresses is called logical address space (LAS)

Process size interchangeably LAS

collection of all physical address space → Physical address space (PAS)

Physical Memory size Interchangeably PAS

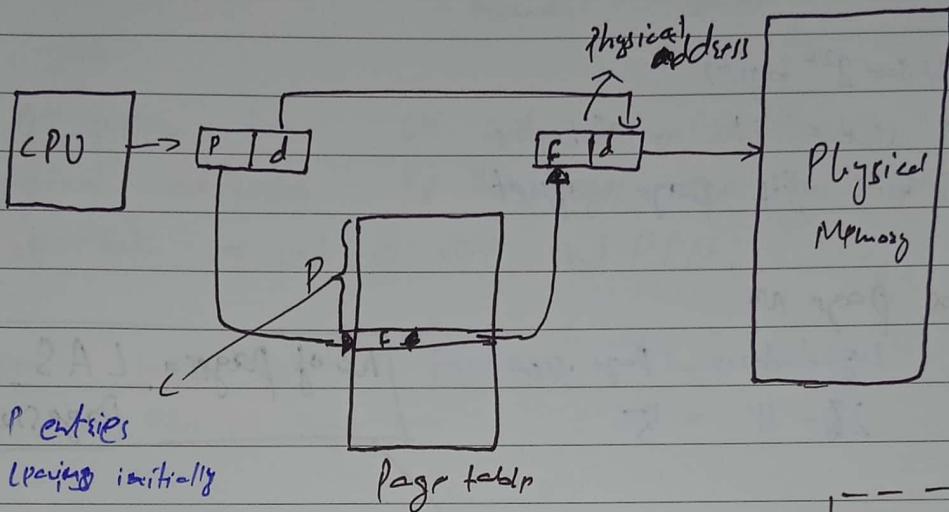
What if PT size is much larger? and PL can't store PT in one page?

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bits for frame No =  $\log_2$  (No of frames in Physical Mem)

bits for logical  $10^0$  =  $\log_2$  (Page size)

Line 100  
displacement  
Page offset PT



Find Page Table size

Logical Address = 24 bits Page size = 4KB

Table Entry = 1 byte

These is a register  
PTBR (Page table base  
Register)  $\Rightarrow$  which holds  
starting physical address  
of page table

Logical Address = 24 bits

22<sup>nd</sup> logical address space

Page size = 4KB =  $2^{12}$  bytes

12 bits for page offset

P = No of bits in page No = Logical Address - Page offset = 24 - 12 = 12

P = 12

No of Pages =  $2^P$  = 4KB

# Practise Questions

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Q:1

Logical Address = 26 bits, Physical Address = 32 bits Page size = 2KB  
One page table entry size = 4 bytes

① Bits in Page offset

Logical Address space  $2^{26}$  bytes

Page size = 2KB =  $2^{11}$  bytes

11 bits for Page offset

② Bits for Page No

Logical address - Page offset

$$26 - 11 = 15$$

$$\boxed{\text{No of Pages} = \frac{\text{LAS}}{\text{Page size}}}$$

③ Bits for Pages in Page table

$$2^{\text{bits for pages}} = 2^{15} = 32\text{KB}$$

④ No of frames in Physical Memory

No of bits for frame = <sup>No</sup>Physical address space - Page offset  
 $32 - 11 = 21$

$$\boxed{\text{No of Frames} = \frac{\text{PAS}}{\text{Page size}}}$$

$$\text{No of frames} = 2^{21} = 2^{10}\text{KB}$$

⑤ Bits for frame no

21 bits

⑥ Page table size

No of pages  $\times$  4 bytes size

$$2^{15} \text{ bytes} \times 4 \text{ bytes} = 2^{17} \text{ bytes}$$

$$\text{Page table size} = 128\text{KB}$$

Page #



1 Entry counter  $\rightarrow$  [frame No | Extra bits]

$$\begin{aligned} \text{1 Entry size} &= 4B = 32 \text{ bits} \\ &= \underbrace{22 \text{ bits}}_{\text{frame No}} + \underbrace{11 \text{ bits}}_{\text{Extra}} \end{aligned}$$

Q.2 A system has 64 bit virtual addresses and 48 bit physical addresses. If the pages are 8kb in size, the number of bits required for VPN and PPN.

$$\begin{aligned} \text{VPN} &\rightarrow \text{logical (Virtual) Page No} = 51 \\ \text{PPN} &\rightarrow \text{Physical Page No} = 30 \end{aligned}$$

Q.3 Logical address space of 8 pages, with page size = 1024 bytes  
Physical memory contains 32 frames

1.) Bits in IA

$$= \text{Page size} = 2^{10} \text{ bytes}$$

$$8 \text{ pages} \rightarrow \text{bits for No of pages} = 3 \text{ bits}$$

$$\text{Page offset} = 10$$

$$\text{Bits of IA} = 10 + 3 = 13$$

2.) Bits for PA

$$= 32 \text{ frames} = \text{bit for No of frames} = 5 \text{ bits}$$

$$\text{Bits for PA} = 10 + 5 = 15$$

3.) Page table size

$$\text{No of pages} = 8 \times 1 \text{ Entry size}$$

$$= 8 \times 5 \text{ bits}$$

$$40 \text{ bits} 5 \text{ bytes}$$

$$\begin{aligned} 1 \text{ Entry size} &= \text{frame No} + \text{Extra} \\ &= 5 + 8 \\ &= 13 \end{aligned}$$



Q: 1

Size of Physical and logical address space?  
 Page table containing 64 entries of 11 bits each (including a valid bit) and a pagesize of 512 bytes

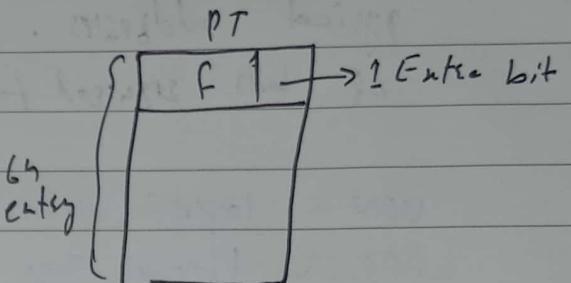
$$\text{Page size} = 2^9 \text{ bytes}$$

$$\text{No. of pages} = \text{Entries of page table} = 2^6$$

8 bits for page no. and 1 bit for page control

$$\text{LAS} = 2^{15} \text{ bytes size}$$

$$\text{PAs} = 2^{15+9-1} = 2^{23} \text{ bytes}$$



Q: 2

frameNumber

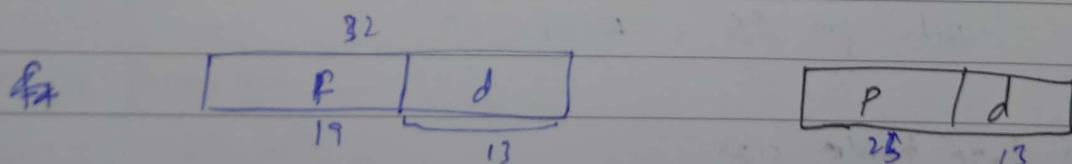
8 kb pages,  $\lceil \frac{32}{8} \rceil = 4$  frame numbers  
 Entry contains a valid bit, a dirty bit, three permissions bit  
 and the translation. If the maximum size of the page table of process is 24 MB, the length of virtual address supported by the system is \_\_\_\_\_ bit

$$\text{Page size} = 8 \text{ KB} \Rightarrow 2^{13} \text{ bytes}$$

$$\text{Physical address} = 32 \text{ bits}$$

$$\text{PT Entry} = f + Lut + Idt + 3P \Rightarrow 1 + 1 + 1 + 3 = 24$$

$$\text{PT size} = 24 \text{ MB}$$



$$\text{PT size} = \text{No. of pages} \times \text{Entry size}$$

$$2^{24}(2^{20}) \times 8 = 2^{24} \times 2^{20} \text{ bits}$$

$$n = 2^{23} \\ = 2^{23} + 13 = 36 \text{ bits}$$

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- ⑦ Logical Address space  $L$  bytes, Physical address space  $F$  bytes  
Page size of  $P$  bytes one Page table Entry size  $E$  bytes  
Field all things in 21

### Time Required in Paging

Effective memory access time = PT access + contention access  
time from main

By default, P.T is stored in main

$$T_{eff} = 2t_{mn} \quad t_{mn} = \text{main access time}$$

Special case: If P.T is very small and is stored in Register  
 $T_{eff} = t_{mn}$  Register access time is negligible

### Performance improvement

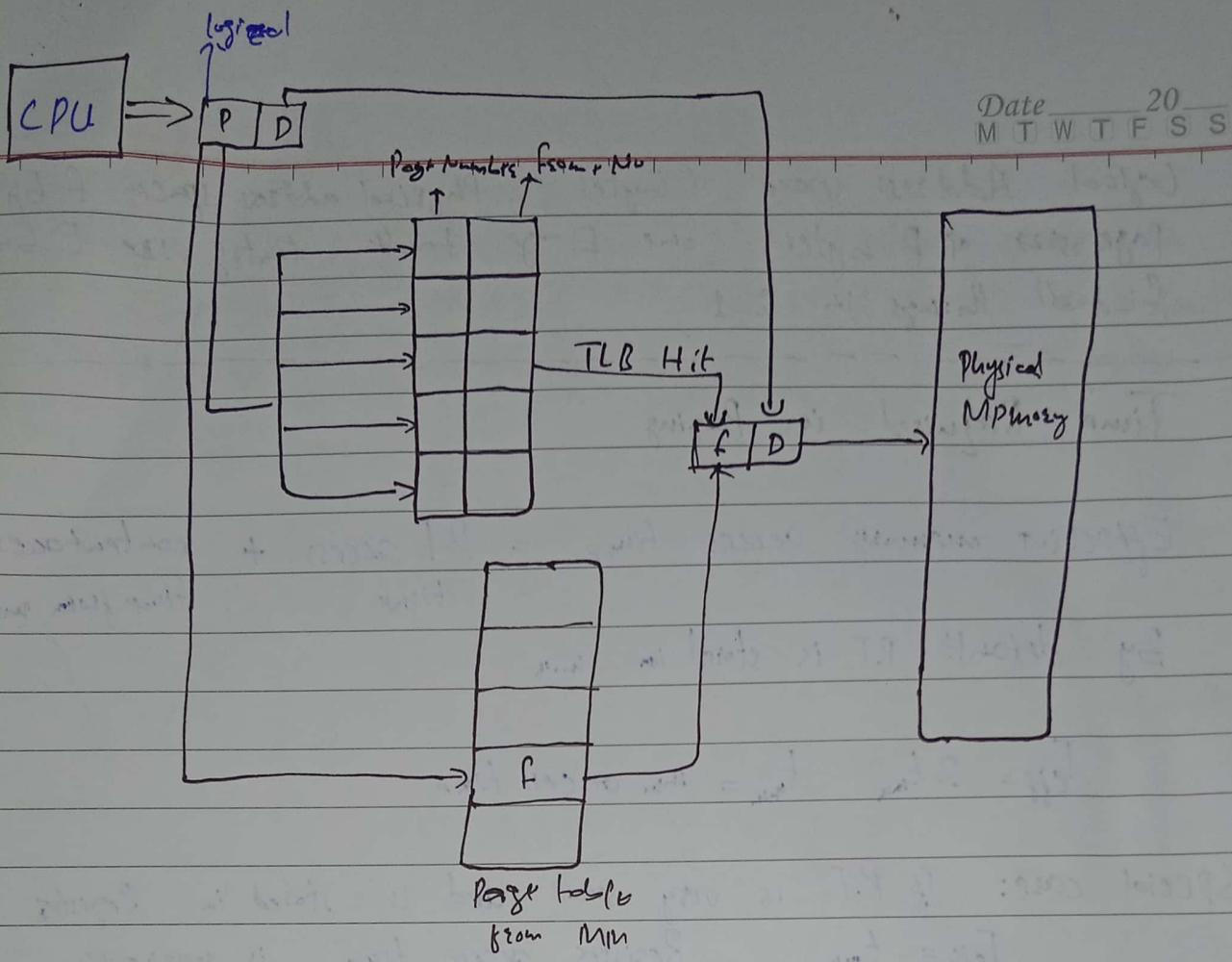
↳ TLB is used to improve the performance of paging

↳ Translation lookaside buffer (To store Physical address)

### TLB:

TLB is a caching hardware that is used to reduce the time taken to access a user memory location

It stores few frequently accessed Page table entries so that CPU can get Physical address w/o accessing the main (in less time)



Effective Access Time with TLB

H → Hit ratio

$$T_{\text{effective}} = H * (t_{\text{TLB}} + t_{\text{mn}}) + (1-H) (t_{\text{TLB}} + 2 * t_{\text{mn}})$$

↓ *TLB access time*      ↓ *content to pursue*  
 hit ratio      miss

Simplified:

$$T_{\text{effective}} = t_{\text{TLB}} + t_{\text{mn}} + (1-H)t_{\text{mn}}$$

Question 1

MM access time = 300 ns

TLB = # = 20 ns

TLB Hit Ratio = 80%

Effective memory access time?

$$\begin{aligned}
 T_{\text{eff}} &= \frac{4}{5}(20 + 300) + \frac{1}{5}(20 + 2 * 300) \\
 &= \frac{320}{5} \times 4 + \frac{800}{5} = 256 + 120 = 376 \text{ ns}
 \end{aligned}$$

Consider a system using TLB for Paging with TLB Access Time of 40 ns what hit ratio is used for TLB to reduce the effective memory Access time from 400 ns to 280 ns

$$H = 0.8 \text{ A.L.S.} \quad n_0 = 40 + t_{\text{mn}} + (1-H)$$

## Semaphore synchronization

Header file → #include <semaphore.h>

Data type for variable → semi-t

## Methods for handling semaphores

```
int sem_init(sem_t *sem, int pshared, unsigned int value);
int sem_wait(sem_t *sem);
int sem_post(sem_t *sem);
```

$p_{shared} \rightarrow 0$  for thread sync. And Non 0 value for processes sync  
 $value \rightarrow$  Initial value for semaphore variable

Address binding:

Process of mapping logical addresses to Physical addresses in memory.

→ compile Time

→ load Time

→ Expectation Time

→ logical And physical address are same in compile time and load time address binding scheme

→ " " " " " " differ in execution time address binding scheme.

Dynamic loading → load the main module at first and others when needed

Dynamic linking → body of function in library and the function needed turn to provide body separation

library. i) like & d

stems → piece of information



## Important Points

- Process Creation is achieved through the fork() system call
- The newly created process is called the child process.
- Cause process to be created. System Initialization, Execution of process creation system call by a running process, A user request to create process
- Instance: An instance is a single copy of software running on a single physical or virtual server.
- Antivirus uses too much CPU is that it runs constantly in background
- Boot loader, is a small program that has a single function. It loads OS into memory and allows it to begin operation.

## File handling in C

`FILE *fptr; → object pointer for communication b/w file and program`  
`fptr=fopen("filename", "mode");`

### Mode →

{ → open for reading → If file doesn't exist returns null  
 w → open for write → = = = it will be created if

`fclose(fptr);`

`fprintf(fptr, "%d"; num);`  
`fscanf(fptr, "%d", &num);`

`char x=fgetc(fptr);` → input from a file single character at a time  
 returns the ASCII code of the character read by the function.  
 ( $x \neq EOF$ )      `scanf("%c", &x);` also

`fputc(2, fptr);`  
`char gets(char str[], size_t size, fptr);`