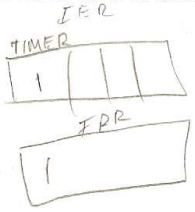
4. ISR question: For system of problem three (Interrupt Controller, 4 bits …) create an Interrupt Service Routine for the following situation. Timer Module is hooked to the most significant bit of the four identified in the question. When the timer service is requested, reset the appropriate flags, in the appropriate order, increment the value in R23 and send to the LEDs. The Interrupt controller address is identified in Problem 3. The Timer Module is located at `0x82440000`, and the LED interface GPIO is located at `0x82560000`. Do not worry about register volatility.



we will have to be constantly resing the registers after each ISR if we don't want volatility. But we don't have to worry about that as it says here

set all registers to zero ?

```
li   r0, 0x0
ori  r0,r0, 0x0
  .
  .
  .
li   r31, 0x0
ori  r31, r31, 0x0
```

why?

then begin to use registers for holding ISR value