

1 MAY 2014

TEST 2

Review

$TRISB \leftarrow 0x3c00$

SW puts bit pattern
into place

$0x04$ of address = clear

any bits
that are set
in SW
store

$0x08 \Rightarrow$ Set \Rightarrow

Sets only bits that
are high in SW store

$0x0C \Rightarrow$ Invert \Rightarrow

inverts all high bits in
the SW store location

li to, 0xFF
sw to, PORTB

Inv \rightarrow inverts 8 LSB's
clr \rightarrow clear 8 LSB's
set \rightarrow set 8 LSB's

timer prescaler \Rightarrow

Let x pulses through
for every 256 pulses.

PORTB & E

IFS Register

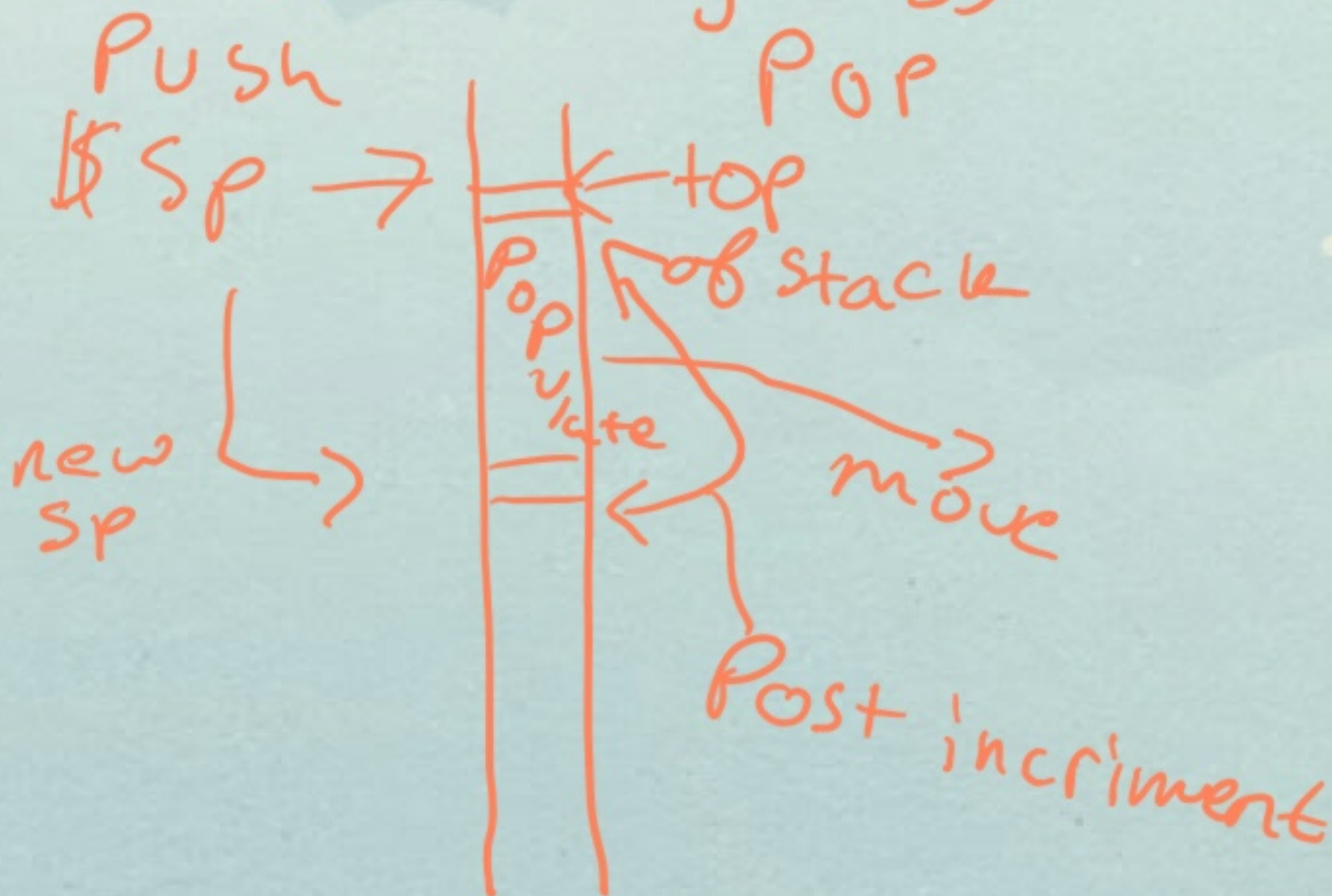
Fetch Decode Execute

How to work done

Stack-!

STACK

(SP {Stack Register})



{Push; Jump to subroutine
if &RA was
included)

① Pre decrement
(from stack)

② move data to stack

{POP

① take data to
Registers

② Post increment
SP

Linked List

