

Final

Fetch, decode, execute

instruction types

{ Test 1 stuff }

Information
Representation

(number stuff
w/ no floating
point)

Little endian
vs Big endian

RISC vs SYSC
↑
MIPS

WORK

- DATA
Structures

ARRAYS

I/O WORK

- Timer

- PORTS

- UART

{ memory mapped I/O }

SubRoutine(S)

1) encapsulate data

2) organize work

A) Parameter Passing

B) Linkage

(where return address is stored & etc)

Interrupts

-how to

Others

MAY include
