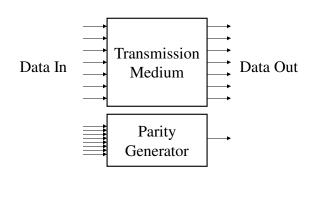


Error Detection with Parity



Error Correcting Code – by Design

Start with Bit Position = Column Number

15 14 13 12 11 10 9 8 7 6 5 4 3 2 1

Error Correcting Code – by Design

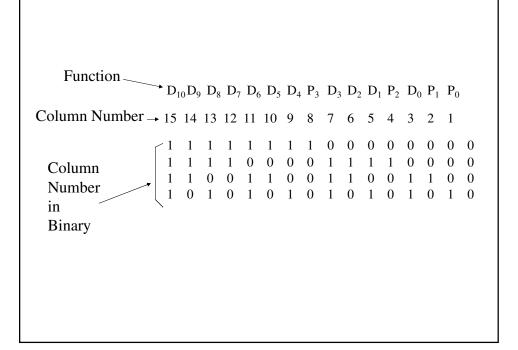
Start with Bit Position = Column Number

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Error Correcting Code – by Design

Start with Bit Position = Column Number



Four Bit Column Identifier? Then build four parity sets

Columns 15, 13, 11, 9, 7, 5, 3, 1

Four Bit Column Identifier? Then build four parity sets

 $D_{10}D_9\ D_8\ D_7\ D_6\ D_5\ D_4\ P_3\ D_3\ D_2\ D_1\ P_2\ D_0\ P_1\ P_0$ 15 14 13 12 11 10 9 3 Parity set 1: all columns 1 0 where second -0 0 0 0 1 **→** 1. 0 0 1 1 0 1 1 0 1 $\begin{bmatrix} 1 & 0 & 1 \end{bmatrix}$ 0 1 0 0 0 bit of column number is '1'

Columns 15, 14, 11, 10, 7, 6, 3, 2

Four Bit Column Identifier? Then build four parity sets

Columns 15, 14, 13, 12, 7, 6, 5, 4

Four Bit Column Identifier? Then build four parity sets

 $D_{10}D_9\ D_8\ D_7\ D_6\ D_5\ D_4\ P_3\ D_3\ D_2\ D_1\ P_2\ D_0\ P_1\ P_0$ Parity set 3: all columns 15 14 13 12 11 10 9 8 where fourth bit of 0 column number 1 0 0 1 0 1 1 0 0 0 0 1 1 1 1 0 is '1'

Columns 15, 14, 13, 12, 11, 10, 9, 8

Finally – Add Parity Across Entire Data Set

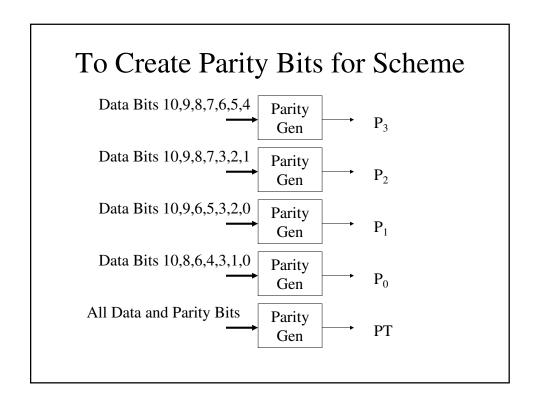
Columns 15, 14, 13, 12, 11, 10, 9, 8, 7, 6, 5, 4, 3, 2, 1

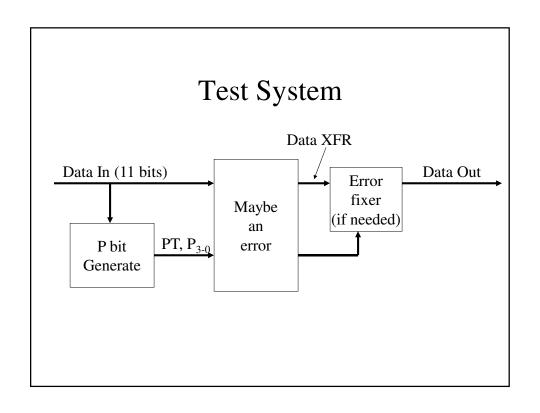
SECDED Coding Scheme

PT – parity across whole word

Pk – parity bits on set basis (any Pk nonzero = error)

PT	PK	Situation
Good Bad	Bad Good	No error – accept data as passed Two bits in error PT in error Single bit error – corrected (Error in column $P_3P_2P_1P_0$)





Start point: set up correct values in parity bits, data bits

For demonstration: make one bit wrong

Check Parity zero for correctness

Check Parity one for correctness

Check Parity two for correctness

Check Parity three for correctness

