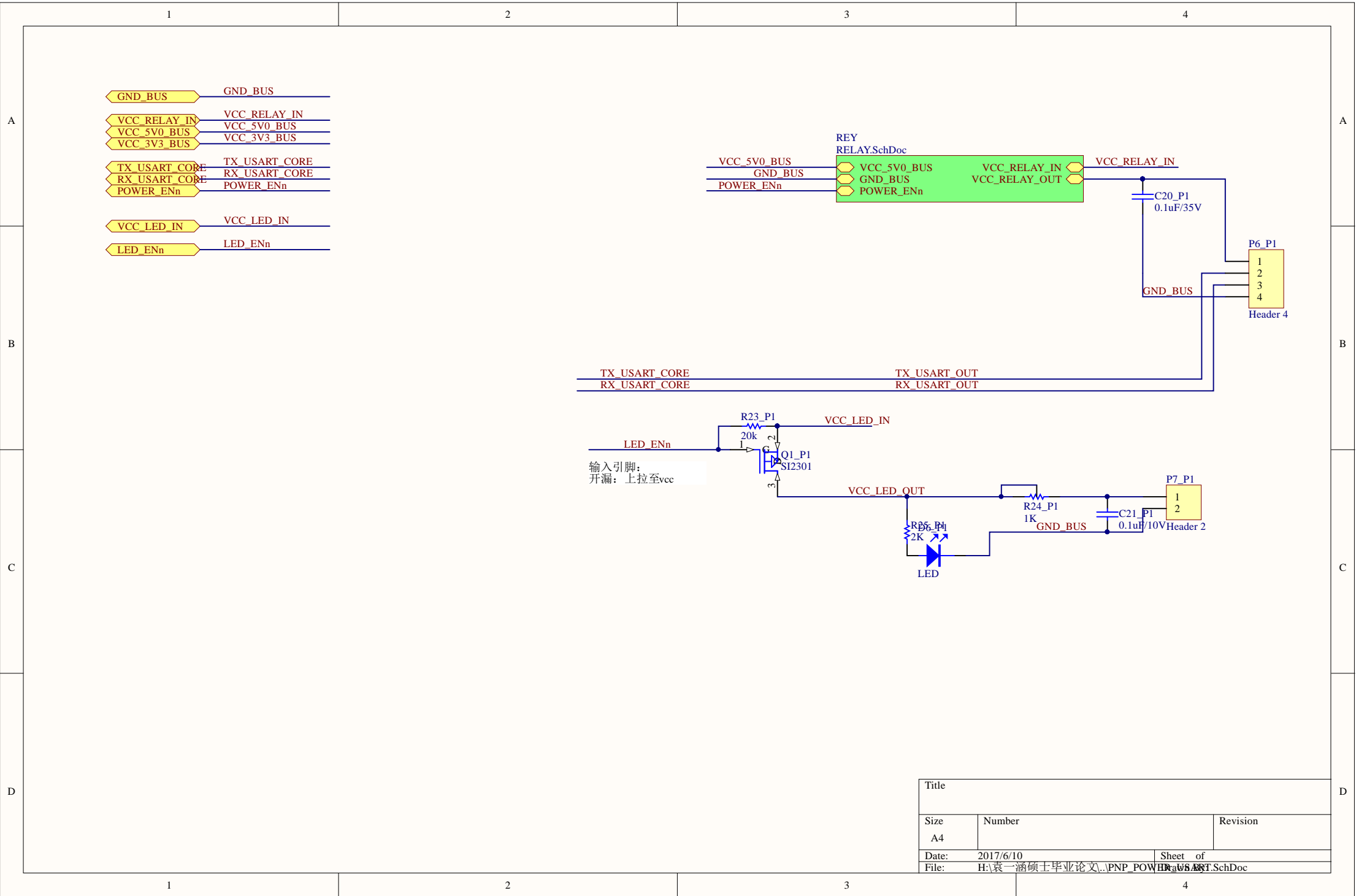
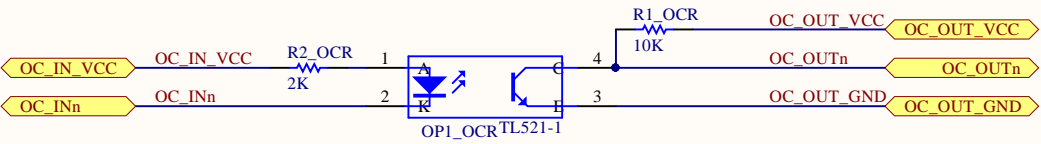


输入引脚：
开漏：上拉至vcc 5v

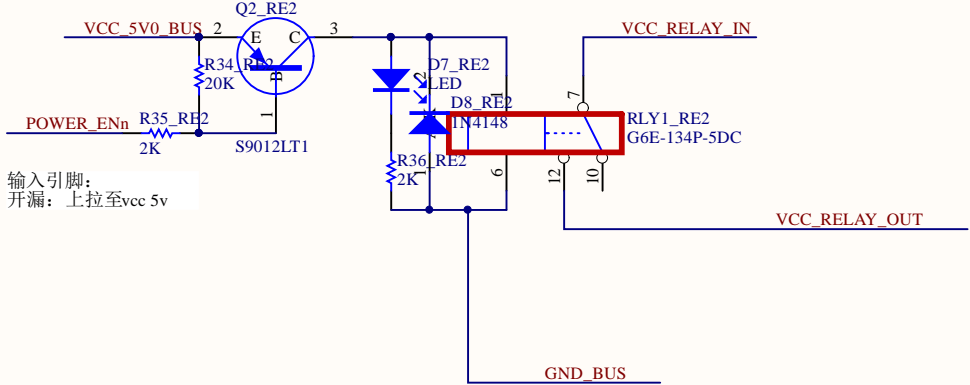
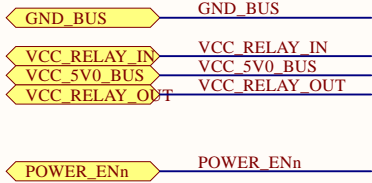
Title		
Size A4	Number	Revision
Date: 2017/6/10	Sheet of	
File: H:\袁一涵硕士毕业论文\powerHARD\BOM\BOMSchDoc	Drawn By	



Title		
Size	Number	Revision
A4		
Date:	2017/6/10	Sheet of
File:	H:\袁一涵硕士毕业论文\PNP_POWER_V5.ABT.SchDoc	

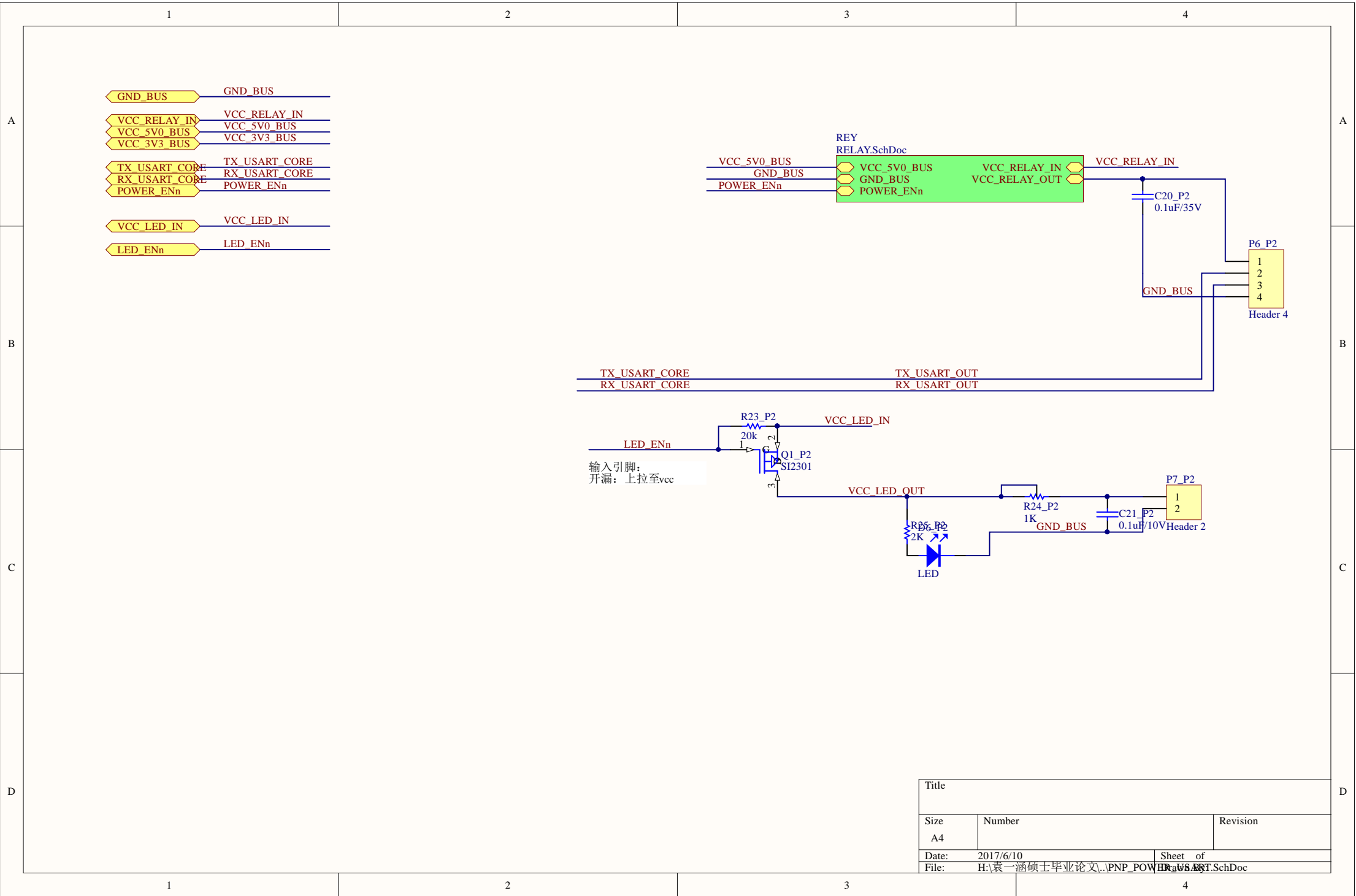


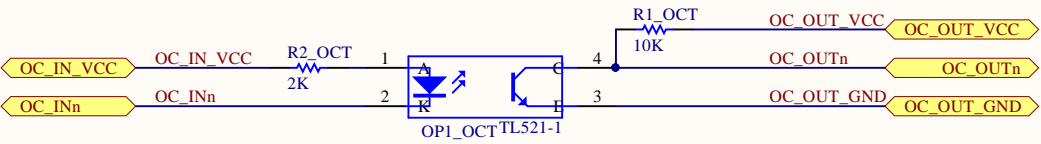
Title		
Size A4	Number	Revision
Date:	2017/6/10	Sheet of
File:	H:\袁一涵硕士毕业论文\powerHARD\Doc\SolidDoc	



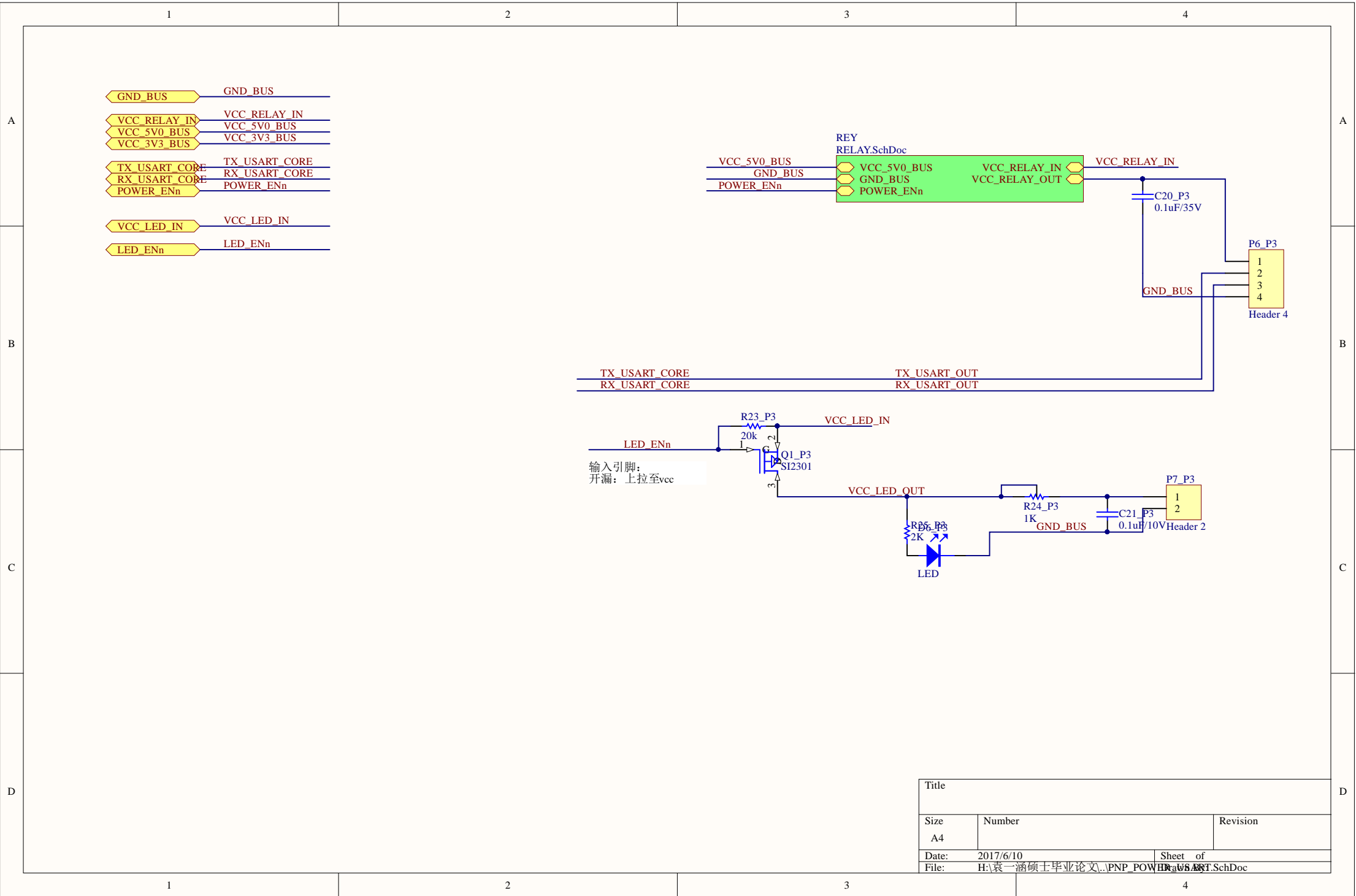
输入引脚：
开漏：上拉至vcc 5v

Title		
Size A4	Number	Revision
Date: 2017/6/10	Sheet of	
File: H:\袁一涵硕士毕业论文\powerHARD\RELAY\BschDoc	Drawn By	

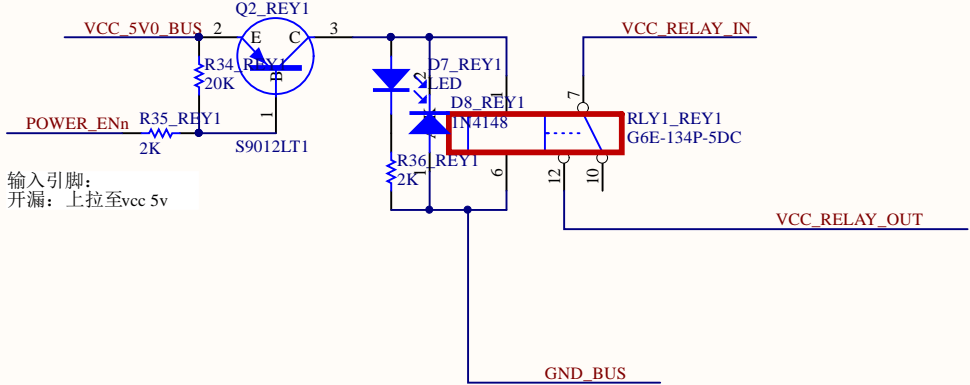
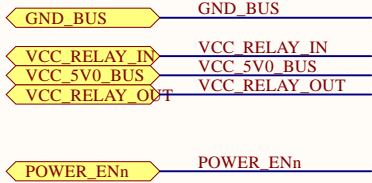




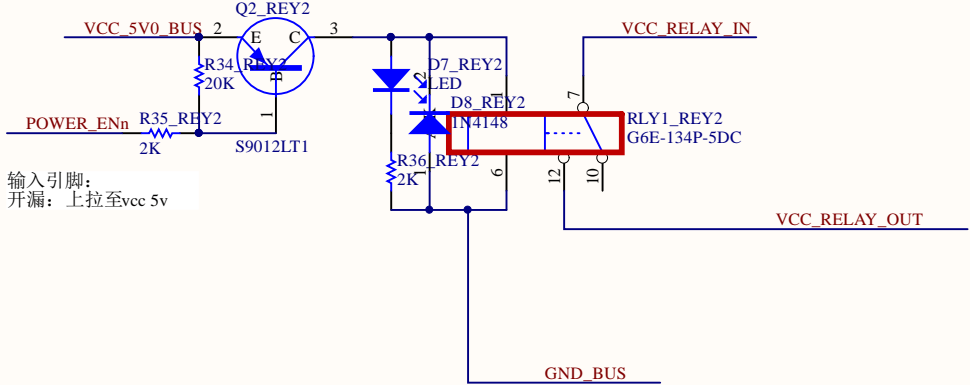
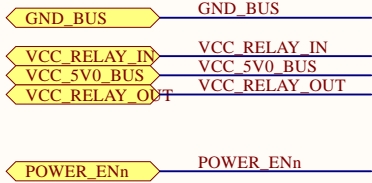
Title			
Size	Number		Revision
A4			
Date:	2017/6/10		Sheet of
File:	H:\袁一涵硕士毕业论文\powerHARD\Doc\SolidDoc		



Title		
Size	Number	Revision
A4		
Date:	2017/6/10	Sheet of
File:	H:\袁一涵硕士毕业论文\PNP_POWER_SWITCH\Draw5.A4T.SchDoc	

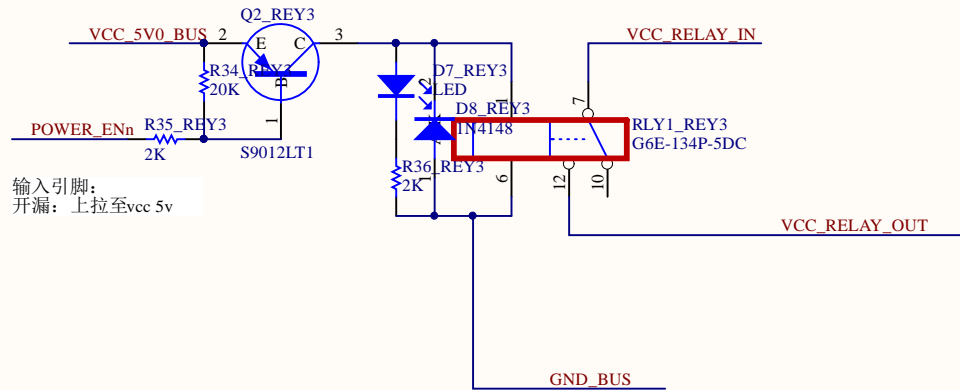
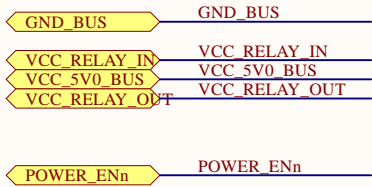


Title		
Size A4	Number	Revision
Date: 2017/6/10	Sheet of	
File: H:\袁一涵硕士毕业论文\powerHARD\RELAY\BschDoc	Drawn By	

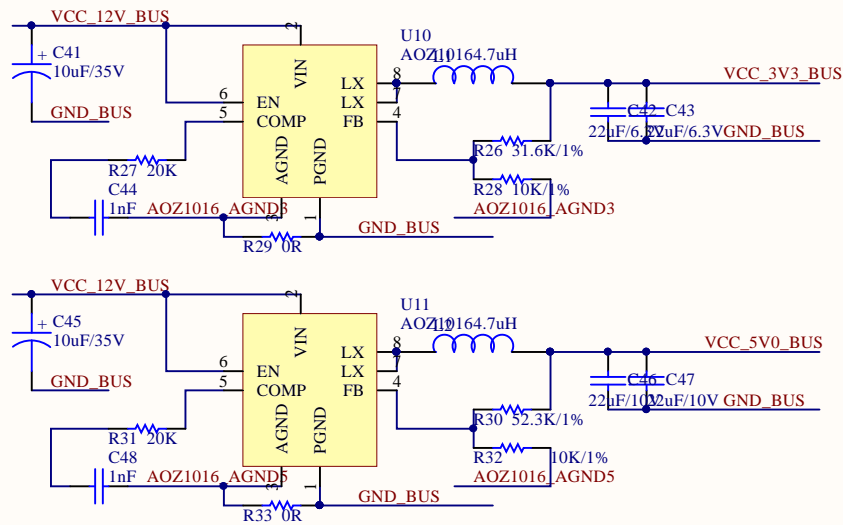
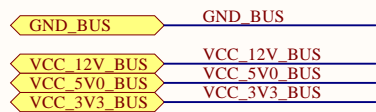


输入引脚：
开漏：上拉至vcc 5v

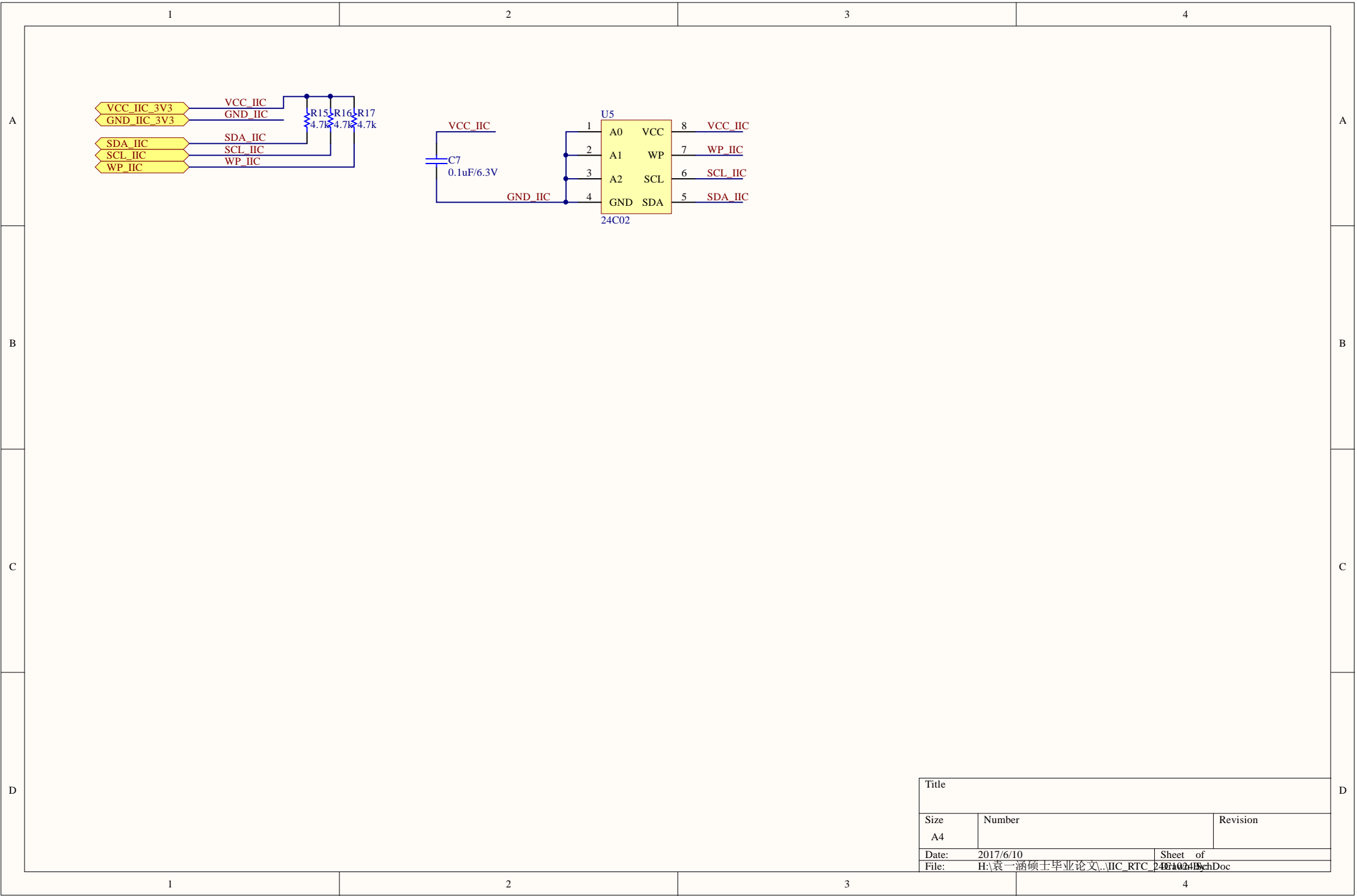
Title		
Size	Number	Revision
A4		
Date:	2017/6/10	Sheet of
File:	H:\袁一涵硕士毕业论文\powerHARD\RELAY\BschDoc	

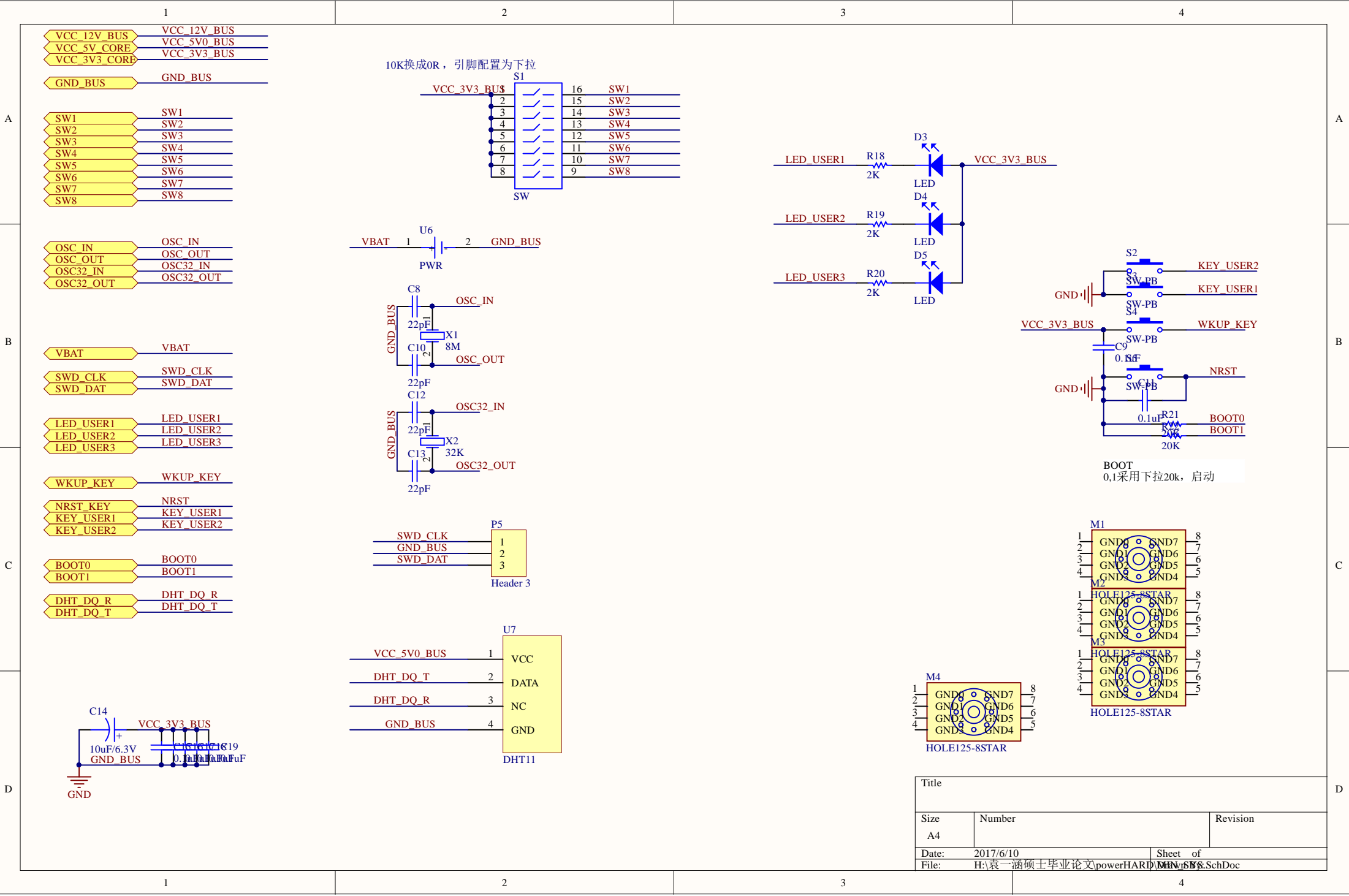


Title		
Size A4	Number	Revision
Date: 2017/6/10	Sheet of	
File: H:\袁一涵硕士毕业论文\powerHARD\RELAY\BschDoc	Drawn By	



Title		
Size	Number	Revision
A4		
Date:	2017/6/10	Sheet of
File:	H:\袁一涵硕士毕业论文\powerHARDWARE\DA106.Bsp.Doc	4





LED_USER1

LED_USER2

LED_USER3

LED_USER1

LED_USER2

LED_USER3

WKUP_KEY

NRST_KEY

KEY_USER1

KEY_USER2

WKUP_KEY

NRST

KEY_USER1

KEY_USER2

BOOT0

BOOT1

BOOT0

BOOT1

DHT_DQ_R

DHT_DQ_T

DHT_DQ_R

DHT_DQ_T

C14

10uF/6.3V

GND_BUS

VCC_3V3_BUS

C15

10uF

C16

10uF

C17

10uF

C18

10uF

C19

10uF

10k换成0R，引脚配置为下拉

VCC_3V3_BUS

S1

2

3

4

5

6

7

8

16

15

14

13

12

11

10

9

SW1

SW2

SW3

SW4

SW5

SW6

SW7

SW8

VBAT

U6

1

2

GND_BUS

PWR

C8

22pF

GND_BUS

OSC_IN

C10

8M

2

OSC_OUT

C12

22pF

GND_BUS

OSC32_IN

C13

32K

3

OSC32_OUT

22pF

SWD_CLK

GND_BUS

SWD_DAT

P5

1

2

3

Header 3

VCC_5V0_BUS

DHT_DQ_T

DHT_DQ_R

GND_BUS

U7

1

2

3

4

VCC

DATA

NC

GND

DHT11

LED_USER1

R18

2K

D3

LED

VCC_3V3_BUS

LED_USER2

R19

2K

D4

LED

LED_USER3

R20

2K

D5

LED

S2

SW-PB

KEY_USER2

SW-PB

KEY_USER1

VCC_3V3_BUS

C9

0.1uF

SW-PB

NRST

SW-PB

R21

20K

BOOT0

BOOT1

BOOT

0.1采用下拉20k，启动

M1

1

2

3

4

GND0

GND1

GND2

GND3

GND7

GND6

GND5

GND4

M2

1

2

3

4

GND0

GND1

GND2

GND3

GND7

GND6

GND5

GND4

M3

1

2

3

4

GND0

GND1

GND2

GND3

GND7

GND6

GND5

GND4

M4

1

2

3

4

GND0

GND1

GND2

GND3

GND7

GND6

GND5

GND4

HOLE125-8STAR

HOLE125-8STAR

HOLE125-8STAR

HOLE125-8STAR

Title

Size

A4

Number

Revision

Date:

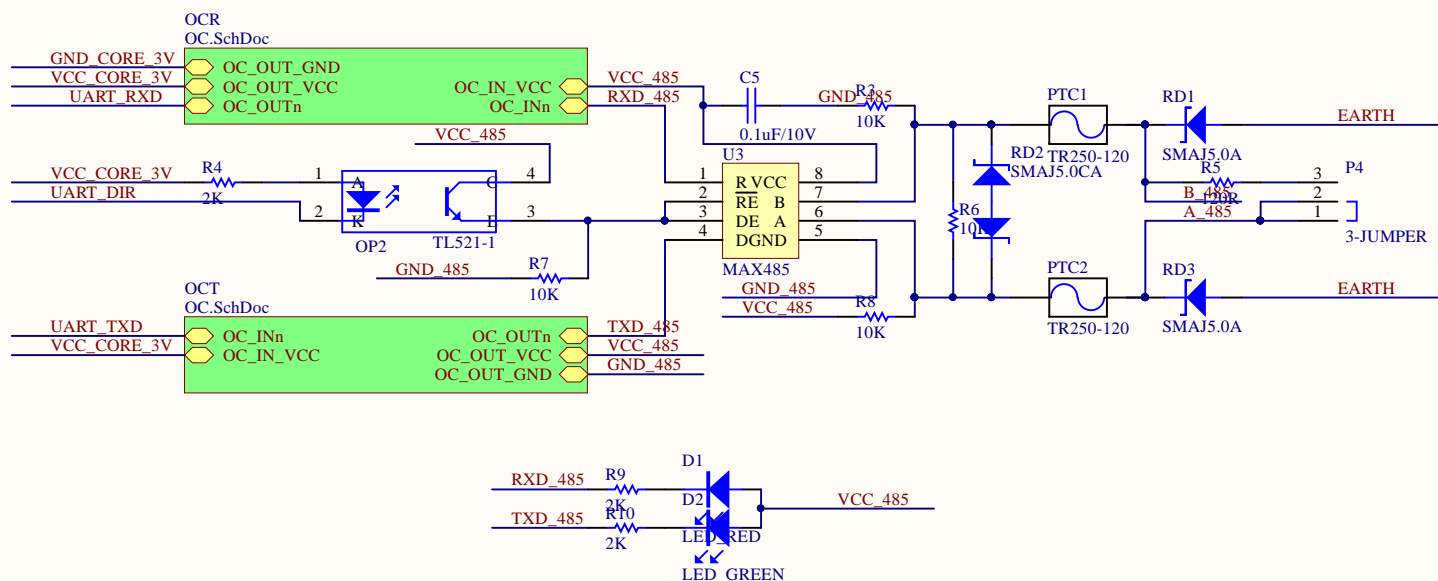
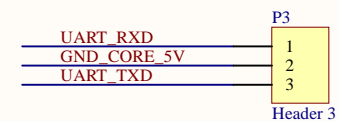
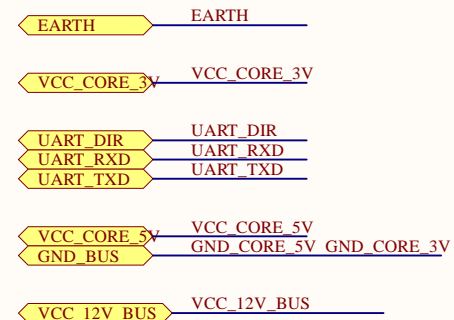
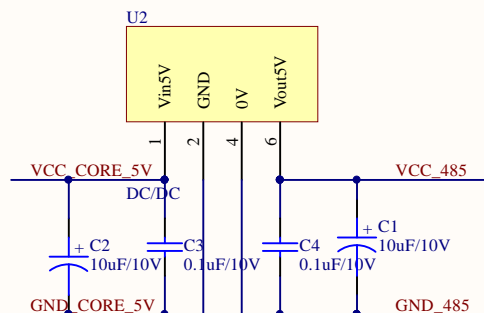
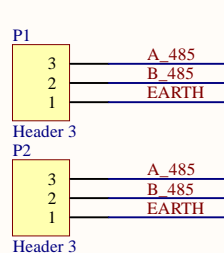
2017/6/10

Sheet of

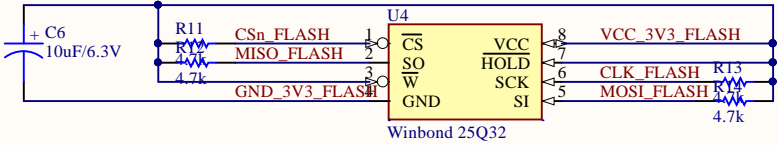
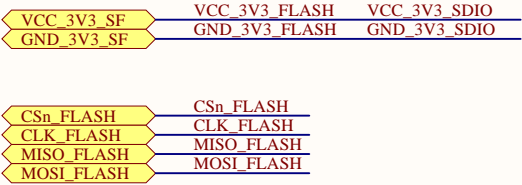
File:

H:\袁一涵硕士毕业论文\powerHARDWARE\PCB\SchDoc





Title		
Size	Number	Revision
A4		
Date:	2017/6/10	Sheet of
File:	H:\袁一涵硕士毕业论文\powerHARDWARE\BOM.BomDoc	袁一涵



FLASH:
其余推免上拉

Title		
Size	Number	Revision
A4		
Date:	2017/6/10	Sheet of
File:	H:\袁一涵硕士毕业论文\powerHARDWARE\MC8080.SchDoc	

VBAT	1	VBAT	VDD	64	3V3_VCC
WP_IIC	2	PC13/ANT1	VSS	63	GND_BUS
OSC32_IN	3	PC14/OSC32	PB9/TIM4_CH4/SDIO_D5	62	KEY_USER1
OSC32_OUT	4	PC15/OSC32	PB8/TIM4_CH3/SDIO_D4	61	KEY_USER2
OSC_IN	5	PD0/OSCIN	BOOT0	60	BOOT0
OSC_OUT	6	PD1/OSCOU	PB7/I2C1_SDA/TIM4_CH2	59	SDA_IIC
NRST_KEY	7	NRST	PB6/I2C1_SCL/TIM4_CH1	58	SCL_IIC
	8	PC0/ADC10	PB5/I2C1_SMB4/SPI3_MOSI/I2S3_SD	57	LED_USER3
SW7	9	PC1/ADC11	PB4/JNTRST/SPI3_MISO	56	LED_USER2
SW6	10	PC2/ADC12	PB3/JTDO/SPI3_SCK/I2S3_CK	55	
SW5	11	PC3/ADC13	PD2/TIM3_ETR/U5_RX/SDIO_CMD	54	DHT_DQ_R
GND_BUS	12	VSSA	PC12/U5_TX/SDIO_CK	53	DHT_DQ_T
3V3_VCC	13	VDDA	PC11/U4_RX/SDIO_D3	52	RXD1
WKUP_KEY	14	PA0/WKUP/ADC0/TIM2_CH1_ETR/TIM5_CH1/TIM8_ETR/U4_TX/SDIO_D2	51	TXD1	
SW4	15	PA1/ADC1/TIM2_CH2/TIM5_CH2	50	SW8	
TXD3	16	PA2/U2_TX/ADC2/TIM2_CH3/TIM5_CH3	49	SWD_CLK	
RXD3	17	PA3/U2_RX/ADC3/TIM2_CH4/TIM5_CH4	48	3V3_VCC	
GND_BUS	18	VSS	VDD	47	GND_BUS
3V3_VCC	19	VSS	VSS	46	SWD_DAT
CSn_FLASH	20	VDD	PA13/JTMS/SDWIO	45	REY_ENn2
CLK_FLASH	21	PA4/SPI1_NSS/ADC4/DAC1	PA12/CAN_TX/USBDP/TIM1_ETR	44	REY_ENn1
MISO_FLASH	22	PA5/SPI1_SCK/ADC5/DAC2	PA11/CAN_RX/USBDP/TIM1_CH4	43	UART_RXD
MOSI_FLASH	23	PA6/SPI1_MISO/ADC6/TIM3_CH1/TIM8_BKIN	PA10/U1_RX/TIM1_CH3	42	UART_TXD
	24	PA7/SPI1_MOSI/ADC7/TIM3_CH2/TIM8_CH1N	PA9/U1_TX/TIM1_CH2	41	UART_DIR
SW3	25	PC4/ADC14	PA8/TIM1_CH1/MCO	40	LED_USER1
SW2	26	PC5/ADC15	PC9/TIM8_CH4/SDIO_D1	39	
SW1	27	PB0/ADC8/TIM3_CH3/TIM8_CH2N	PC8/TIM8_CH3/SDIO_D0	38	POWER_ENn1
BOOT1	28	PB1/ADC9/TIM3_CH4/TIM8_CH3N	PC7/I2S3_MCK/TIM8_CH2/SDIO_D7	37	POWER_ENn2
TXD2	29	PB2/BOOT1	PC6/I2S2_MCK/TIM8_CH1/SDIO_D6	36	POWER_ENn3
RXD2	30	PB10/I2C2_SCL/U3_TX	PB15/SPI2_MOSI/I2S2_SD/TIM1_CH3N	35	LED_ENn1
GND_BUS	31	PB11/I2C2_SDA/U3_RX	PB14/SPI2_MISO/TIM1_CH2N	34	LED_ENn2
3V3_VCC	32	VSS	PB13/SPI2_SCK/I2S2_CK/TIM1_CH1N	33	LED_ENn3
		VDD	PB12/SPI2_NSS/I2S2_WS/I2C2_SMB4/TIM1_BKIN		

STM32F103RDT6

POWER

POWER.SchDoc

TXD1	TXD1	POWER_ENn1	POWER_ENn1
TXD2	TXD2	POWER_ENn2	POWER_ENn2
TXD3	TXD3	POWER_ENn3	POWER_ENn3
RXD1	RXD1	LED_ENn1	LED_ENn1
RXD2	RXD2	LED_ENn2	LED_ENn2
RXD3	RXD3	LED_ENn3	LED_ENn3
GND_BUS	GND_BUS	VCC_3V3_BUS	3V3_VCC
VCC_12V_BUS	VCC_12V_BUS	VCC_5V0_BUS	5V0_VCC
REY_ENn2	REY_ENn2	EARTH	EARTH
REY_ENn1	REY_ENn1		

MIN_SYS.SchDoc

LED_USER1	LED_USER1	SW1	SW1
LED_USER2	LED_USER2	SW2	SW2
LED_USER3	LED_USER3	SW3	SW3
NRST_KEY	NRST_KEY	SW4	SW4
KEY_USER1	KEY_USER1	SW5	SW5
KEY_USER2	KEY_USER2	SW6	SW6
WKUP_KEY	WKUP_KEY	SW7	SW7
OSC_IN	OSC_IN	SW8	SW8
OSC_OUT	OSC_OUT	VCC_5V_CORE	5V0_VCC
OSC32_IN	OSC32_IN	VCC_3V3_CORE	3V3_VCC
OSC32_OUT	OSC32_OUT	VCC_12V_BUS	VCC_12V_BUS
SWD_CLK	SWD_CLK	VBAT	VBAT
SWD_DAT	SWD_DAT	BOOT0	BOOT0
DHT_DQ_R	DHT_DQ_R	BOOT1	BOOT1
		GND_BUS	GND_BUS
		DHT_DQ_T	DHT_DQ_T

RS485

RS485.SchDoc

UART_RXD	UART_RXD	VCC_CORE_3V	3V3_VCC
UART_TXD	UART_TXD	GND_BUS	GND_BUS
UART_DIR	UART_DIR	VCC_CORE_5V	5V0_VCC
VCC_12V_BUS	VCC_12V_BUS	EARTH	EARTH

RTC

IIC_RTC_24C1024.SchDoc

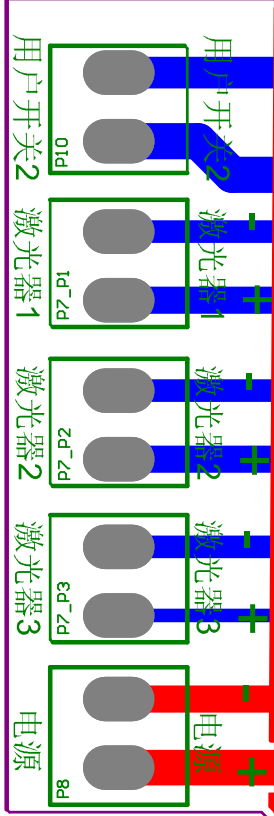
WP_IIC	WP_IIC	VCC_IIC_3V3	3V3_VCC
SDA_IIC	SDA_IIC	GND_IIC_3V3	GND_BUS
SCL_IIC	SCL_IIC		

TF

TF_M25P80.SchDoc

MISO_FLASH	MISO_FLASH	CSn_FLASH	CSn_FLASH
MOSI_FLASH	MOSI_FLASH	VCC_3V3_SF	3V3_VCC
CLK_FLASH	CLK_FLASH	GND_3V3_SF	GND_BUS

Title		
Size	Number	Revision
A4		
Date:	2017/6/10	Sheet of
File:	H:\袁一涵硕士毕业论文\power\HARDWARE\PCB\PCB.DOC	



电源不可反接！

