Current 673 - cachedRegsAssignmentKernel (5242880, 4, 1) **Time:** 1.70 second **Cycles:** 2,468,163,719 **Regs:** 49 **GPU:** NVIDIA GeForce GTX 1060 **SM Frequency:** 1.45 Ghz **CC:** 6.1 **Process:** [14884] k-means.exe

View: SASS

_Z26cachedRegsAssignmentKernelIfj15AoSLayoutPolicyILi1EES1_Li4ELi4ELi8EEvPKT_S4_PT0_S5_S5_S5_   Live Registers

#	Address	Source	Live Registers	Sampling Data (All)	Instructions Executed	Predicated-On Thread Instructions Executed	Memory Address Space	Memory Access Operation	Memory Access Size	Memory L1 Tran
536		**_Z26cachedRegsAssignme**		0						
537	005e8b48	MOV R1, c[0x0][0x20]	3	447	655,360	20,971,520				
538	005e8b50	MOV R13, c[0x0][0x8]	4	64	655,360	20,971,520				
539	005e8b58	S2R R0, SR_TID.Y	5	0	655,360	20,971,520				
540	005e8b68	MOV R3, c[0x0][0xc]	6	9	655,360	20,971,520				
541	005e8b70	SSY 0x5e8fd8	6	0	655,360	20,971,520				
542	005e8b78	ISETP.NE.AND P1, PT, RZ, c[	6	40	655,360	20,971,520				
543	005e8b88	S2R R17, SR_TID.X	7	0	655,360	20,971,520				
544	005e8b90	MOV32I R12, 0x4cbebc20	8	11	655,360	20,971,520				
545	005e8b98	S2R R11, SR_CTAID.X	9	0	655,360	20,971,520				
546	005e8ba8	SHL R2, R13, 0x2	10	30	655,360	20,971,520				
547	005e8bb0	XMAD R4, R13, c[0x0] [0xc],	11	11	655,360	20,971,520				
548	005e8bb8	SHL R3, R3, 0x2	11	41	655,360	20,971,520				
549	005e8bc8	XMAD.MRG R7, R13, c[0x0] [0	12	71	655,360	20,971,520				
550	005e8bd0	MOV32I R14, 0x4cbebc20	13	9	655,360	20,971,520				
551	005e8bd8	XMAD R15, R2, c[0x0] [0x158	14	17	655,360	20,971,520				
552	005e8be8	XMAD.MRG R8, R2, c[0x0] [0x	15	7	655,360	20,971,520				
553	005e8bf0	XMAD R9, R3, c[0x0] [0x158]	16	10	655,360	20,971,520				
554	005e8bf8	XMAD.MRG R10, R3, c[0x0] [0	17	26	655,360	20,971,520				
555	005e8c08	XMAD.PSL.CBCC R4, R13.H1, R	17	25	655,360	20,971,520				
556	005e8c10	MOV32I R7, 0x4cbebc20	16	7	655,360	20,971,520				
557	005e8c18	MOV32I R13, 0x4cbebc20	17	10	655,360	20,971,520				
558	005e8c28	XMAD.PSL.CBCC R15, R2.H1, R	17	10	655,360	20,971,520				
559	005e8c30	MOV R8, RZ	17	15	655,360	20,971,520				
560	005e8c38	ISETP.GE.U32.AND P0, PT, R0	17	60	655,360	20,971,520				
561	005e8c48	XMAD.MRG R6, R0, c[0x0] [0x	18	59	655,360	20,971,520				
562	005e8c50	XMAD R5, R0, c[0x0] [0x8],	19	15	655,360	20,971,520				
563	005e8c58	SHL R15, R15, 0x2	19	17	655,360	20,971,520				
564	005e8c68	XMAD.MRG R19, R2, R11.H1, R	20	3	655,360	20,971,520				
565	005e8c70	XMAD R16, R2, R11, R17	21	5	655,360	20,971,520				
566	005e8c78	XMAD.PSL.CBCC R5, R0.H1, R6	20	28	655,360	20,971,520				
567	005e8c88	XMAD.PSL.CBCC R6, R3.H1, R1	20	83	655,360	20,971,520				
568	005e8c90	MOV R9, RZ	19	8	655,360	20,971,520				
569	005e8c98	MOV R10, RZ	20	2	655,360	20,971,520				
570	005e8ca8	MOV R11, RZ	21	10	655,360	20,971,520				
571	005e8cb0	XMAD.PSL.CBCC R16, R2.H1, R	21	5	655,360	20,971,520				
572	005e8cb8	@P0 SYNC	20	0	655,360	0				
573	005e8cc8	XMAD R22, R16, c[0x0] [0x15	20	40	655,360	20,971,520				