

ROADMAP FOR ELECTRICAL ENGINEERING CORE PLACEMENTS

WEEK 3

In this week will focus on the advanced EE courses.

3

WEEK 4

Here, we will go through some programming concepts and cover OOPs.

4

WEEK 5

Finally, we will go through Verilog.

5

WEEK 2

In this week as well, you will focus on revising the second year UG and equivalent PG courses.

2

WEEK 1

Brushing up all the basics of 2nd year UG courses and equivalent PG courses.

1

 **TEXAS
INSTRUMENTS**



Qualcomm



WEEK 1 & 2

Revise the following fundamental courses during this time:

ESC201

Introduction to Electronics

Watch [this](#) playlist which covers all the relevant topics.

EE200

Signal Systems and Networks

Watch [this](#) playlist which covers all the relevant topics.

EE210

Microelectronics - I

Watch [this](#) playlist which covers all the relevant topics.

EE250

Control Systems and Analysis

Watch [this](#) playlist which covers all the relevant topics.

ESO203

Introduction to Electrical Engineering

Watch [this](#) playlist and read [these](#) lecture slides.

WEEK 3

UG Students

EE370

PG Students

**EE698I,
EE604A,
EE610A**

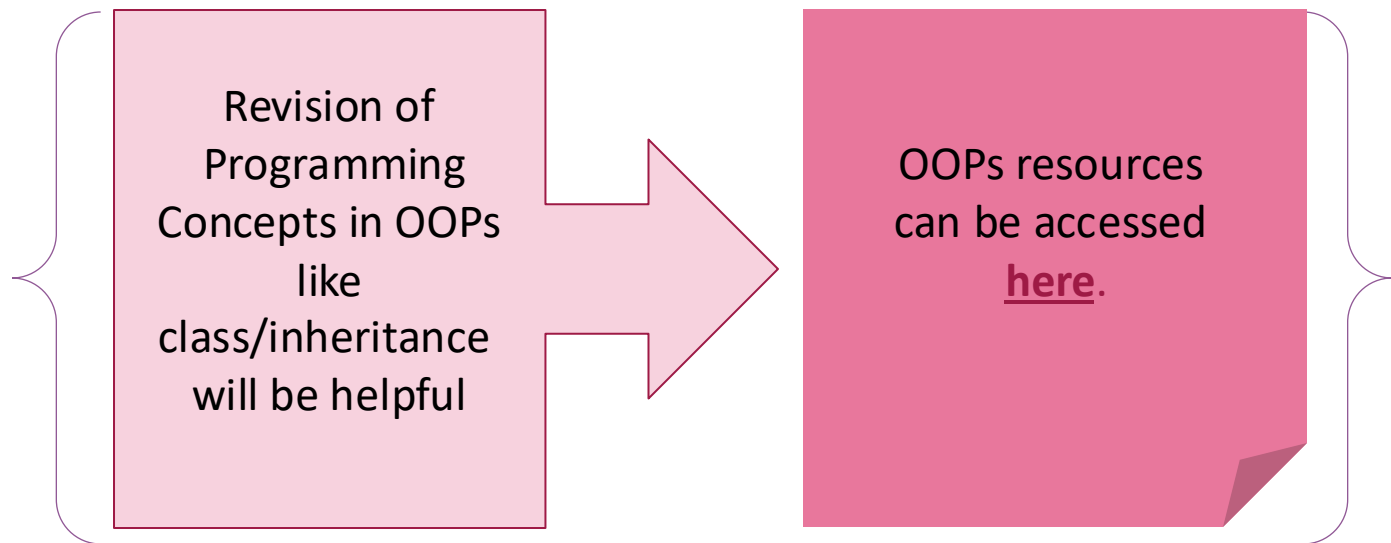
Studying EE370 is essential for a robust digital profile favored by core companies. Reviewing PG courses like EE698I, EE604A, and EE610A through the sixth semester will enhance career readiness significantly.

For advanced Digital IC designs Prof. Janakiraman's lectures can be used.
[Link.](#)

For analog IC design, NPTEL lectures of Prof. Nagendra Krishnapura can be followed.
[Link.](#)

WEEK 4

Revision of ESC101A will be helpful
for Embedded Systems



WEEK 5

Verilog is a Hardware language and it is very important for interviews in core profile. Verilog can be revised/ learnt from [this](#) playlist.



Questions can be practiced from [this](#) repository.